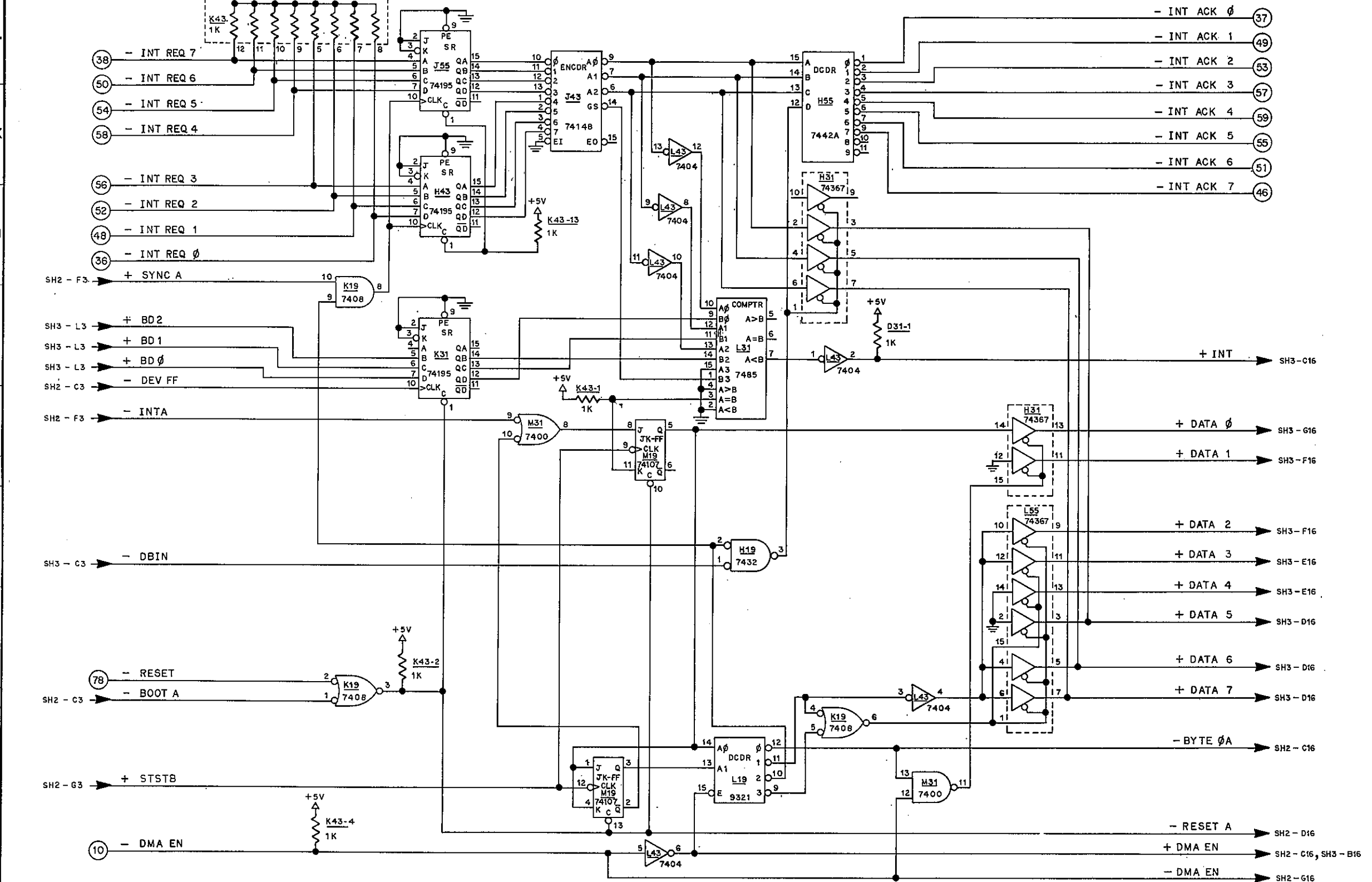
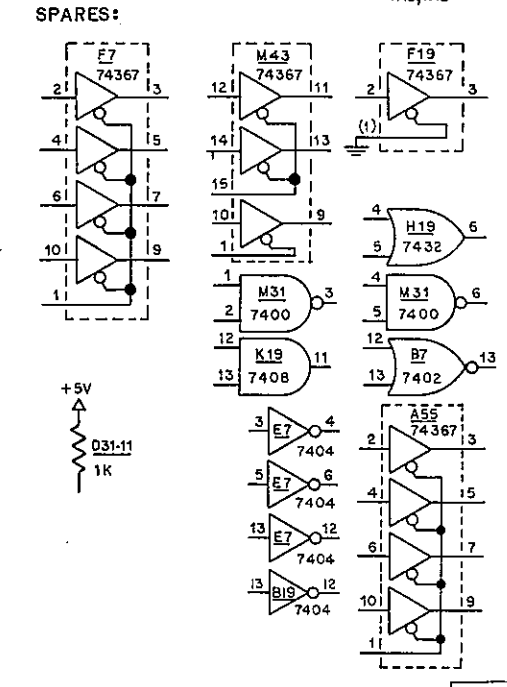
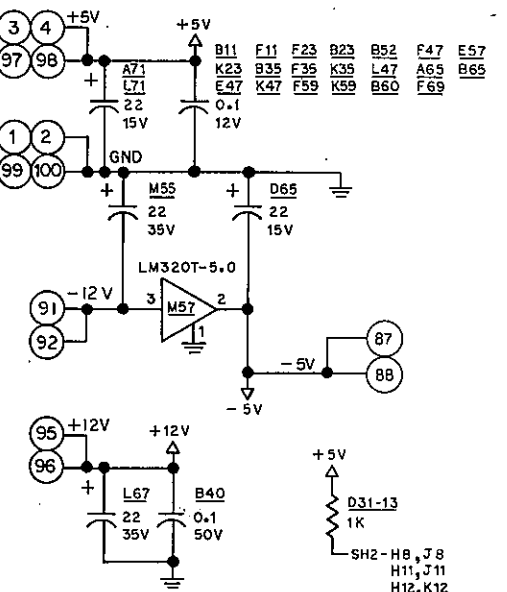


PRODUCT NO. 23404-04

PRODUCT NO. 23404-04 DEVELOPMENT REV. 1



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ±5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
+5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.

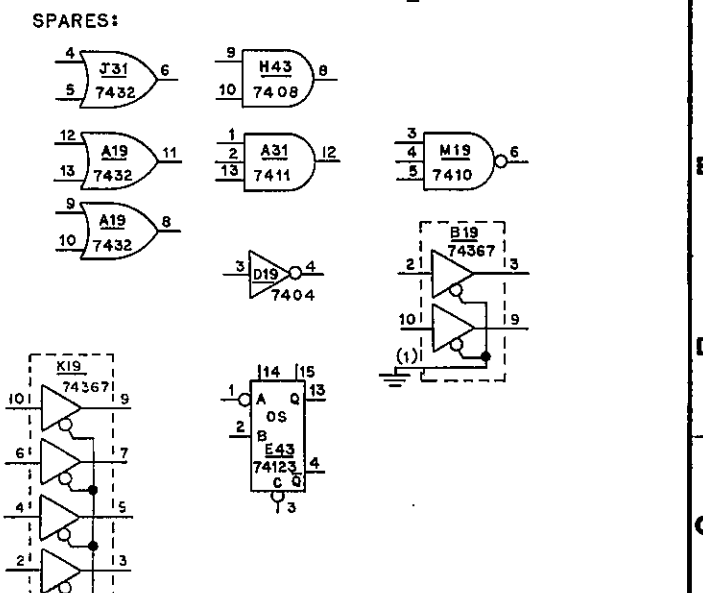
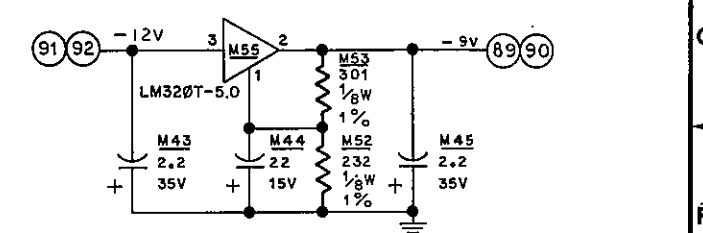
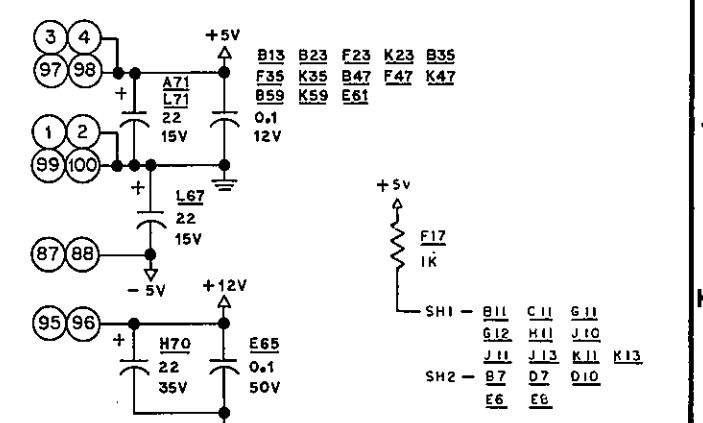
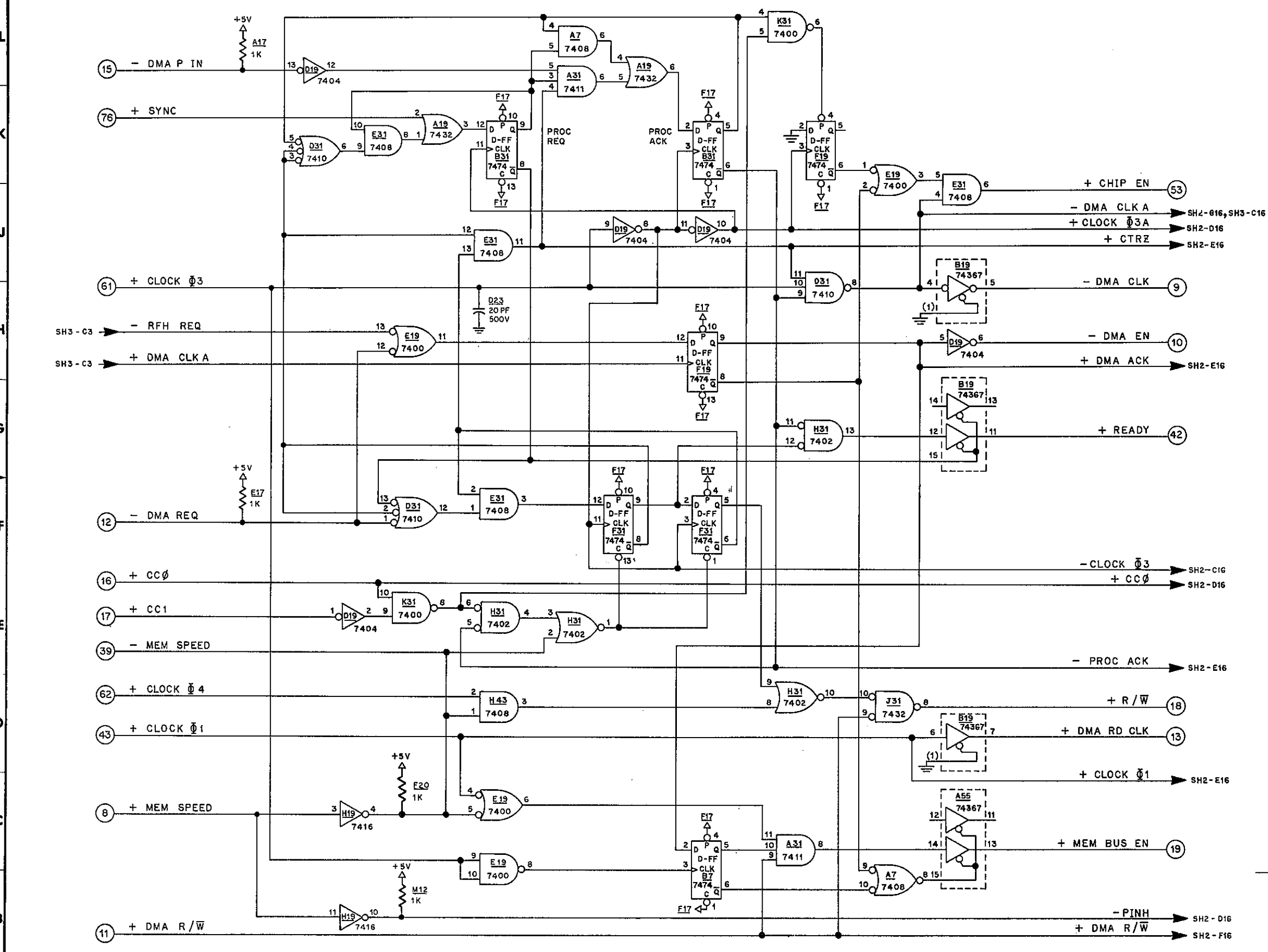


SCHEMATIC DIAGRAM RS

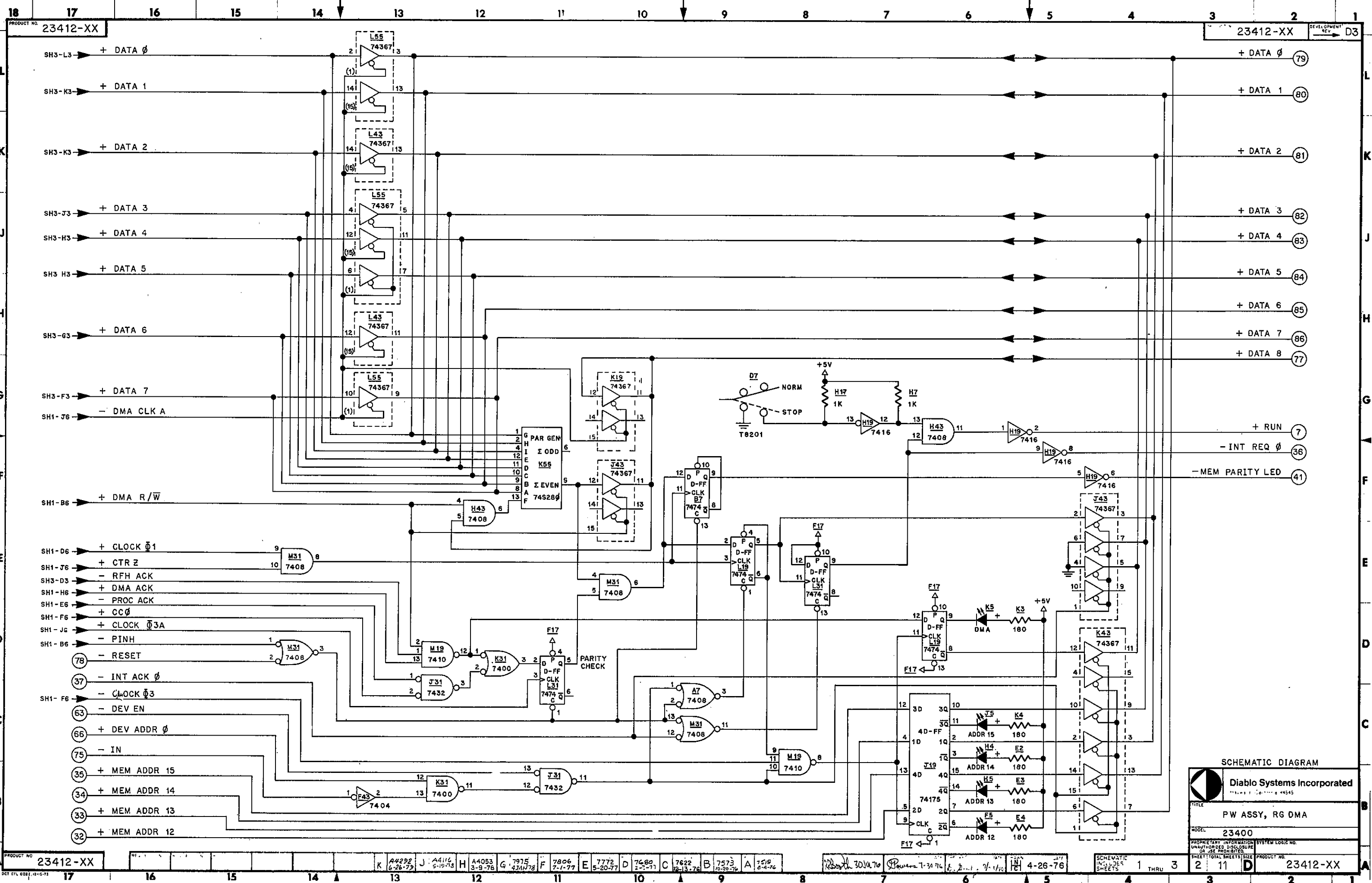
Diablo Systems Incorporated
Hayward, California 94545

TITLE	PW ASSY, PROC 2		
MODEL	3200		
PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED	SYSTEM LOGIC NO.	23404-04	
SHEET TOTAL SHEETS SIZE	1 3 D	23404-04	

NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
 +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. ALL LIGHT EMITTING DIODES ARE MV 5753.



SCHMATIC DIAGRAM PWA
 Diablo Systems Incorporated
 PW ASSY, RG DMA
 MODEL: 23400
 SHEETS: 1 THRU 3



PRODUCT NO. 23412-XX

23412-XX DEVELOPMENT REV D3

SCHEMATIC DIAGRAM

Diablo Systems Incorporated
PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

TITLE PW ASSY, RG DMA

MODEL 23400

SHEET TOTAL SHEETS SIZE PRODUCT NO. 23412-XX

2 11 D

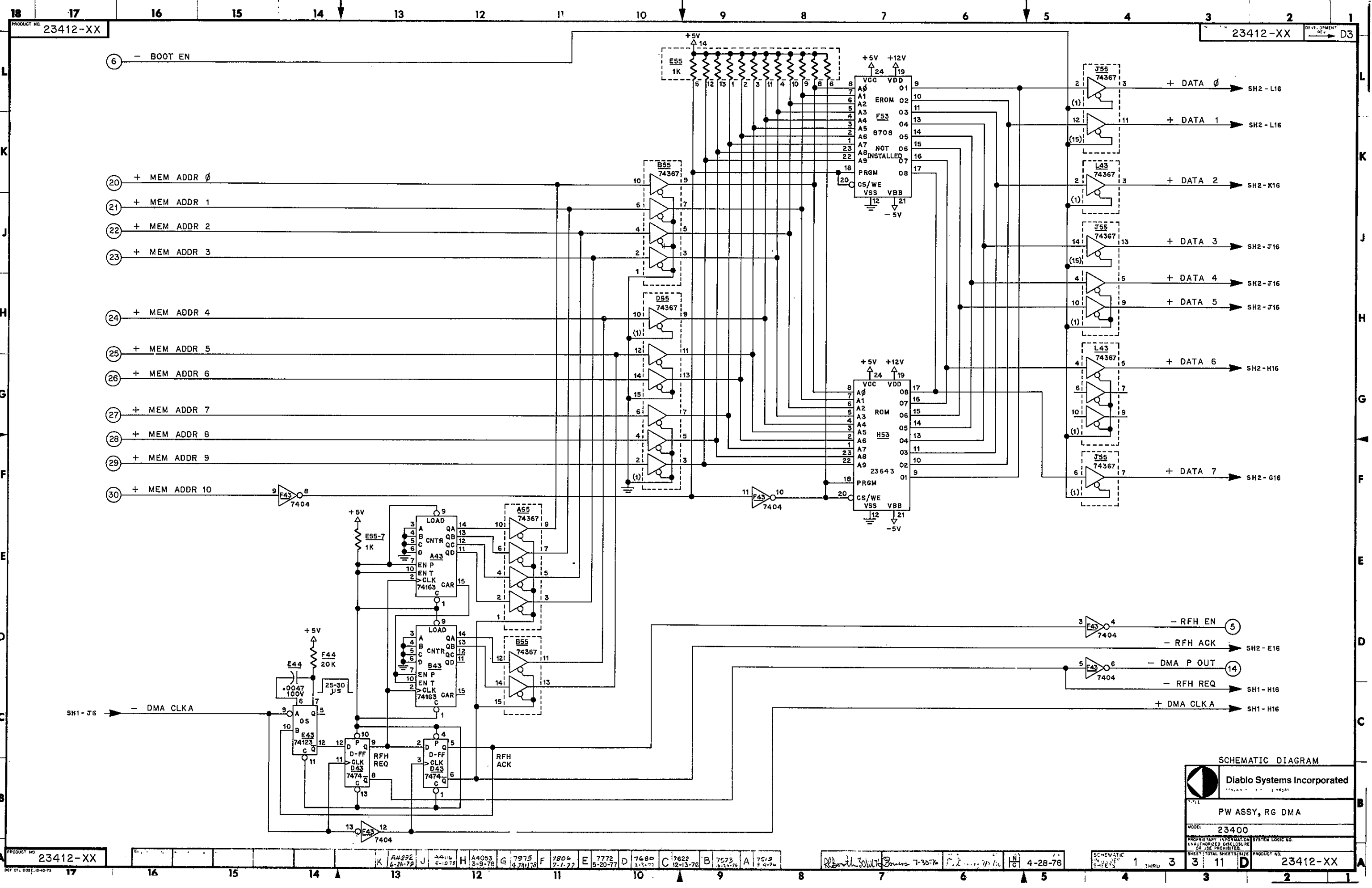
PRODUCT NO. 23412-XX

17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

K A4292 6-26-75 J A4114 5-19-75 H A4053 3-9-76 G 7975 4-11-76 F 7806 7-1-77 E 7772 5-20-77 D 7680 2-1-77 C 7622 12-13-76 B 7573 10-29-76 A 7519 8-4-76

30W70 Powers 7-30-76 4-26-76

SCHEMATIC INCLUDES SHEETS 1 THRU 3



SCHEMATIC DIAGRAM

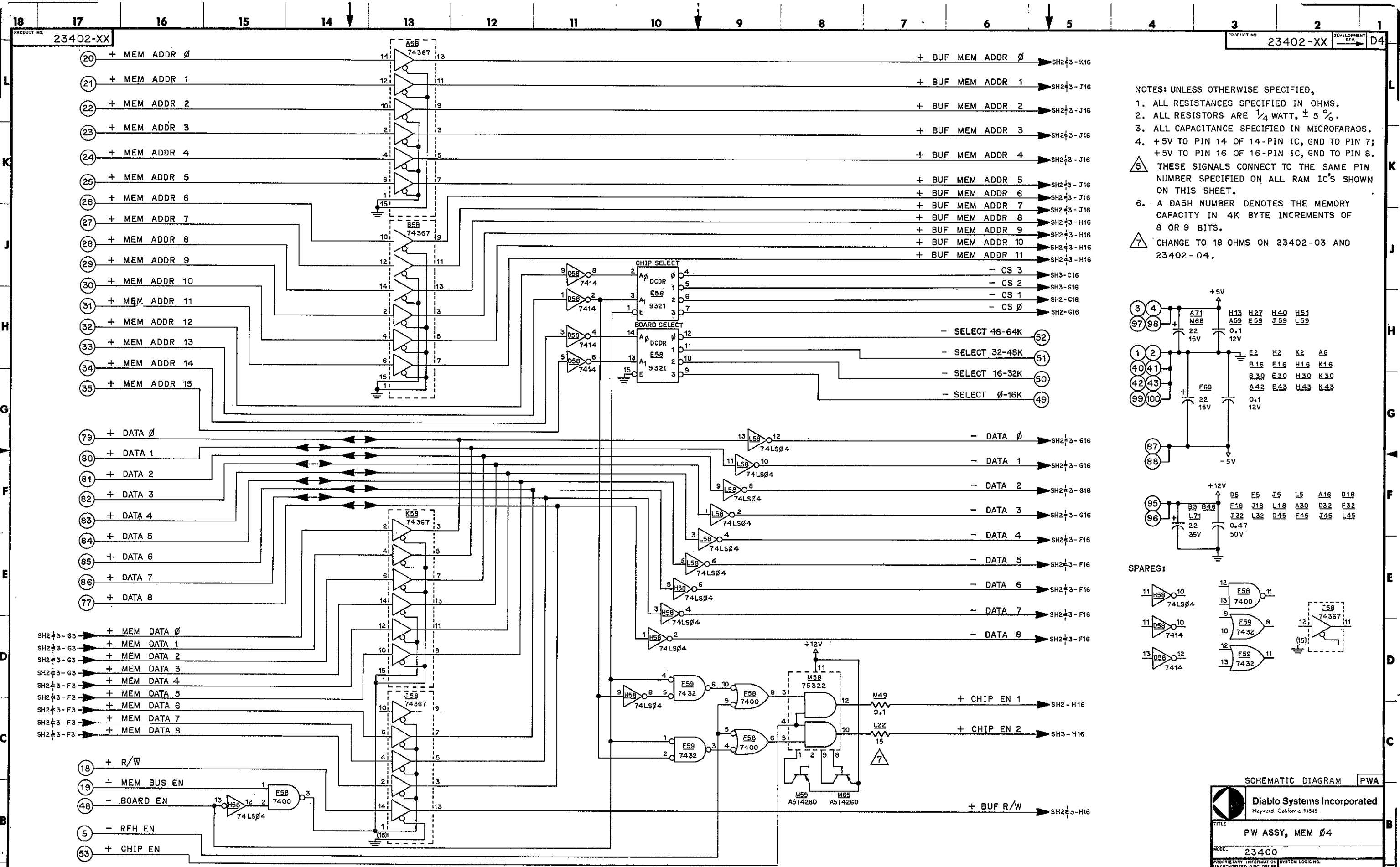
Diablo Systems Incorporated

PW ASSY, RG DMA

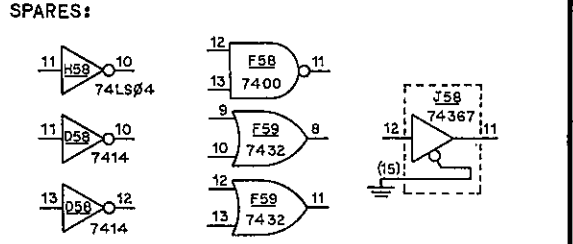
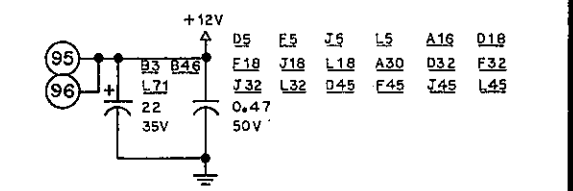
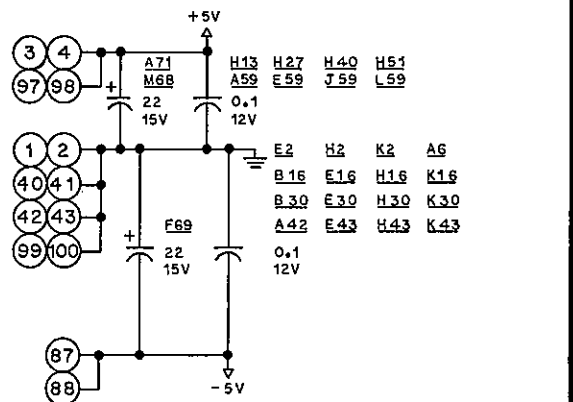
MODEL 23400

PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.

SHEET TOTAL SHEETS/SIZE PRODUCT NO. 23412-XX



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5 %.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7; +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. THESE SIGNALS CONNECT TO THE SAME PIN NUMBER SPECIFIED ON ALL RAM IC'S SHOWN ON THIS SHEET.
 6. A DASH NUMBER DENOTES THE MEMORY CAPACITY IN 4K BYTE INCREMENTS OF 8 OR 9 BITS.
 7. CHANGE TO 18 OHMS ON 23402-03 AND 23402-04.



SCHMATIC DIAGRAM PWA

Diablo Systems Incorporated
Hayward, California 94545

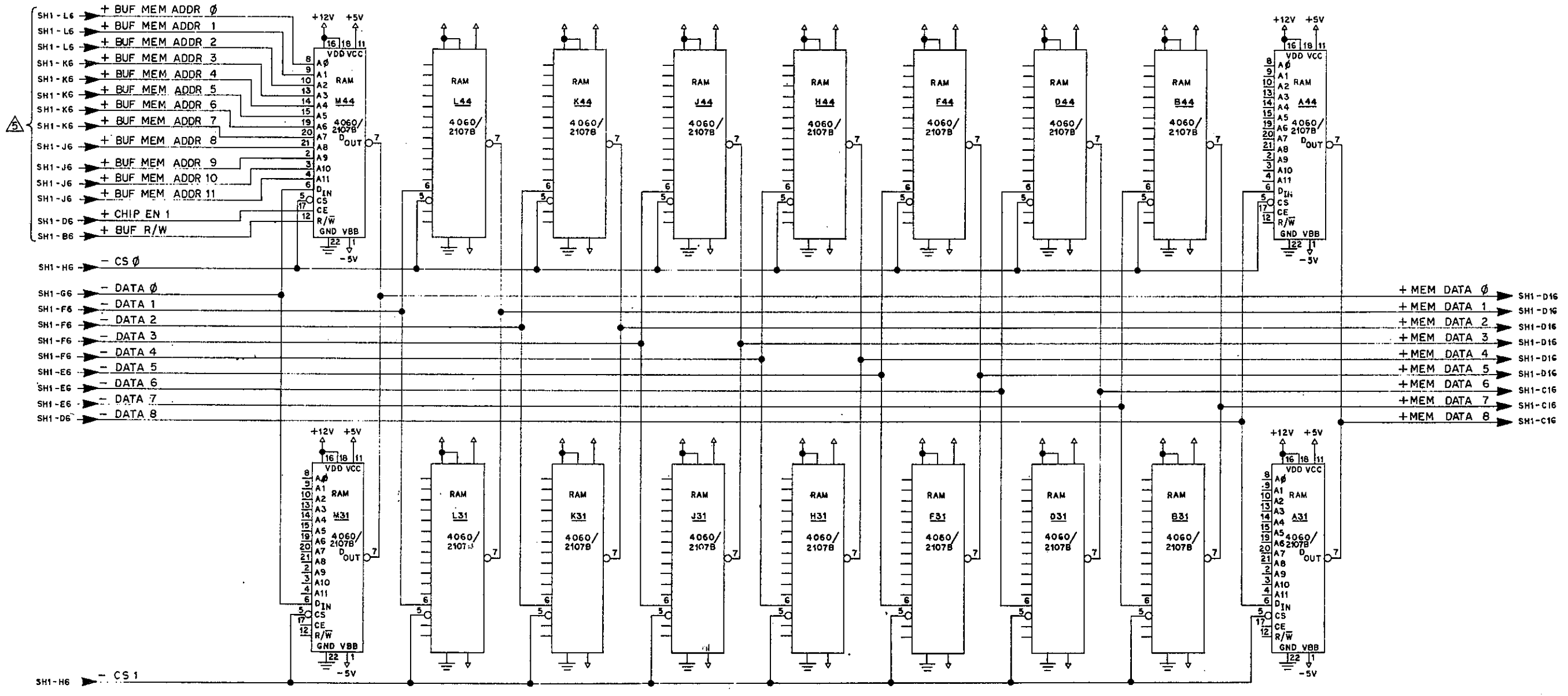
TITLE: PW ASSY, MEM 04

MODEL: 23400

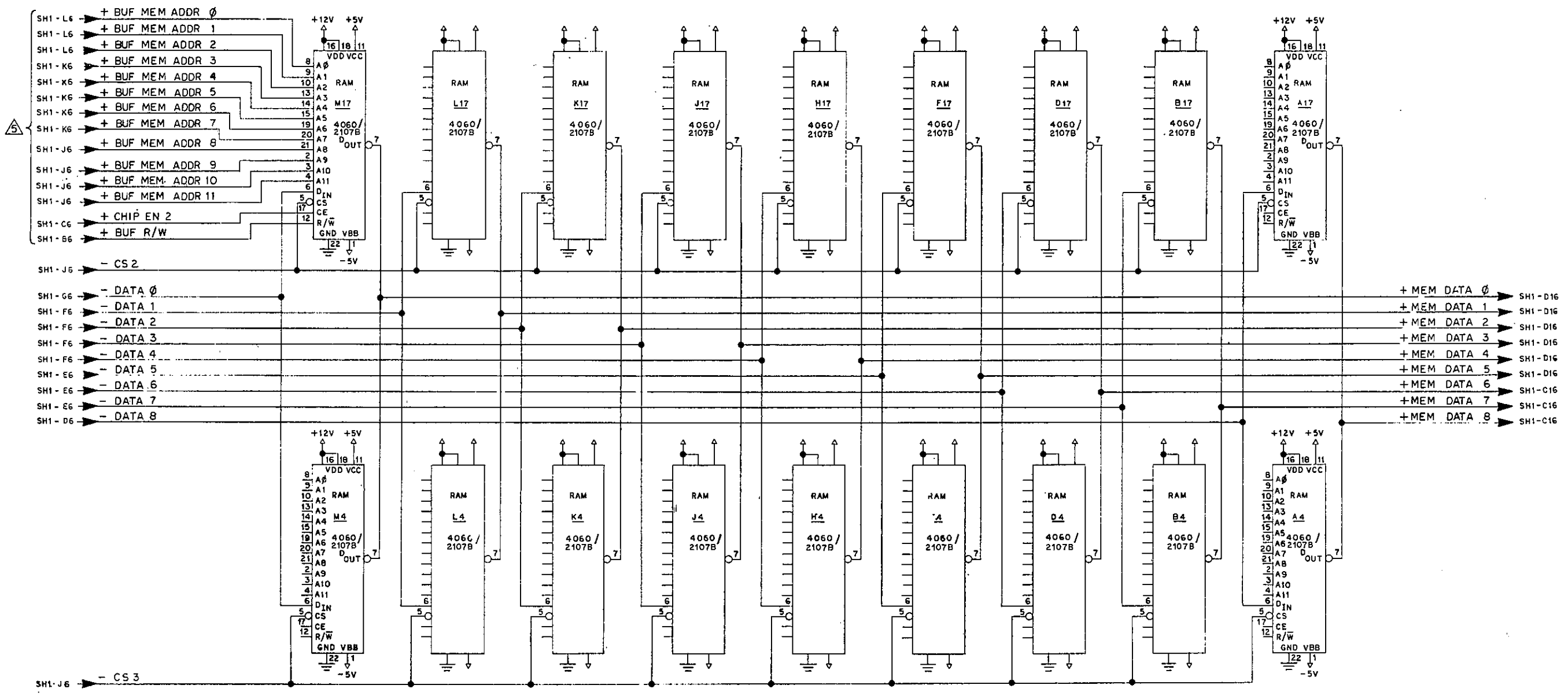
PROPRIETARY INFORMATION SYSTEM LOGIC NO. QUALITY CONTROLLED DISCLOSURE OR USE PROHIBITED.

SHEET TOTAL SHEETS: 14 SHEET NO: 14

PRODUCT NO: 23402-XX

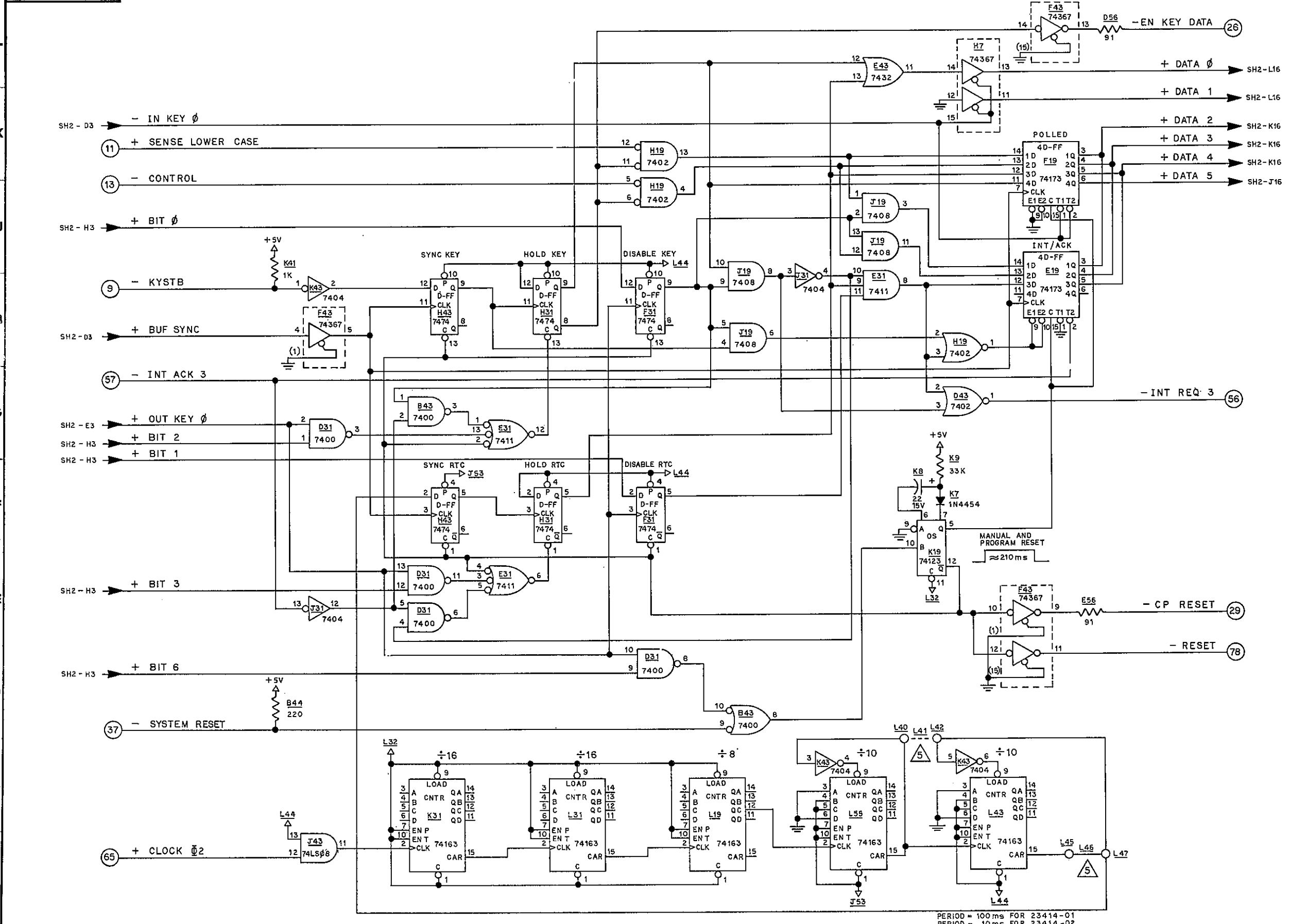


SCHMATIC DIAGRAM
Diablo Systems Incorporated
Hayward, California 94545
TITLE PW ASSY, MEM 04
MODEL 23400
SHEET TOTAL SHEETS SIZE PRODUCT NO. 23402-XX

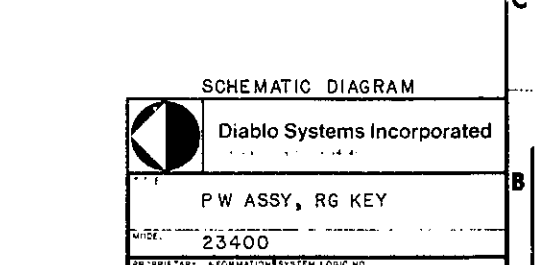
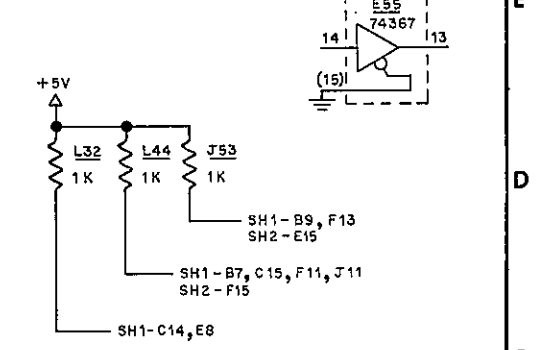
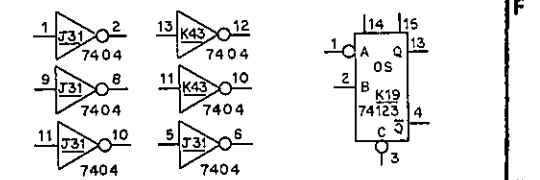
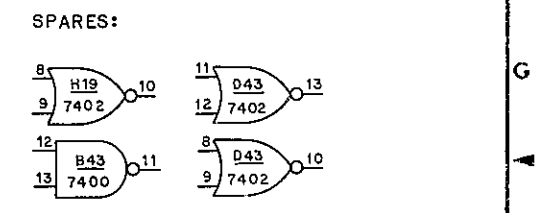
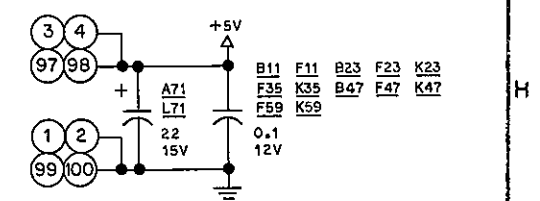


SCHEMATIC DIAGRAM

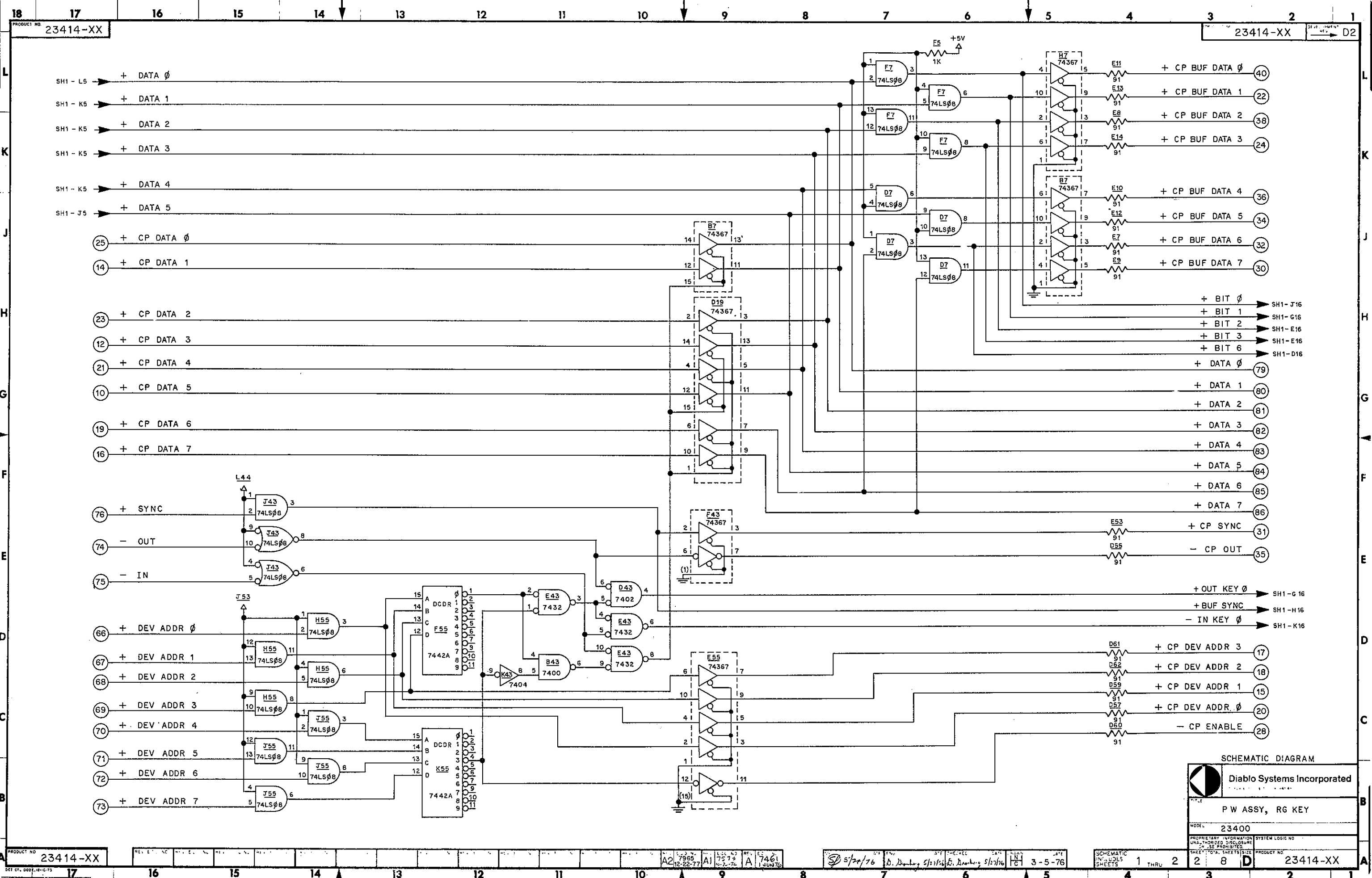
Diablo Systems Incorporated Hayward, California 94545	
TITLE P W ASSY, MEM 04	
MODEL 23400	
<small>PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.</small>	
SHEET TOTAL SHEETS SIZE 1 THRU 3 3 14 D	PRODUCT NO. 23402-XX



NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
 +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. 23414-01 SHOWN.
 ON 23414-02 ASSEMBLY, JUMPER L41
 IS INSTALLED AND THE CONDUCTOR
 L46 IS CUT.



SCHMATIC DIAGRAM
 Diablo Systems Incorporated
 P.W. ASSY, RG KEY
 PART NO. 23400
 1 THRU 2 1 8 D 23414-XX



SCHEMATIC DIAGRAM

Diablo Systems Incorporated

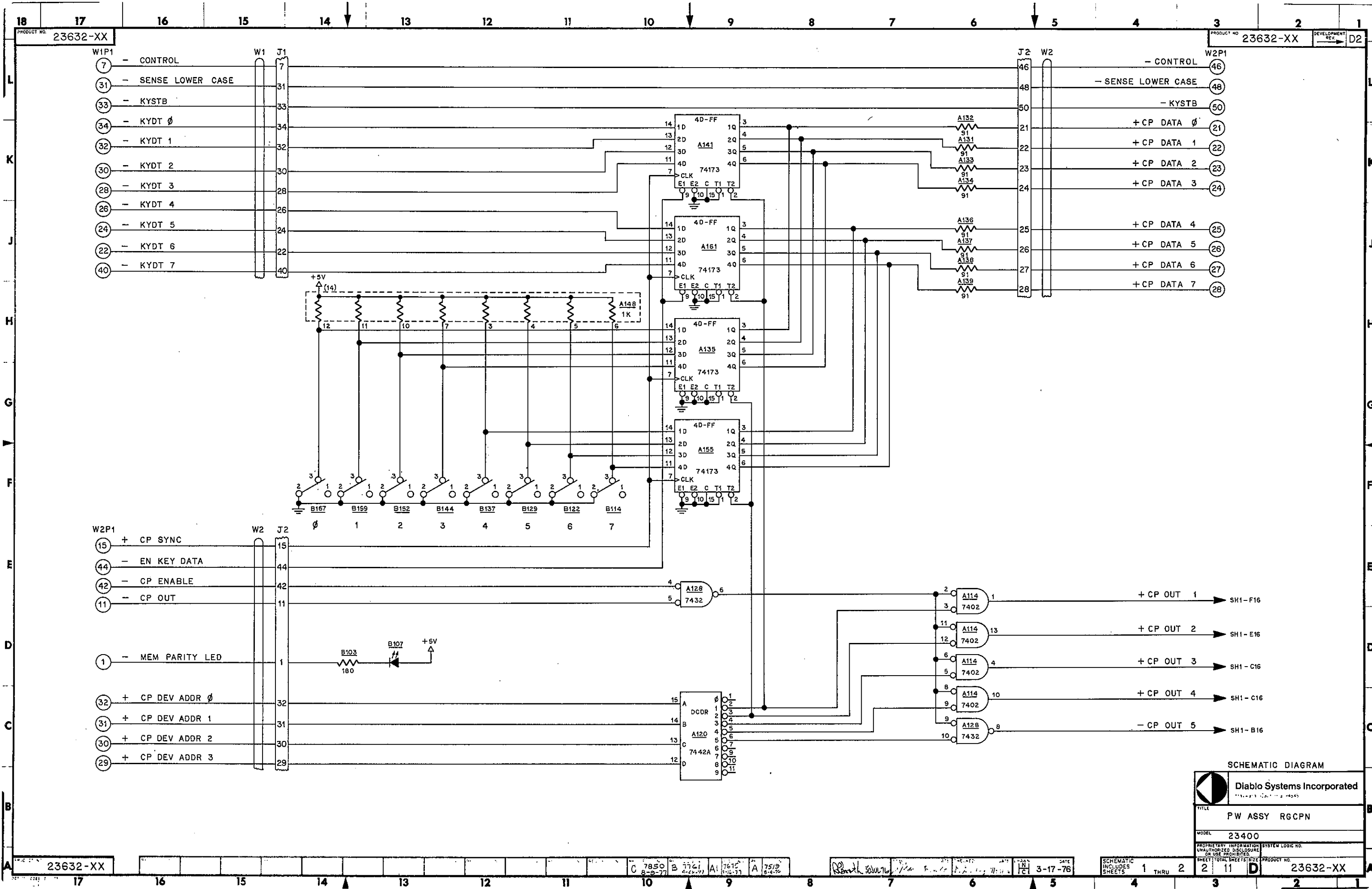
PWA ASSY, RG KEY

MODEL 23400

PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE IS PROHIBITED

SHEET NO. 8

PRODUCT NO. 23414-XX



SCHEMATIC DIAGRAM

Diablo Systems Incorporated
11400 California Street, San Diego, CA 92121

TITLE: PW ASSY RGCPN

MODEL: 23400

PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.

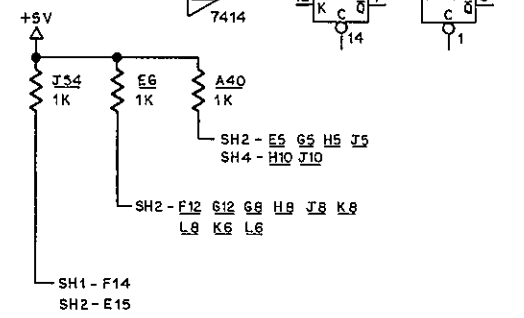
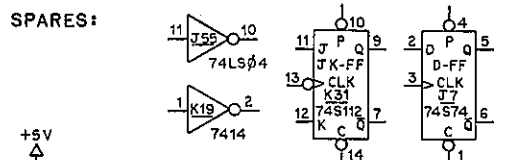
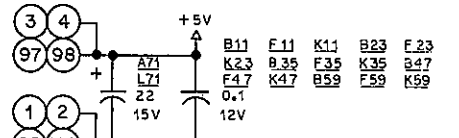
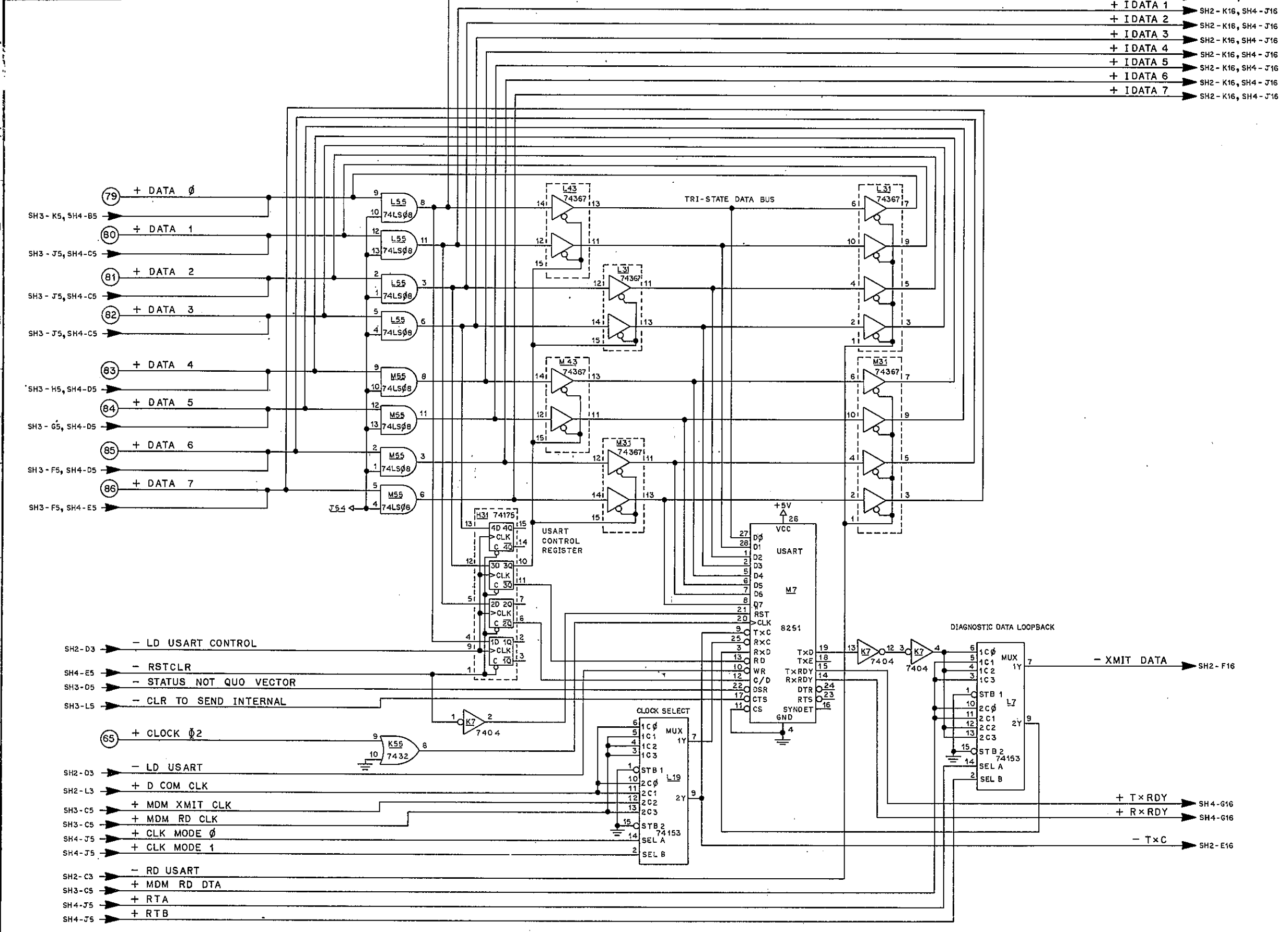
SHEET TOTAL SHEETS/SIZE PRODUCT NO. 23632-XX

PRODUCT NO. 23416-XX

23416-XX DEVELOPMENT D3

- + I DATA 0 SH2 - K16, SH4 - J16
- + I DATA 1 SH2 - K16, SH4 - J16
- + I DATA 2 SH2 - K16, SH4 - J16
- + I DATA 3 SH2 - K16, SH4 - J16
- + I DATA 4 SH2 - K16, SH4 - J16
- + I DATA 5 SH2 - K16, SH4 - J16
- + I DATA 6 SH2 - K16, SH4 - J16
- + I DATA 7 SH2 - K16, SH4 - J16

NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
 +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5 THESE RESISTORS ARE REMOVED WHEN THE INPUT SIGNAL IS PRESENT
 6 ON 23416-02, E42 AND E49 ARE INSTALLED, JUMPER E46 IS INSTALLED AND THE TRACE BETWEEN E45 AND E43-1 IS CUT. 23416-01 SHOWN.



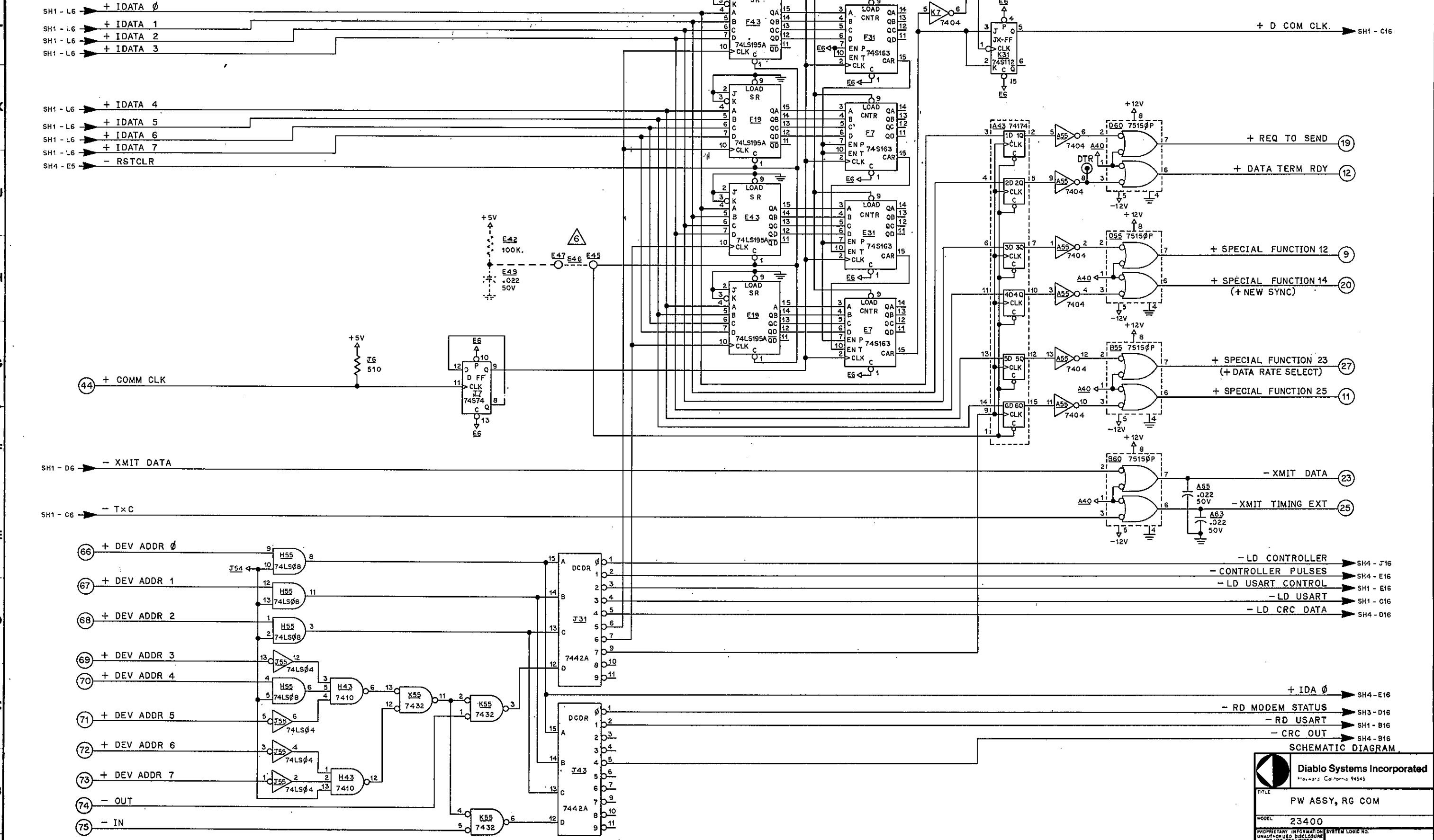
SCHMATIC DIAGRAM PWA

Diablo Systems Incorporated
 PW ASSY, RG COM
 MODEL 23400
 SHEET TOTAL SHEETS SIZE PRODUCT NO. 23416-XX

PRODUCT NO. 23416-XX

PRODUCT NO. 23416-XX

23416-XX DEVELOPMENT REV. D3



PRODUCT NO. 23416-XX

REV. NO.	DATE	BY	CHKD	DATE	BY	DATE	SHEET	TOTAL SHEETS	SIZE	PRODUCT NO.
							1	4	D	23416-XX

Diablo Systems Incorporated
 17000 California 94545

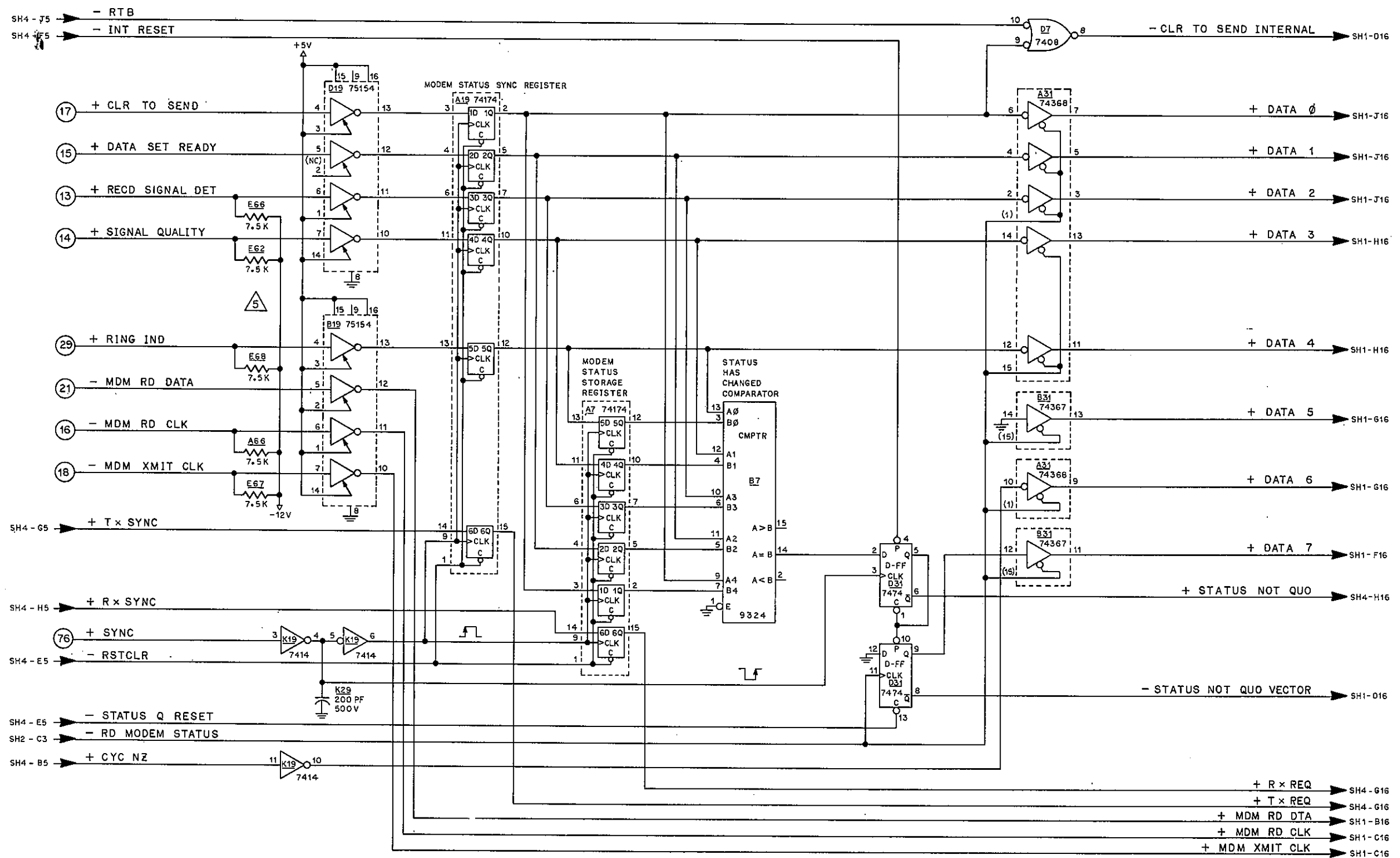
TITLE: PW ASSY, RG COM

MODEL: 23400

UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.

PROPRIETARY INFORMATION SYSTEM LOGIC NO.

SHEET TOTAL SHEETS SIZE PRODUCT NO. 23416-XX



SCHEMATIC DIAGRAM

Diablo Systems Incorporated

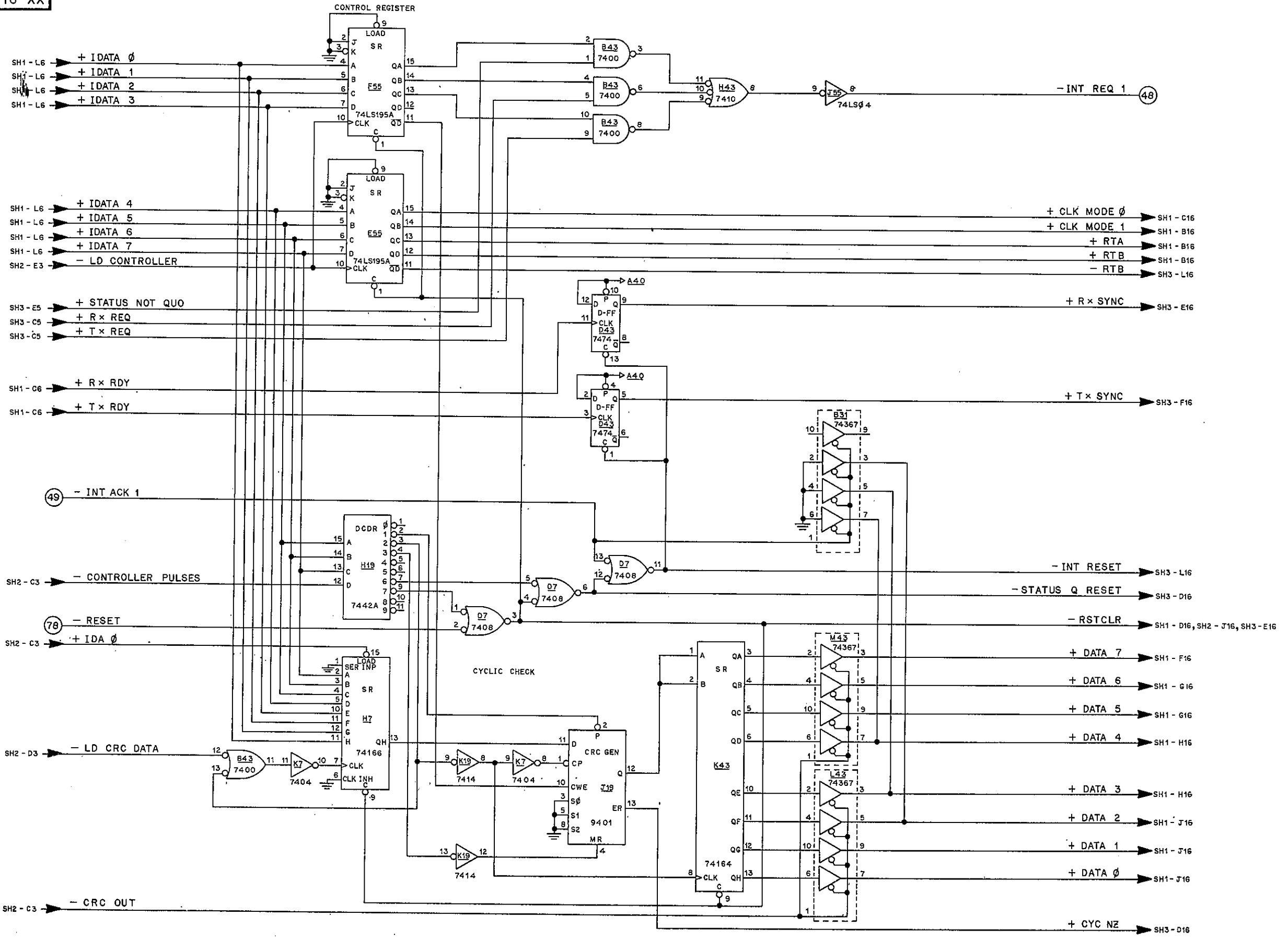
TITLE PW ASSY, RG COM

MODEL 23400

PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.

SHEET/TOTAL SHEETS/REV 3/11/D

PRODUCT NO. 23416-XX



SCHEMATIC DIAGRAM

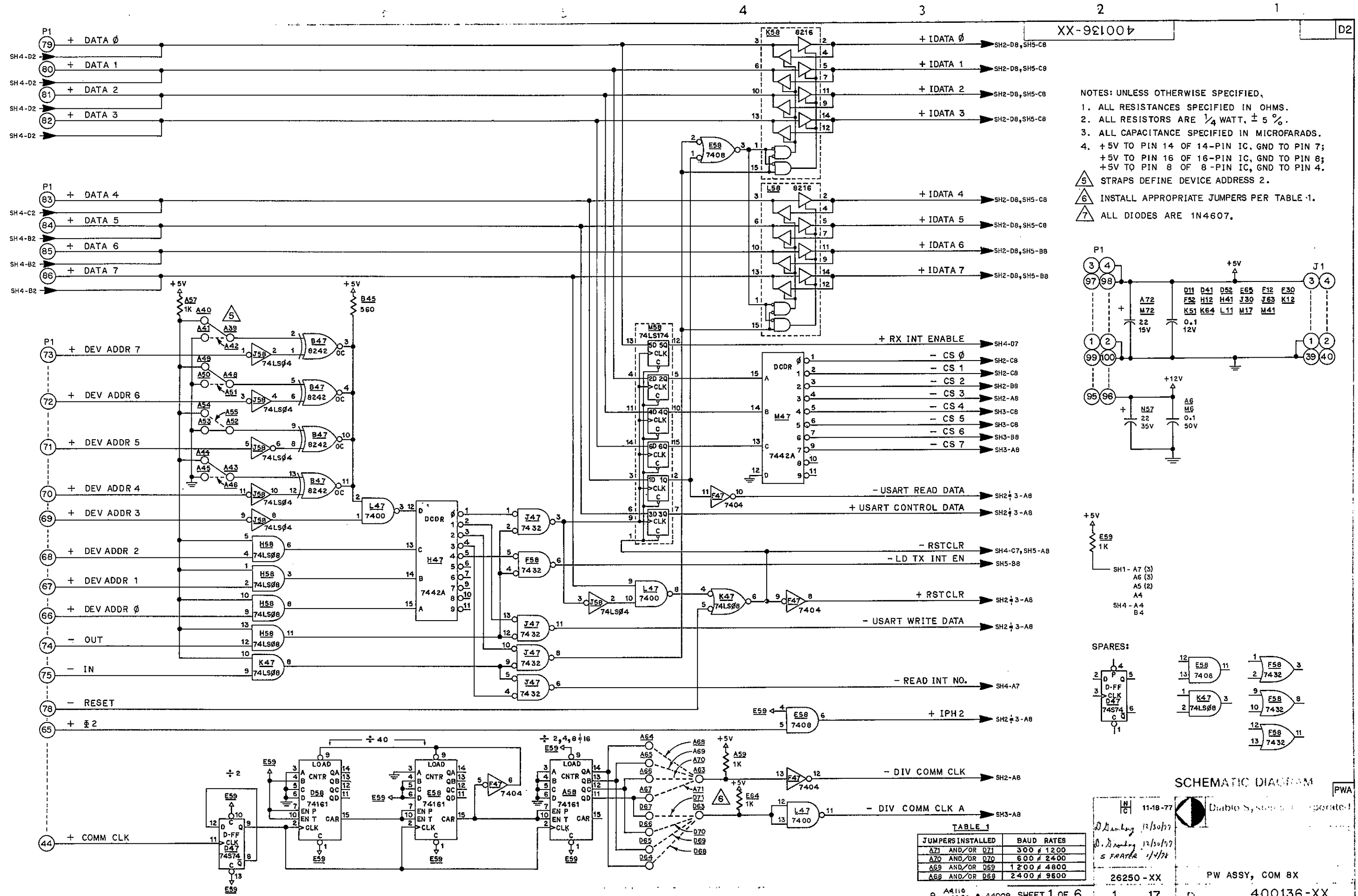
Diablo Systems Incorporated
Folsom, California 95635

TITLE
PW ASSY, RG COM

MODEL
23400

PROPRIETARY INFORMATION SYSTEM LOGIC NO.
UNAUTHORIZED DISCLOSURE
OR USE PROHIBITED

SHEET TOTAL SHEETS SIZE PRODUCT NO
4 11 D 23416-XX



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
+5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8;
+5V TO PIN 8 OF 8-PIN IC, GND TO PIN 4.
 5. STRAPS DEFINE DEVICE ADDRESS 2.
 6. INSTALL APPROPRIATE JUMPERS PER TABLE 1.
 7. ALL DIODES ARE 1N4607.

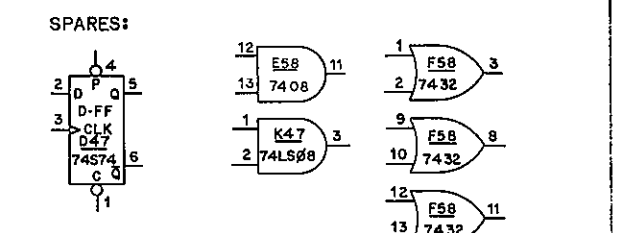
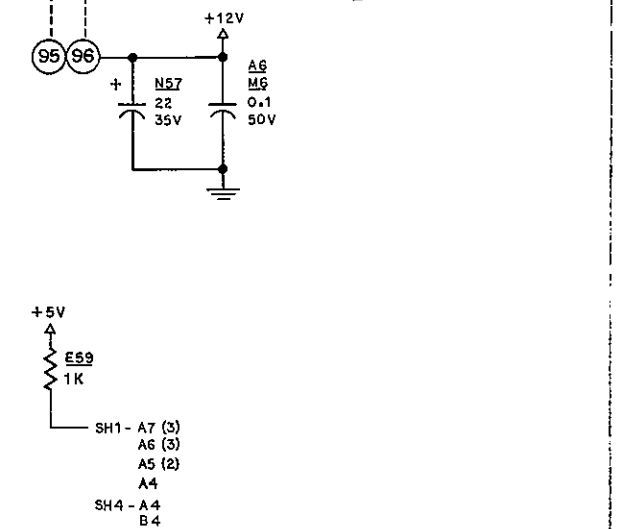
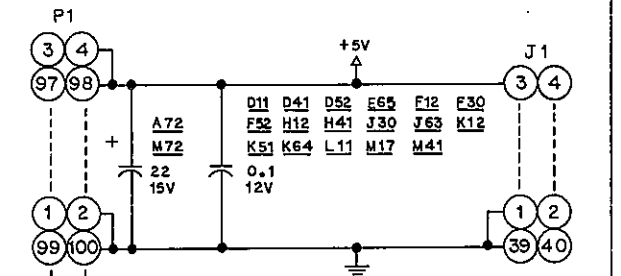


TABLE 1

JUMPERS INSTALLED	BAUD RATES
A71 AND/OR D71	300 # 1200
A70 AND/OR D70	600 # 2400
A69 AND/OR D69	1200 # 4800
A68 AND/OR D68	2400 # 9600

SCHEMATIC DIAGRAM

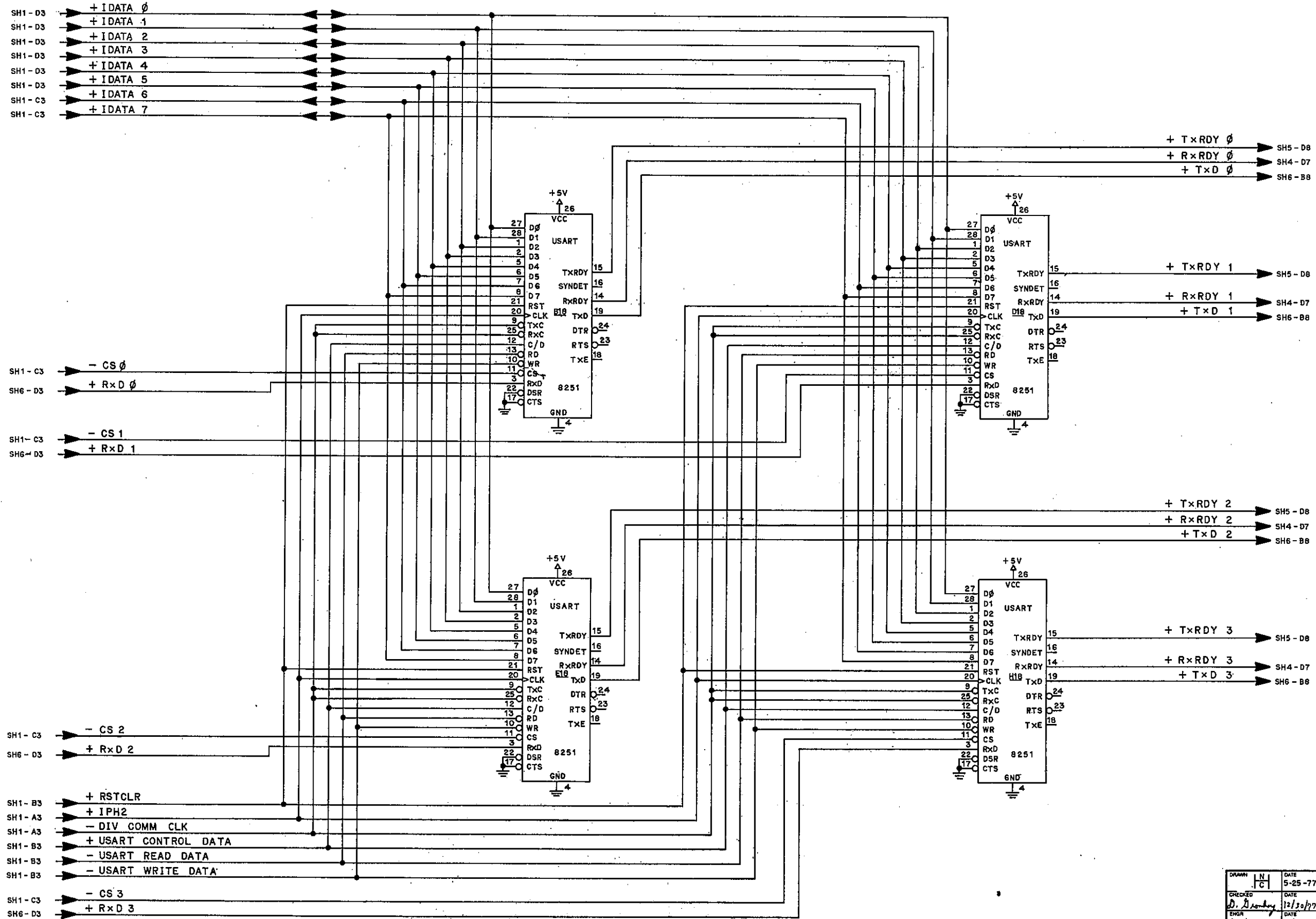
Diablo Systems Corporation

11-18-77

26250-XX

PW ASSY, COM 8X

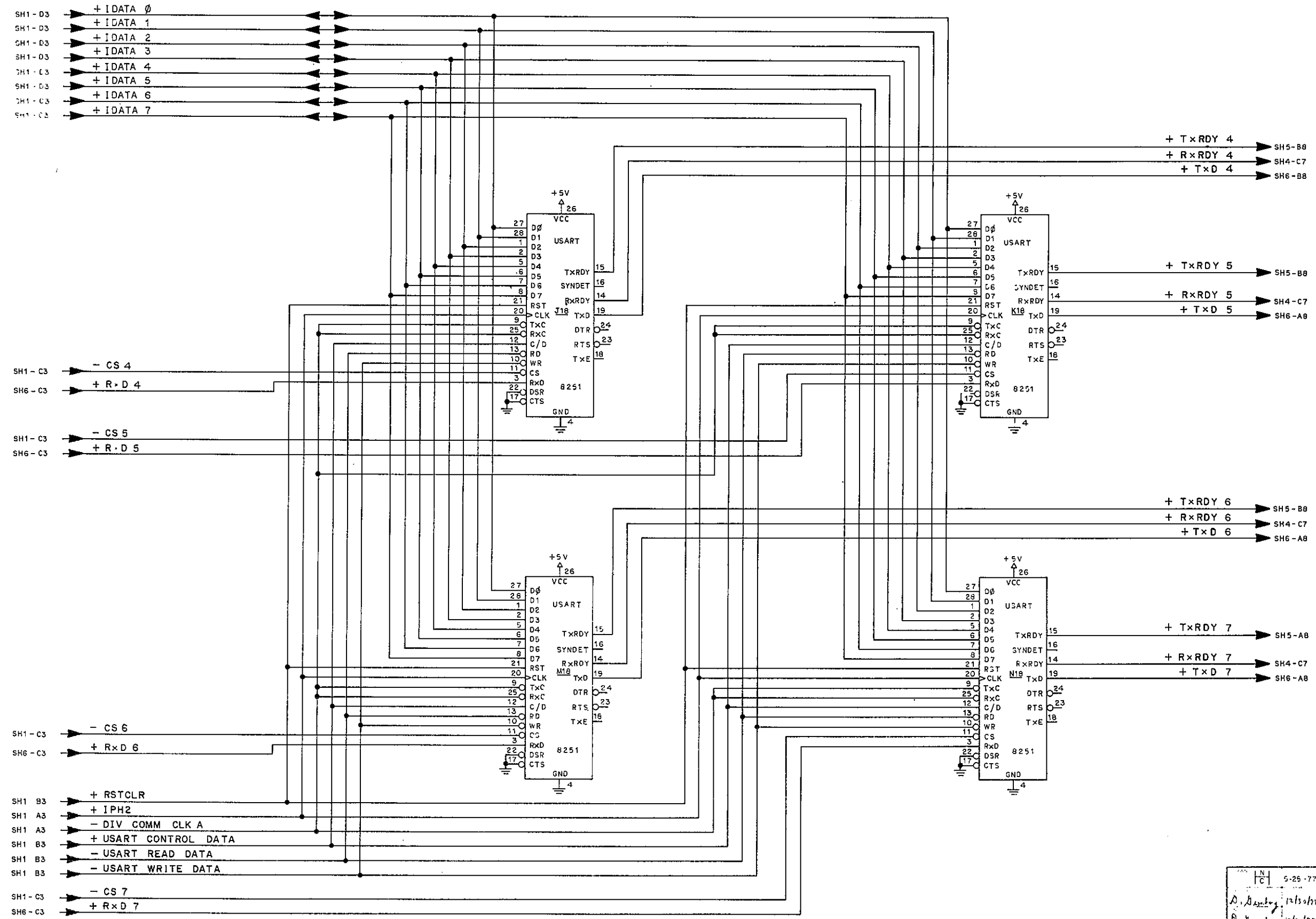
400136-XX



SCHEMATIC DIAGRAM

DRAWN	N/C	DATE	5-25-77	<p>Diablo Systems Incorporated Hayward, California 94545</p> <p>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</p>	
CHECKED	D. Donkey	DATE	12/20/77		
ENGR	D. Donkey	DATE	12/20/77		
APP'D	S. FRATER	DATE	1/4/78		
FIRST USED (NEXT ASBY)		26250-XX		PW ASSY, COM 8X	
SHEET	2	TOT SHEET	17	DWG NO	400136-XX

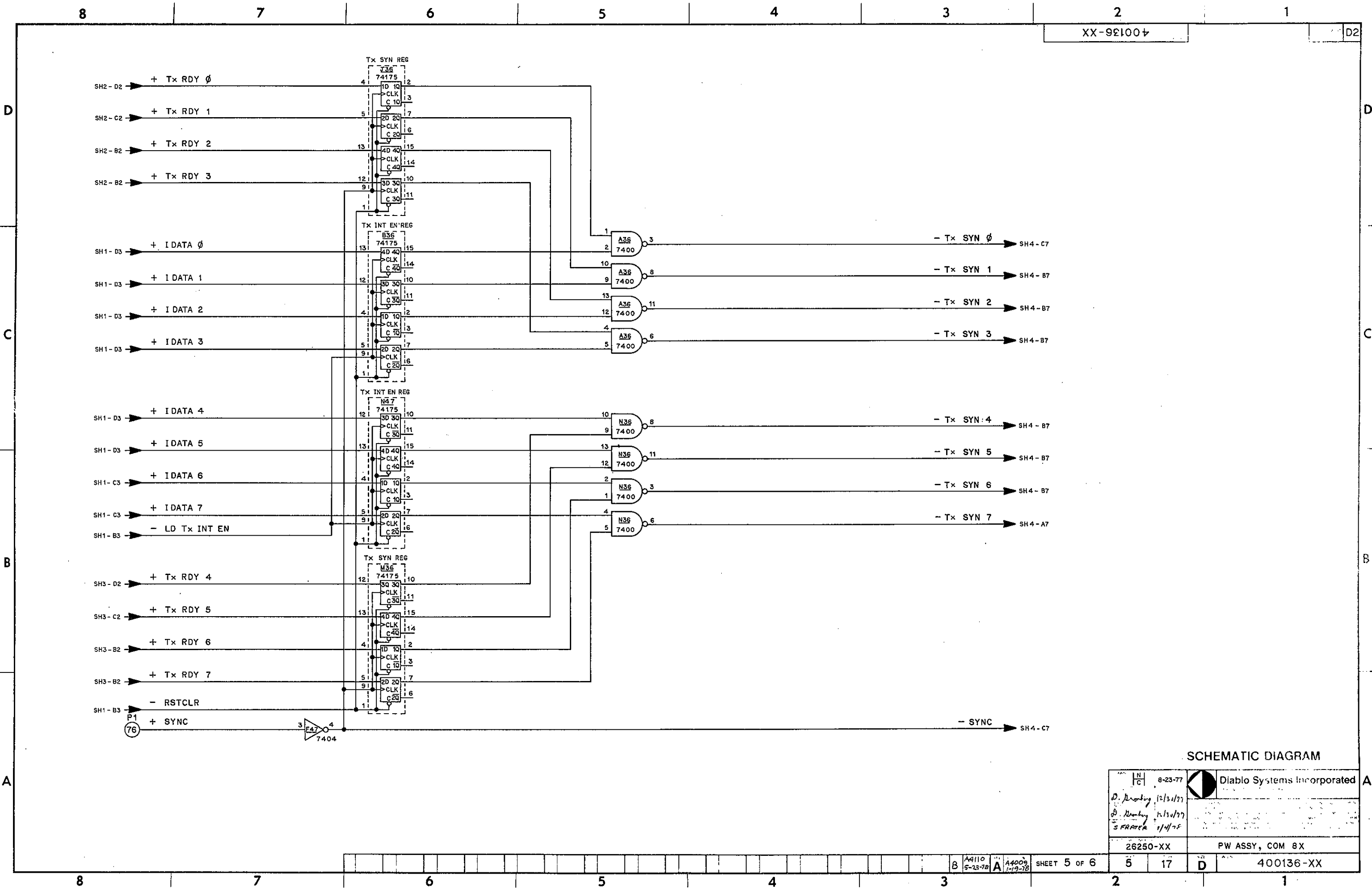
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SCHEMATIC DIAGRAM

DATE: 5-25-77	 Diablo Systems Incorporated
D. Bentley 11/3/77 S. FRATER 1/1/78	

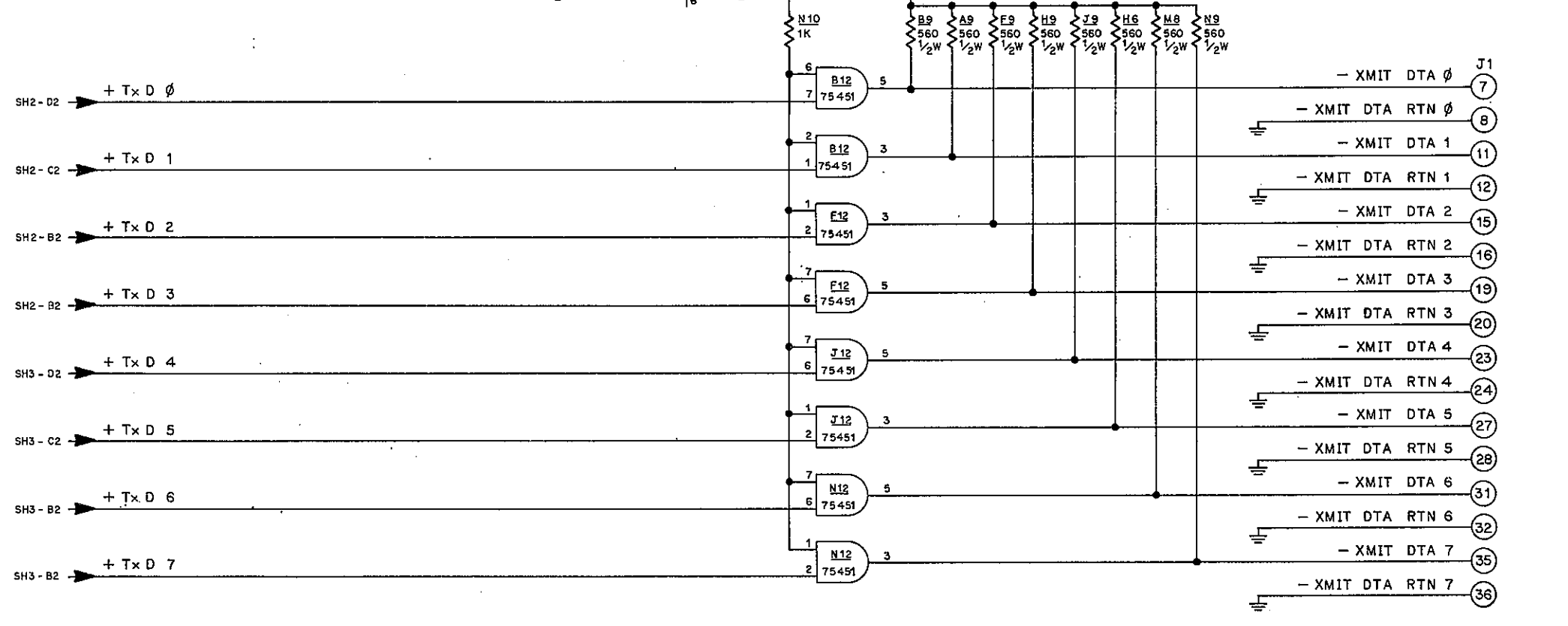
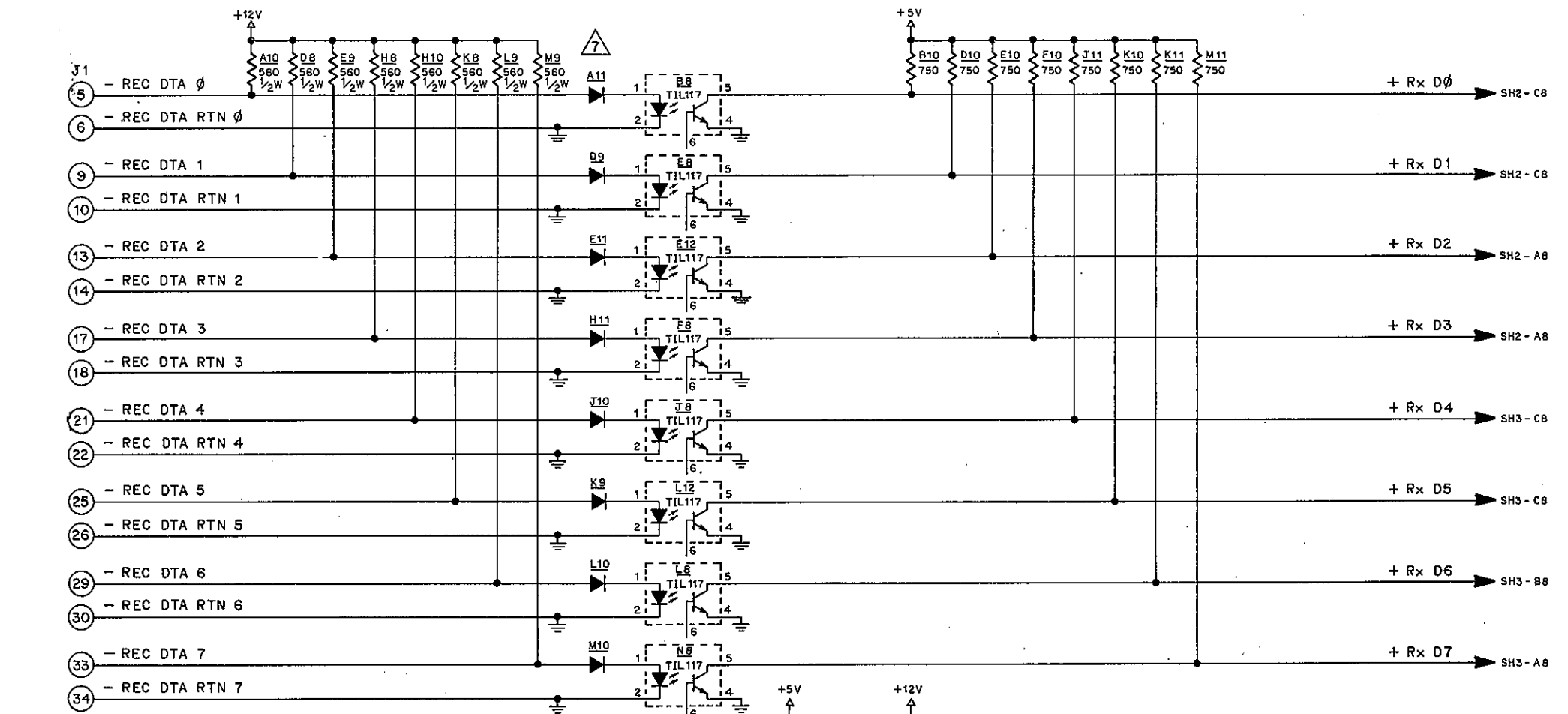
26250-XX	PW ASSY, COM 8X
3 17 D	400136-XX



SCHEMATIC DIAGRAM

8-23-77		Diablo Systems Incorporated	
D. Munking 12/31/77			
D. Munking 11/15/77			
S. PRATER 1/4/78			
26250-XX		PW ASSY, COM 8X	
5	17	D	400136-XX

8 7 6 5 4 3 2 1

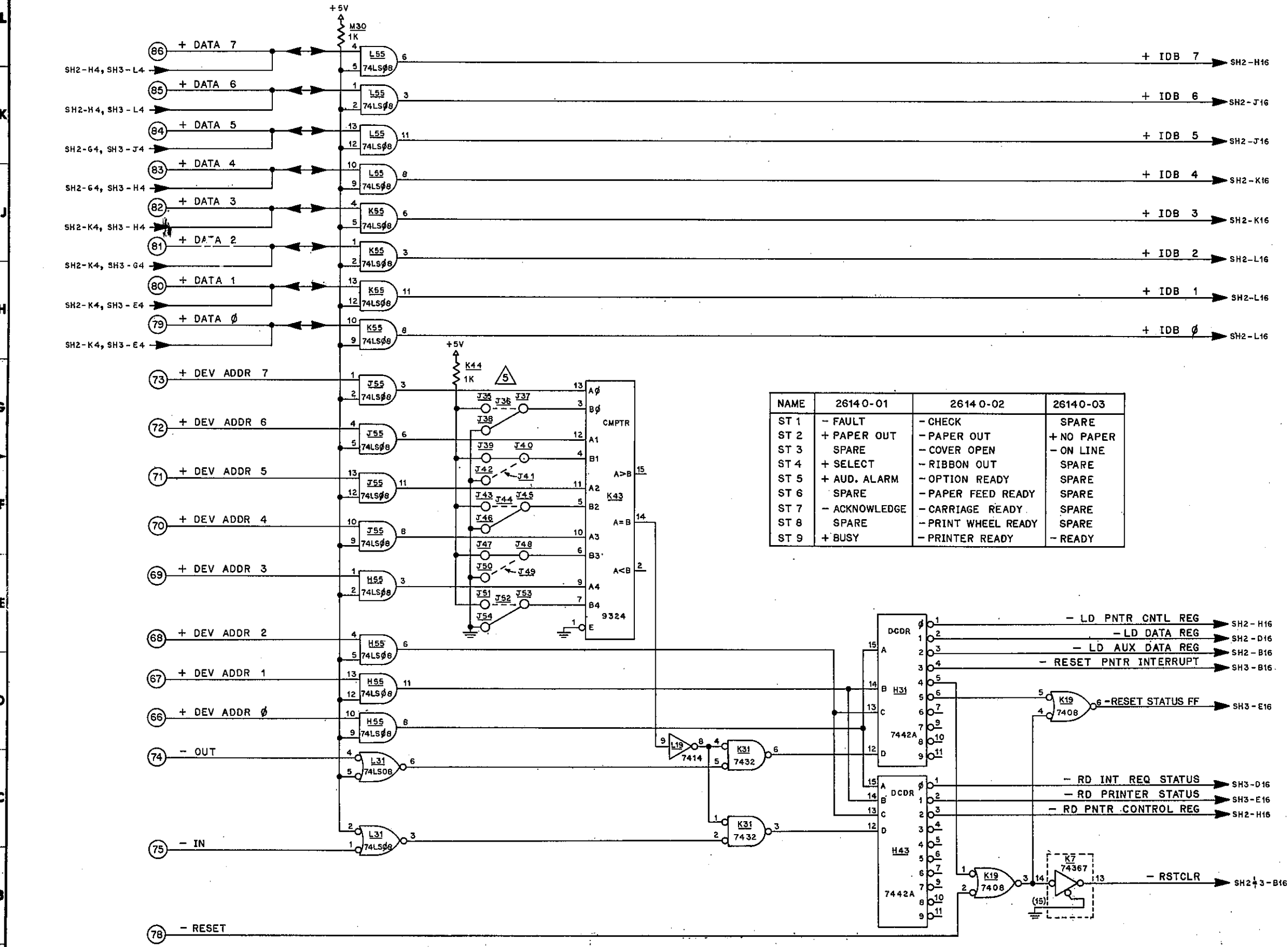


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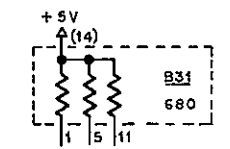
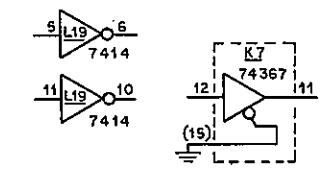
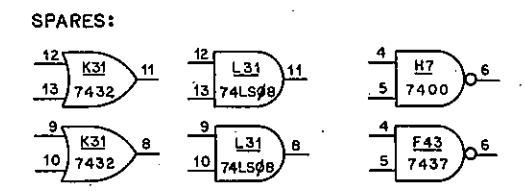
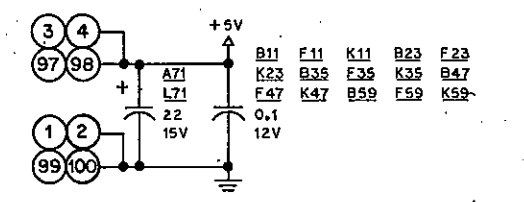
REV. NO.	REV. DATE	REV. BY	REV. DATE	Diablo Systems Incorporated Hayward, California 94545
1	12/30/77	D. B. B. / S. PRATER	1/13/77	
TITLE: PW ASSY, COM 8X SHEET: 6 OF 17 SIZE: D DWG NO: 400136-XX				THESE DRAWINGS AND/OR SPECIFICATIONS AND THE DATA CONTAINED THEREIN ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT BE DISCLOSED OR REPRODUCED IN ANY MANNER TO ANY OTHER PERSON OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS, INC.
PART NO. 26250-XX SHEET 6 OF 6 DATE 1/19/78 REV. 1				

8 7 6 5 4 3 2 1

- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7; +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. STRAPS DEFINE DEVICE ADDRESS 5.
 6. 26140-01 (MATRIX) SHOWN. FOR 26140-02 (HY TYPE II), REMOVE JUMPERS A19 B20 M20 D31, ADD JUMPERS A22 B17 D21 M17; FOR 26140-03, (VERSATEC), REMOVE JUMPERS B19 M19 M20 D53, ADD JUMPERS B2 D50 M12 M17.
 7. ACKNOWLEDGE AND REQUEST SIGNALS ARE STRAPABLE TO OTHER INTERRUPT LEVELS.
 8. SEE TABLE.



NAME	26140-01	26140-02	26140-03
ST 1	- FAULT	- CHECK	SPARE
ST 2	+ PAPER OUT	- PAPER OUT	+ NO PAPER
ST 3	SPARE	- COVER OPEN	- ON LINE
ST 4	+ SELECT	- RIBBON OUT	SPARE
ST 5	+ AUD. ALARM	- OPTION READY	SPARE
ST 6	SPARE	- PAPER FEED READY	SPARE
ST 7	- ACKNOWLEDGE	- CARRIAGE READY	SPARE
ST 8	SPARE	- PRINT WHEEL READY	SPARE
ST 9	+ BUSY	- PRINTER READY	- READY



PWA

SCHMATIC DIAGRAM

Diablo Systems Incorporated
Hayward, California 94545

TITLE PW ASSY, RG PRN

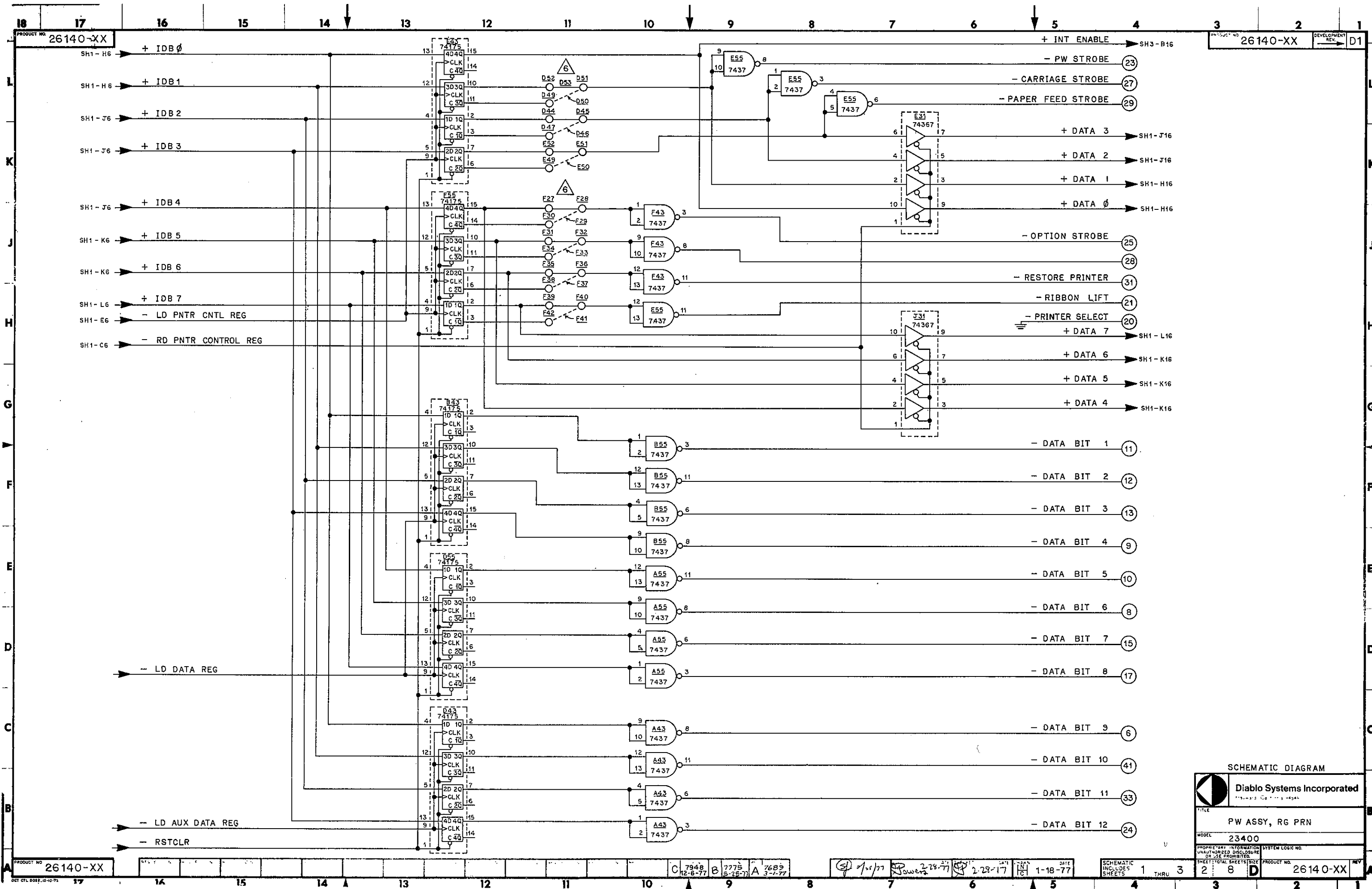
MODEL 23400

UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

DATE 1-18-77

1 THRU 3

26140-XX



SCHEMATIC DIAGRAM

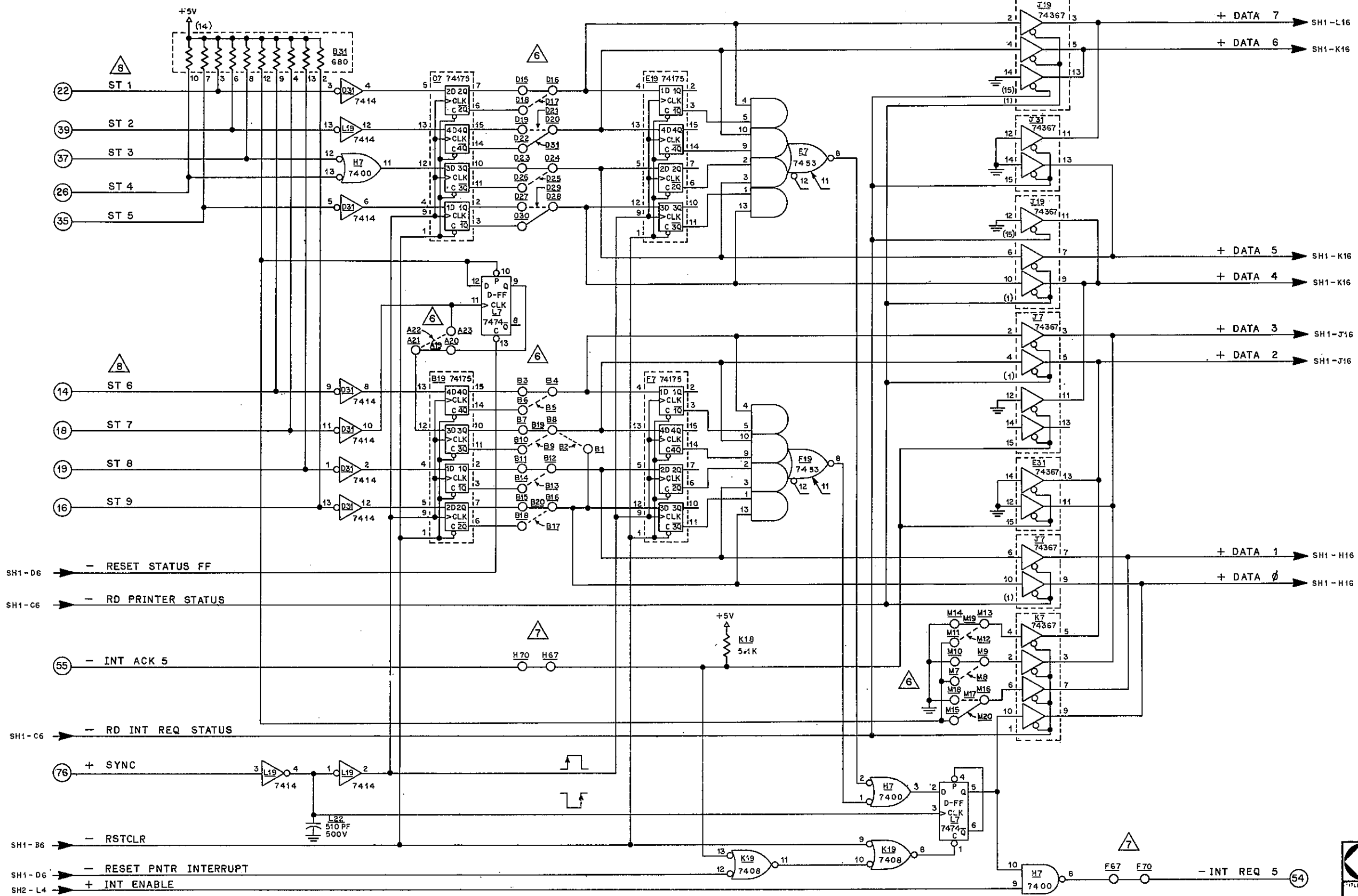
Diablo Systems Incorporated
PROPRIETARY INFORMATION
 UNAUTHORIZED DISCLOSURE
 OR USE PROHIBITED

PW ASSY, RG PRN

MODEL
23400

SYSTEM LOGIC NO.
UNAUTHORIZED DISCLOSURE
 OR USE PROHIBITED

SHEET TOTAL SHEETS SIZE PRODUCT NO. REV
2 8 D 26140-XX



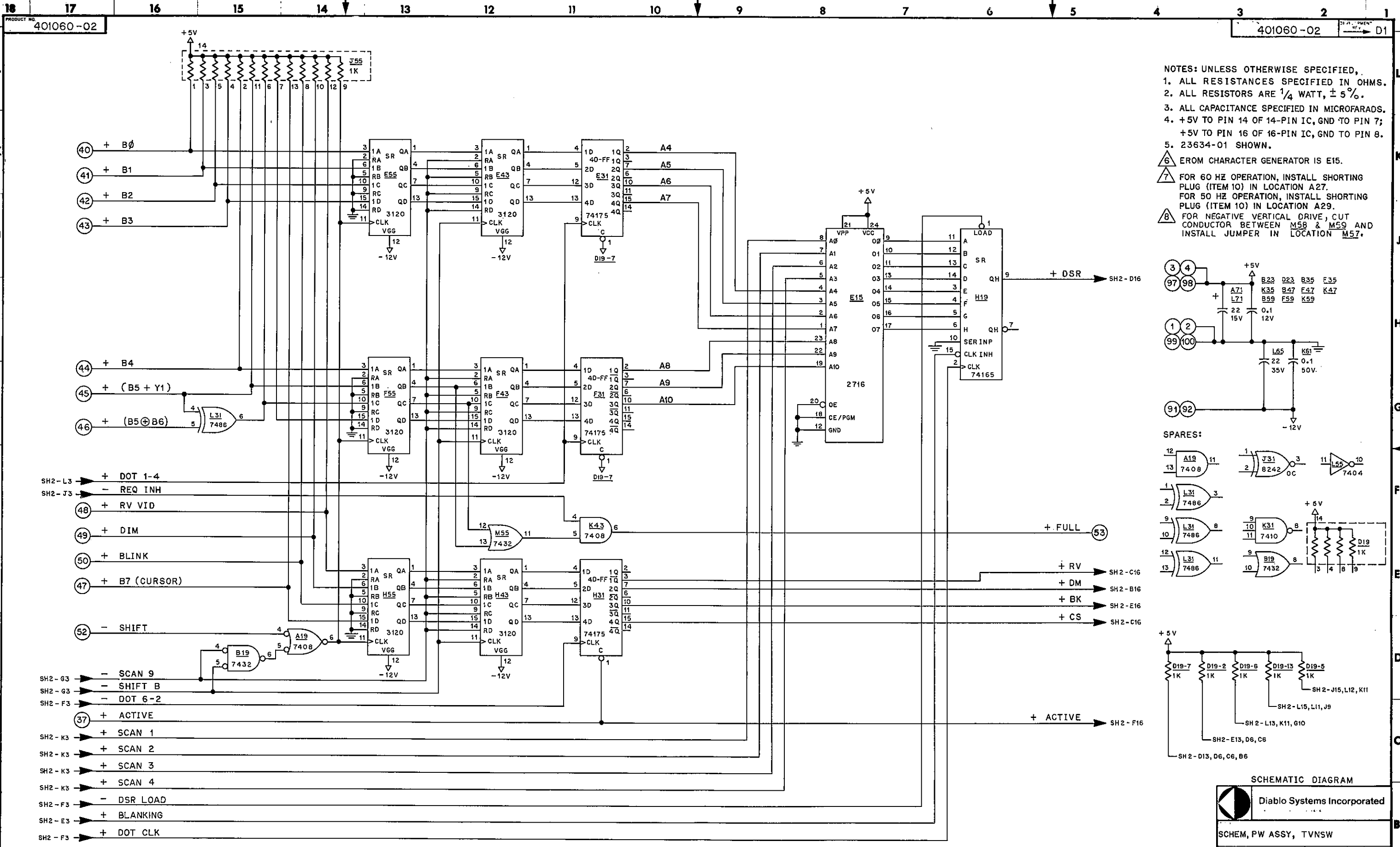
SCHEMATIC DIAGRAM

Diablo Systems Incorporated
1330 S. Bascom Ave. - San Jose, CA 95128

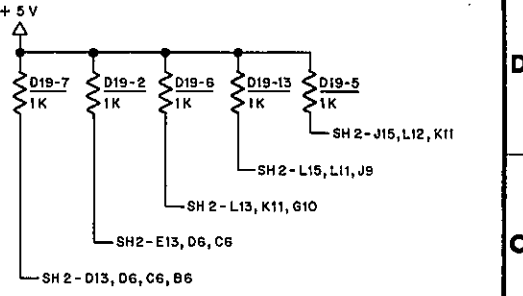
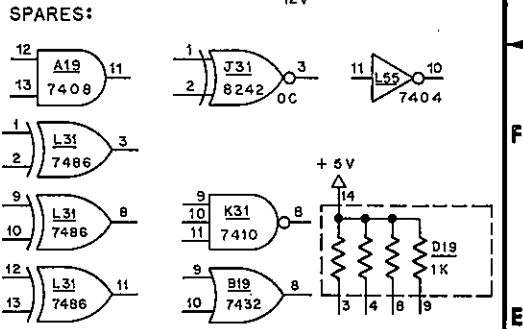
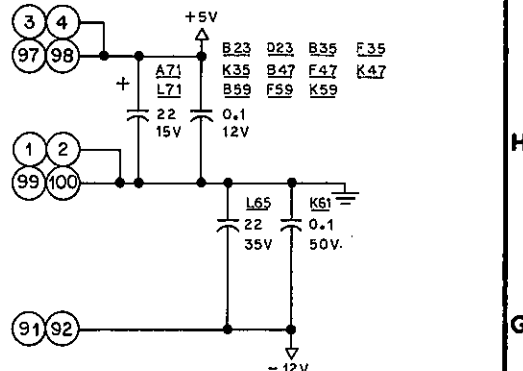
PW ASSY, RG PRN

MODEL 23400

PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7; +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. 23634-01 SHOWN.
 6. ROM CHARACTER GENERATOR IS E15.
 7. FOR 60 HZ OPERATION, INSTALL SHORTING PLUG (ITEM 10) IN LOCATION A27. FOR 50 HZ OPERATION, INSTALL SHORTING PLUG (ITEM 10) IN LOCATION A29.
 8. FOR NEGATIVE VERTICAL DRIVE, CUT CONDUCTOR BETWEEN M58 & M59 AND INSTALL JUMPER IN LOCATION M57.



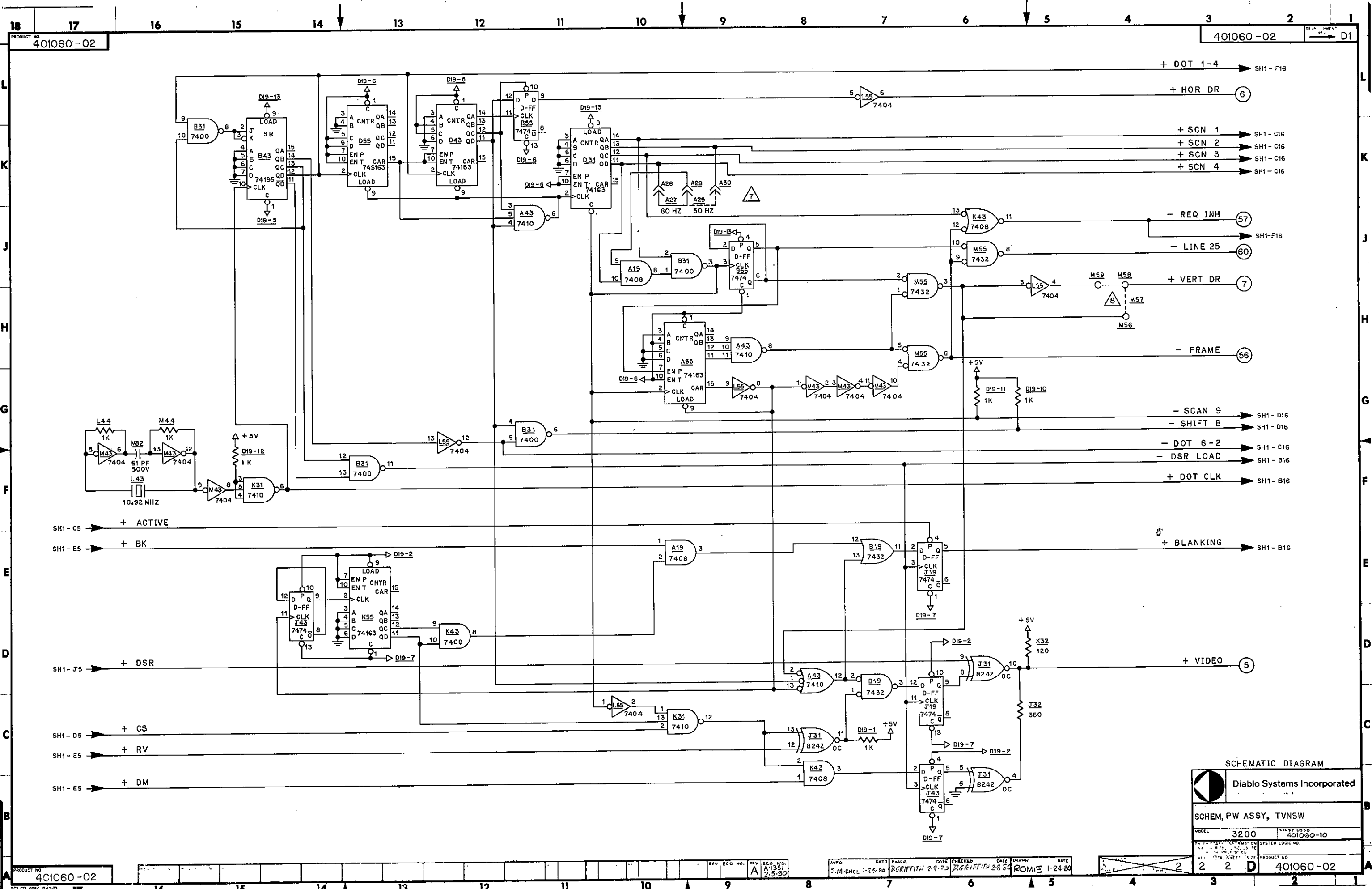
SCHMATIC DIAGRAM

Diablo Systems Incorporated

SCHEM, PW ASSY, TVNSW

MODEL	3200	FIRST USED	401060-10
DATE	1-24-80	DATE	
DRN	2	DATE	
REV	1	DATE	

PRODUCT NO 401060-02



PRODUCT NO
401060-02

401060-02

PRODUCT NO
401060-02

SCHEMATIC DIAGRAM

Diablo Systems Incorporated

SCHEM, PW ASSY, TVNSW

MODEL	3200	PRINTED USING	401060-10
DATE	1-25-80	DESIGNED BY	ROMIE 1-24-80
CHECKED BY	ROMIE 1-24-80	DRAWN BY	ROMIE 1-24-80
REV	ECO NO.	REV	ECO NO.
2	2	D	401060-02

18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

A B C D E F G H J K L

PRODUCT NO. 23636-XX

23636-XX DEPT. DOCUMENT NO. D2

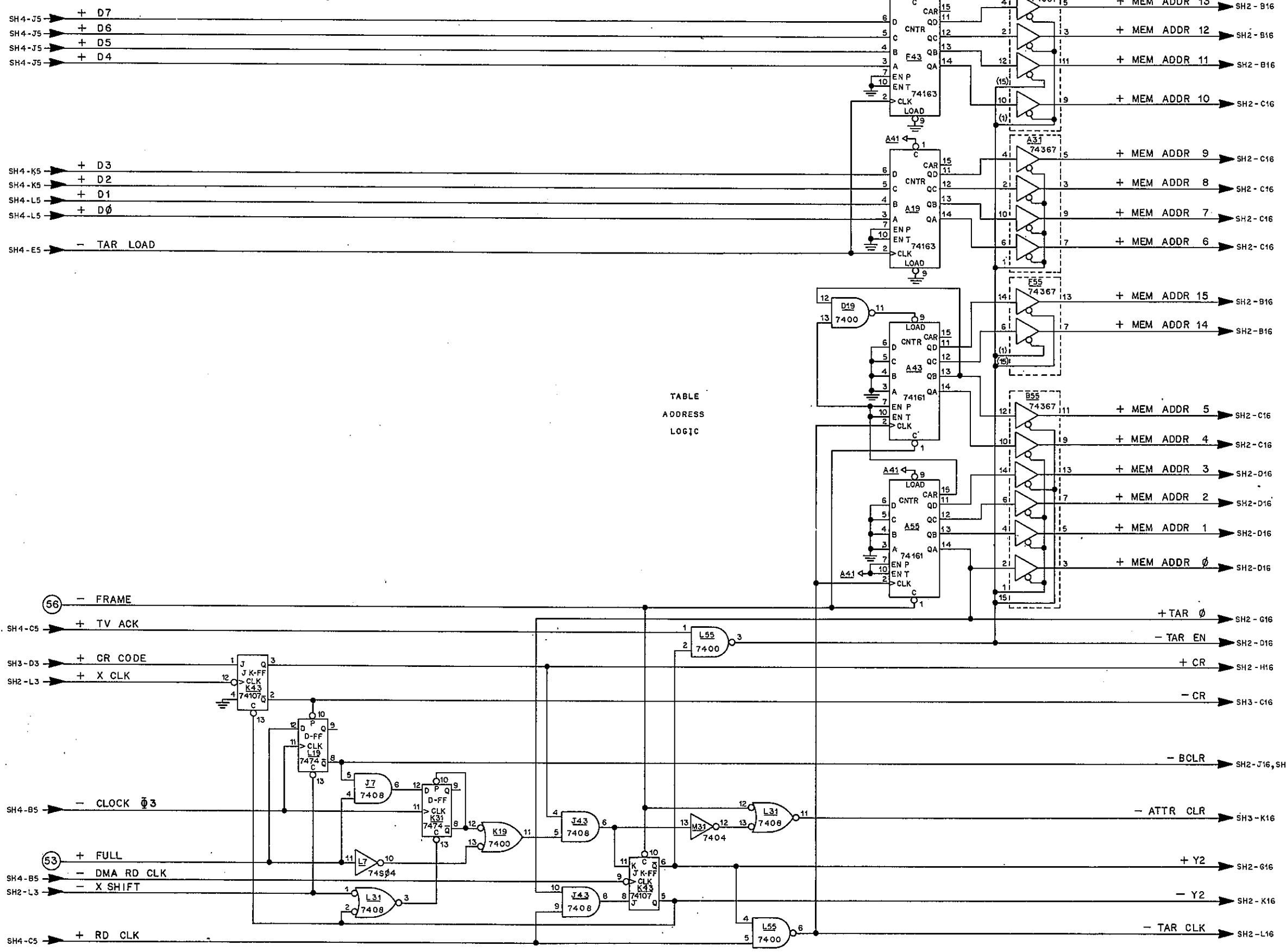
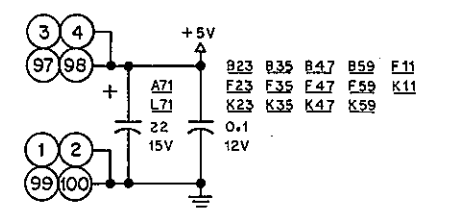
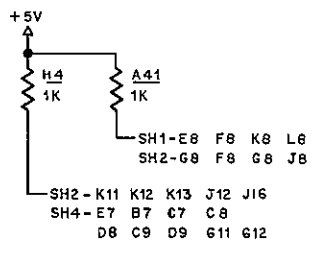
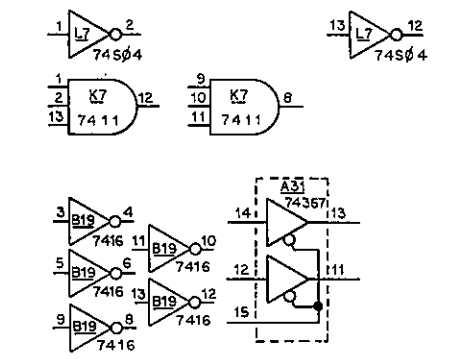


TABLE ADDRESS LOGIC

- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7; +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. 23636-01 SHOWN.



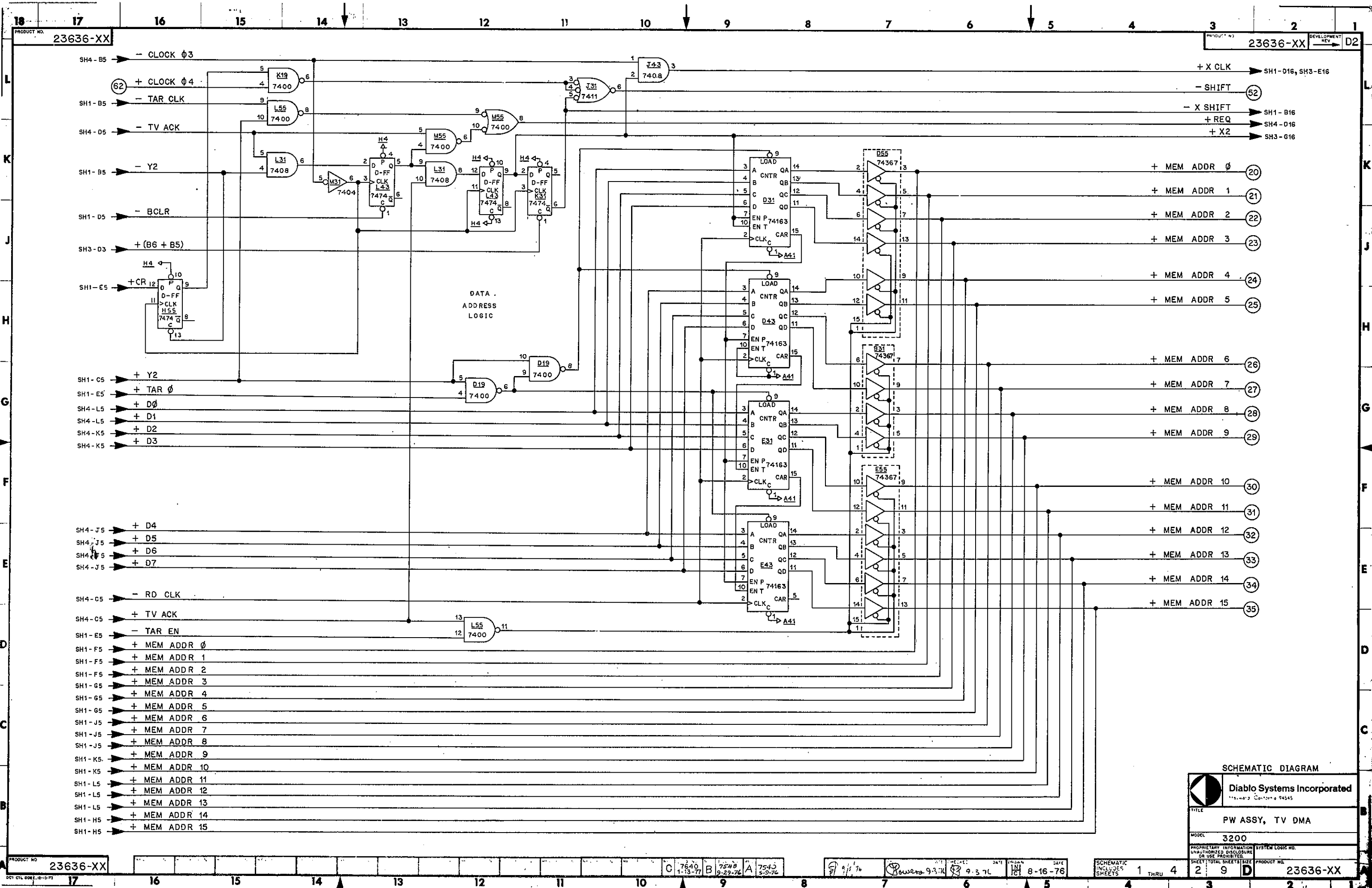
SPARES:



SCHEMATIC DIAGRAM

Diablo Systems Incorporated
 TITLE: PW ASSY, TV DMA
 MODEL: 3200
 PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.
 SHEET TOTAL SHEETS SIZE PRODUCT NO. 23636-XX

PRODUCT NO. 23636-XX



PRODUCT NO. 23636-XX

PRODUCT NO. 23636-XX DEVELOPMENT REV. D2

DATA ADDRESS LOGIC

SCHMATIC DIAGRAM

Diablo Systems Incorporated
 Hayward, California 94545

TITLE: PW ASSY, TV DMA

MODEL: 3200

PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

SHEET TOTAL SHEETS SIZE PRODUCT NO. 23636-XX

DATE: 8-16-76

DATE: 9-3-76

DATE: 1-13-77

DATE: 9-29-76

DATE: 5-3-76

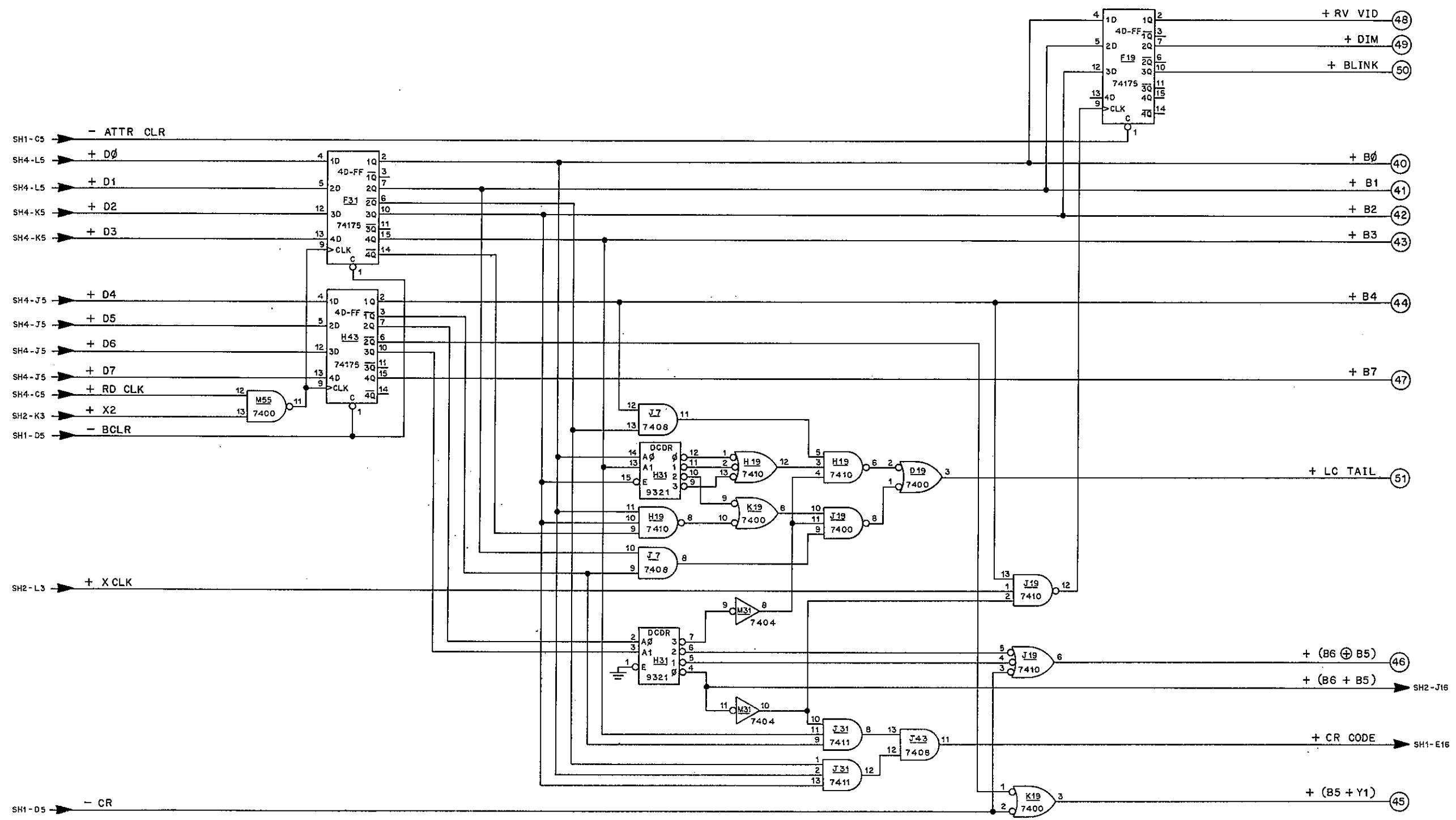
SHEETS: 1 THRU 4

2 9 D

PRODUCT NO. 23636-XX

23636-XX

23636-XX DEVELOPMENT REV. D2



SCHEMATIC DIAGRAM

Diablo Systems Incorporated
 Inland, California 94545

TITLE: PW ASSY, TV DMA

MODEL: 3200

PROPERTY INFORMATION SYSTEM LOGIC NO.
 UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

SHEET TOTAL SHEETS: 9
 SHEET: 3

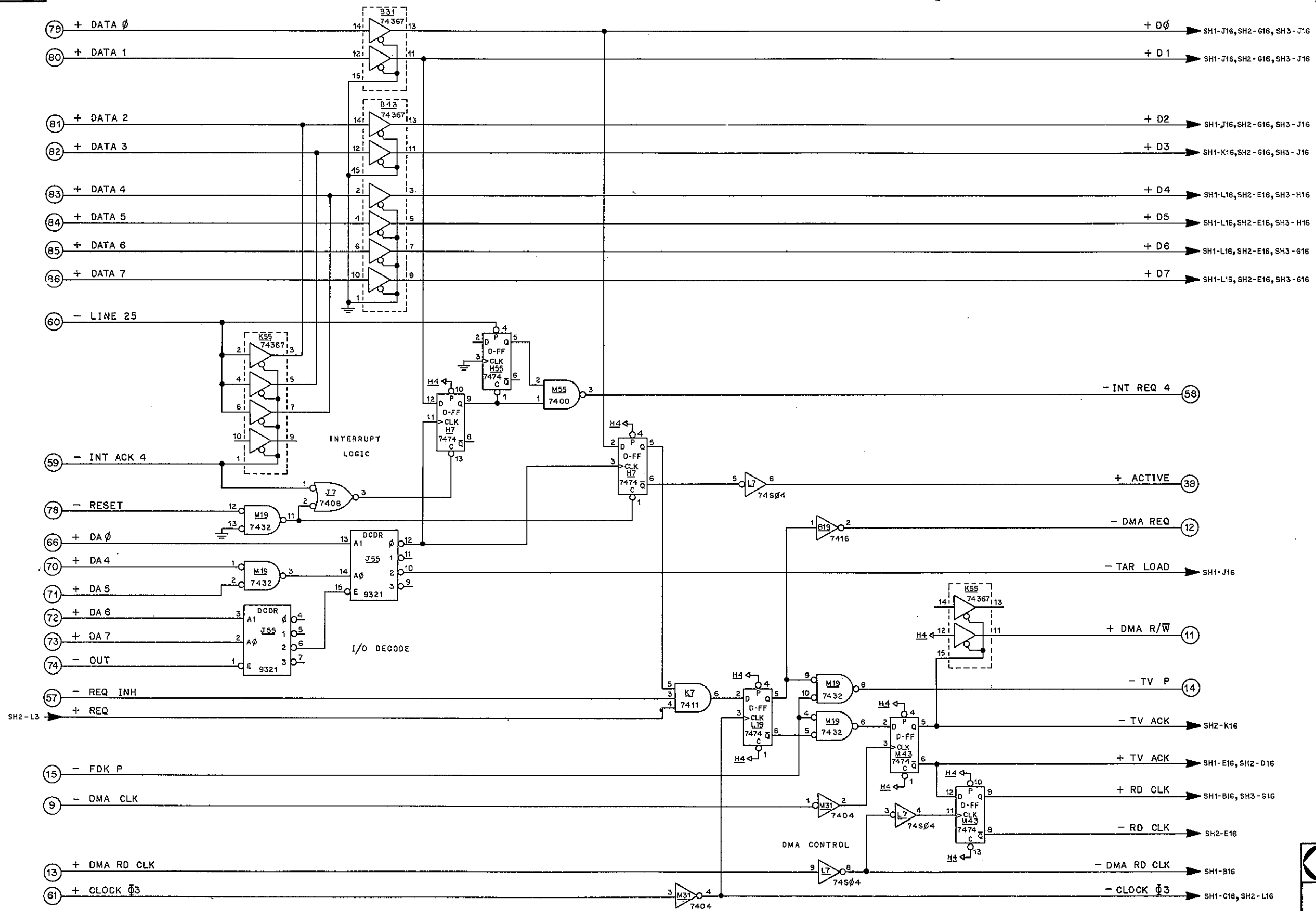
DATE: 8-17-76

PRODUCT NO. 23636-XX

23636-XX

C	7640	B	7548	A	7540	9/1/76	Power 9.170	9.2.76	8-17-76
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SCHEMATIC INCLUDES SHEETS 1 THRU 4 3 9 D 23636-XX



SCHMATIC DIAGRAM

Diablo Systems Incorporated

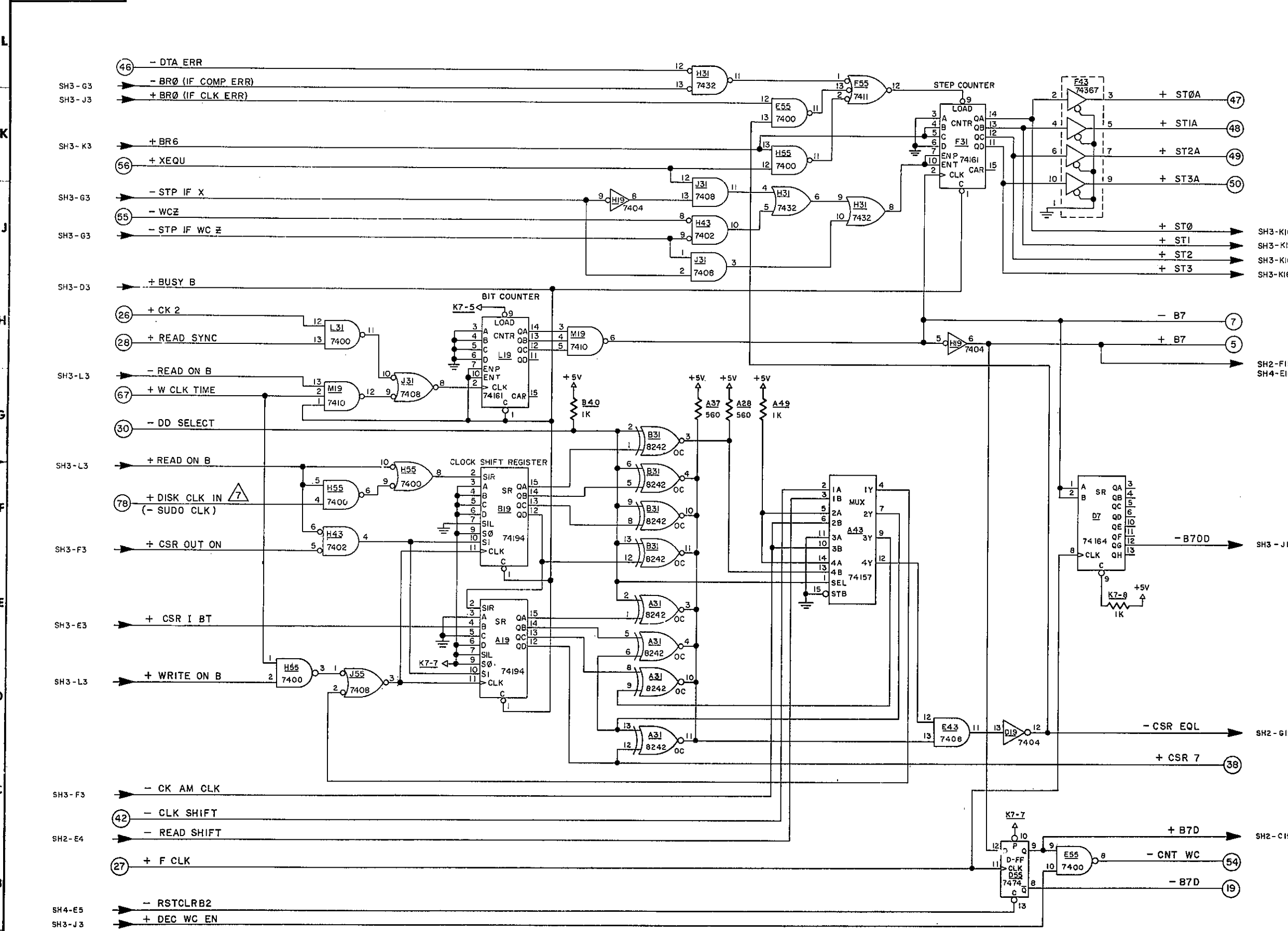
PW ASSY, TV DMA

MODEL 3200

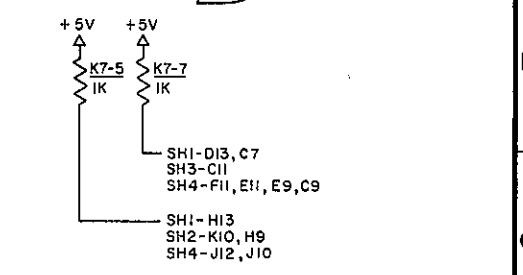
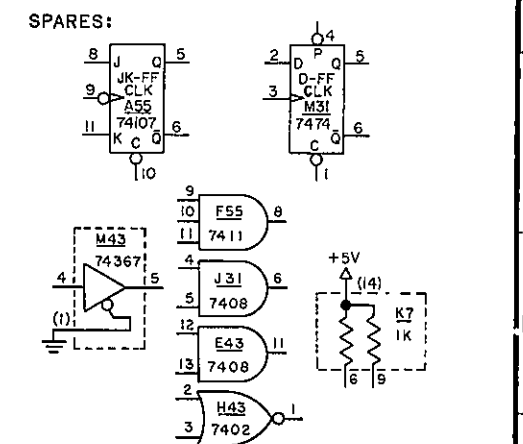
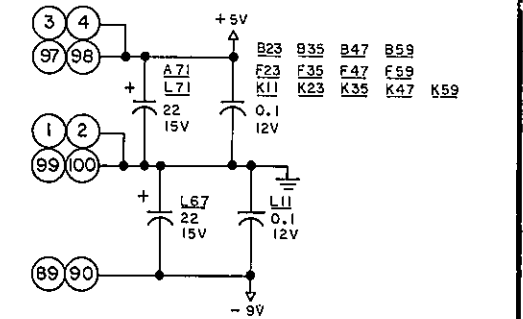
PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNL. IMPRINTED DISCLOSURE IS PROHIBITED.

SHEET TOTAL SHEETS SIZE PRODUCT NO. 23636-XX

1 THRU 4 4 9 D



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ±5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7; +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.
 5. 26136-01 SHOWN.
- △ DDB PREFIX IS USED FOR DOUBLE DENSITY RECORDING, FCB PREFIX IS USED FOR SINGLE DENSITY RECORDING.
- △ SIGNAL NAMES IN PARENTHESIS ARE USED WITH RGFCC AND RGFSC. NAMES WITHOUT PARENTHESIS ARE USED WITH RGFDC.



SCHEMATIC DIAGRAM PWA

Diablo Systems Incorporated
10001 California 94545

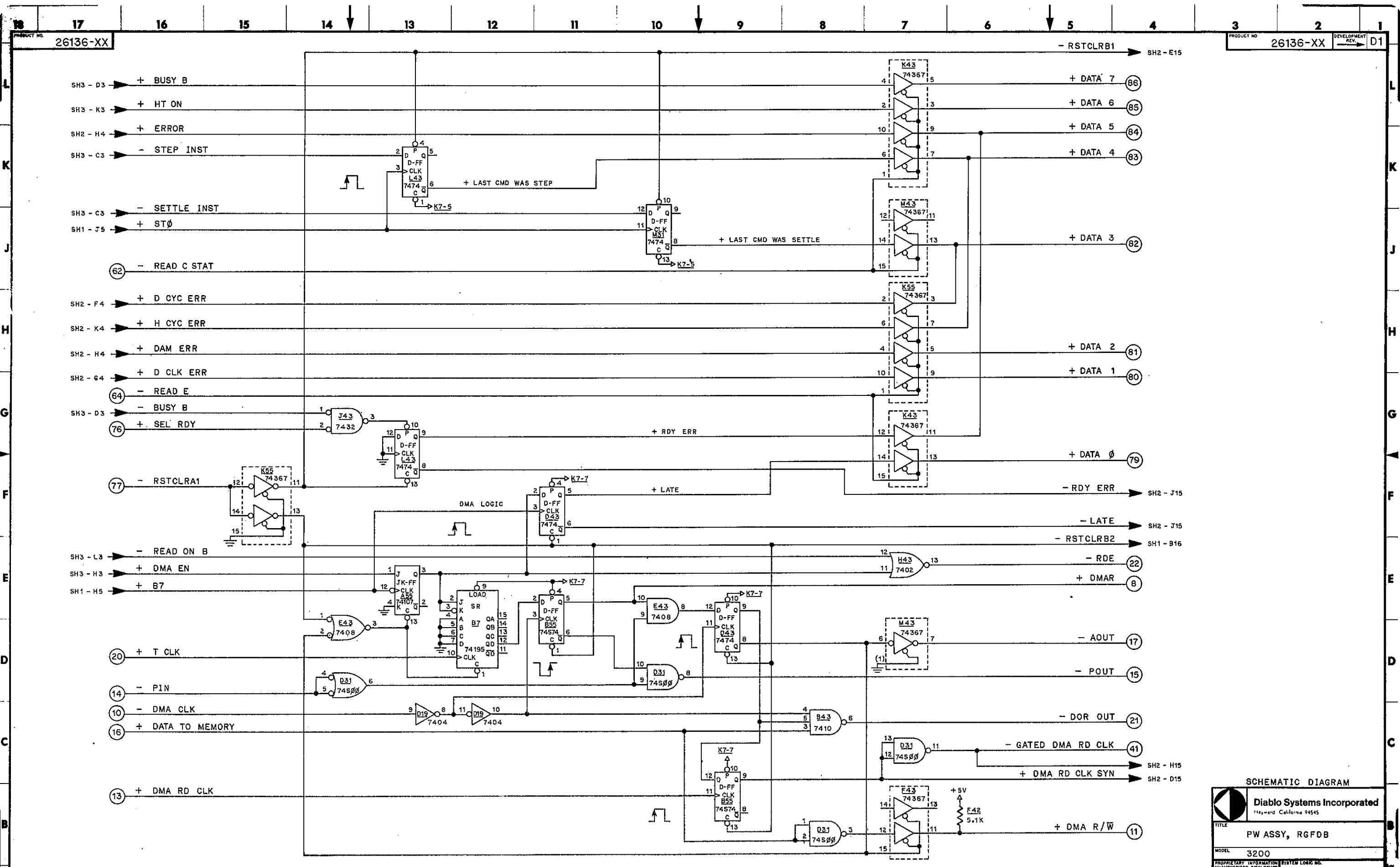
TITLE PW ASSY, RGFDB

MODEL 3200

PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED SYSTEM LOGIC NO.

SHEET TOTAL SHEETS SIZE PRODUCT NO. 26136-XX

10 D



18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1

PRODUCT NO. 26136-XX

SH3 - D3 + BUSY B

SH3 - K3 + HT ON

SH2 - H4 + ERROR

SH3 - C3 - STEP INST

SH3 - C3 - SETTLE INST

SH1 - J5 + ST0

(62) - READ C STAT

SH2 - F4 + D CYC ERR

SH2 - K4 + H CYC ERR

SH2 - H4 + DAM ERR

SH2 - G4 + D CLK ERR

(64) - READ E

SH3 - D3 - BUSY B

(76) + SEL RDY

(77) - RSTCLRA1

SH3 - L3 - READ ON B

SH3 - H3 + DMA EN

SH1 - H5 + B7

(20) + T CLK

(14) - PIN

(10) - DMA CLK

(16) + DATA TO MEMORY

(13) + DMA RD CLK

SH2 - E15 - RSTCLRB1

+ DATA 7 (86)

+ DATA 6 (85)

+ DATA 5 (84)

+ DATA 4 (83)

+ DATA 3 (82)

+ DATA 2 (81)

+ DATA 1 (80)

+ DATA 0 (79)

- RDY ERR (SH2 - J15)

- LATE (SH2 - J15)

- RSTCLRB2 (SH1 - B16)

- RDE (22)

+ DMAR (8)

- AOUT (17)

- POUT (15)

- DOR OUT (21)

- GATED DMA RD CLK (41)

+ DMA RD CLK SYN (SH2 - H15)

+ DMA R/W (11)

PRODUCT NO. 26136-XX

REV ECD NO. 7892 10-10-77

REV ECD NO. 7813 7-1-77

REV ECD NO. 7772 5-23-77

REV ECD NO. 7760

DATE CHECKED 4/1/77

DATE DRAWN 1-7-77

SHEET TOTAL SHEETS 4

SHEET 1 THRU 4

PRODUCT NO. 26136-XX

PRODUCT NO. 26136-XX DEVELOPMENT REV. D1

SCHMATIC DIAGRAM

Diablo Systems Incorporated
Hayward, California 94545

TITLE PW ASSY, RG FDB

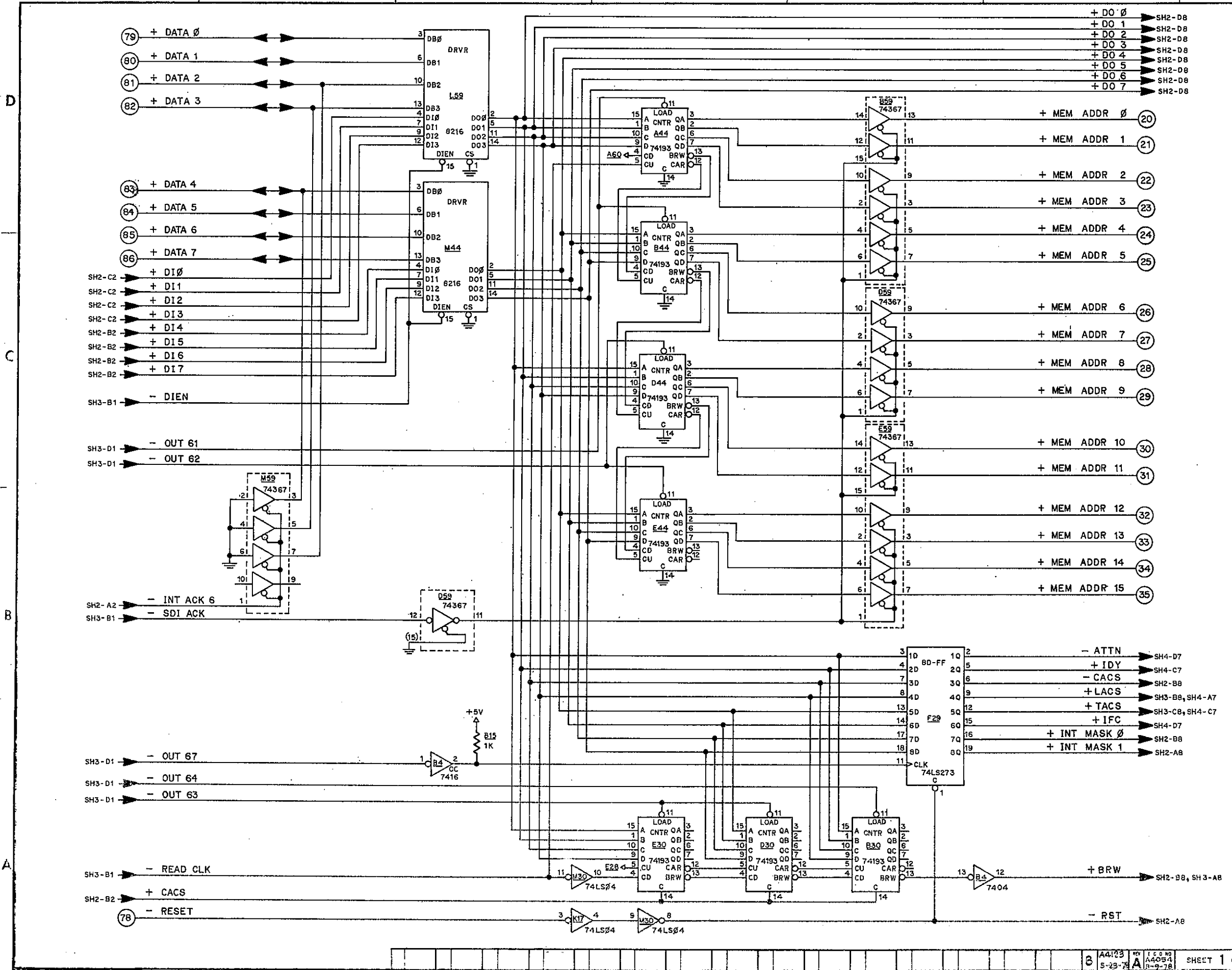
MODEL 3200

PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

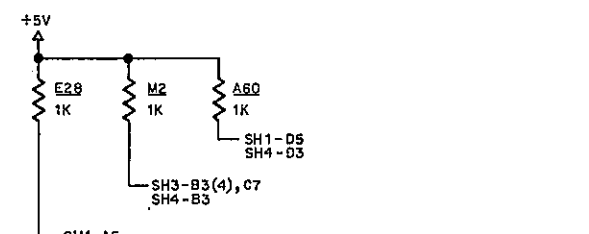
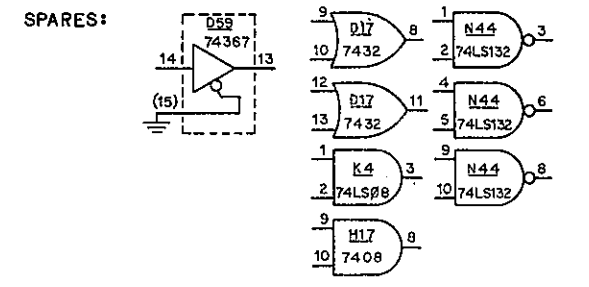
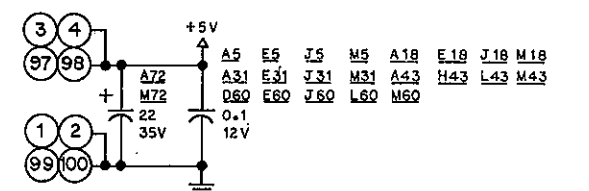
STATED LOGIC NO.

DATE 1-7-77

PRODUCT NO. 26136-XX



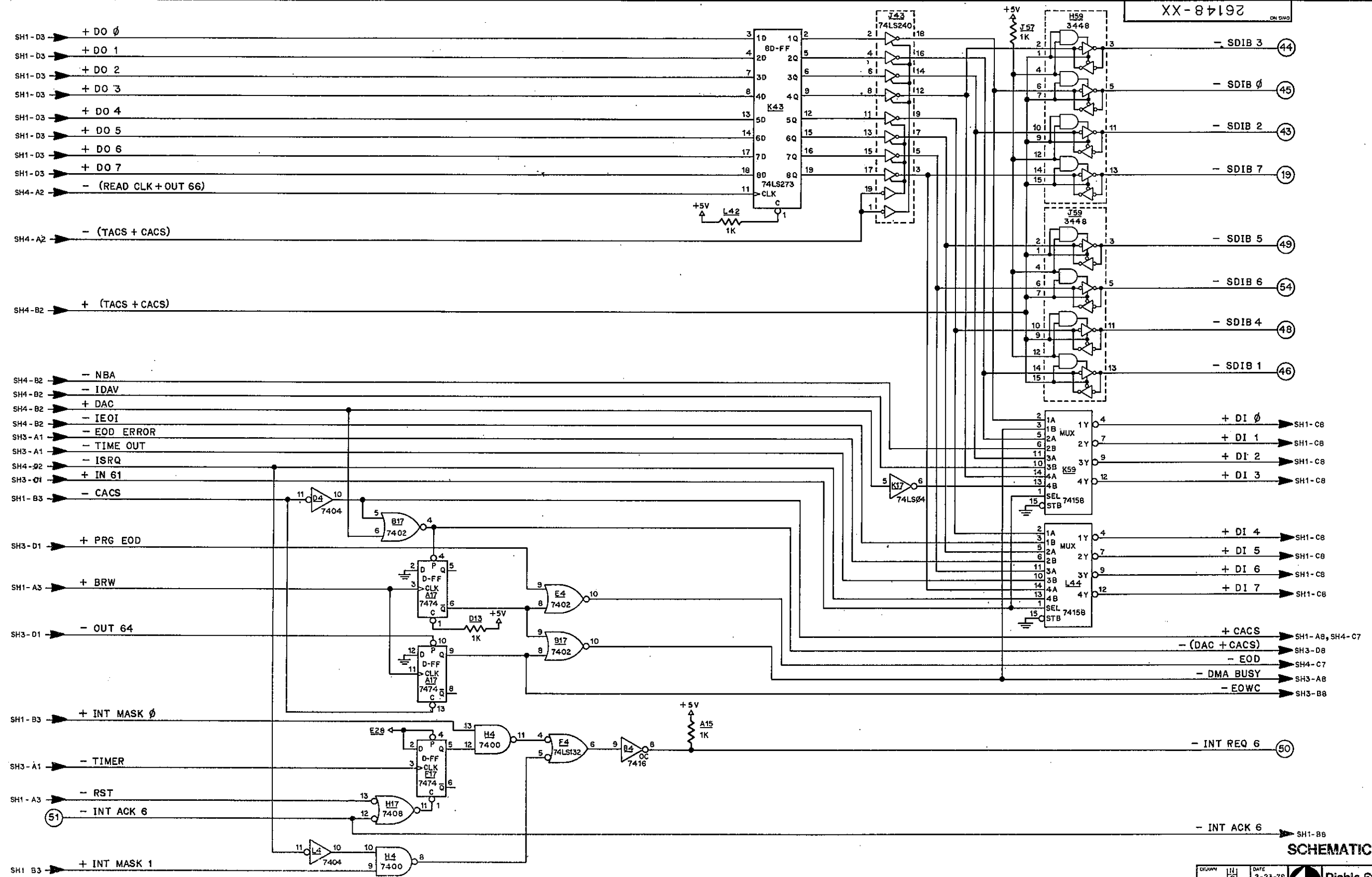
- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5 %.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
+5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8;
+5V TO PIN 20 OF 20-PIN IC, GND TO PIN 10.
 5. FOR USAGE OF H63 D70 AND F63 REFER TO PARTS LIST.



SCHEMATIC DIAGRAM [PWA]

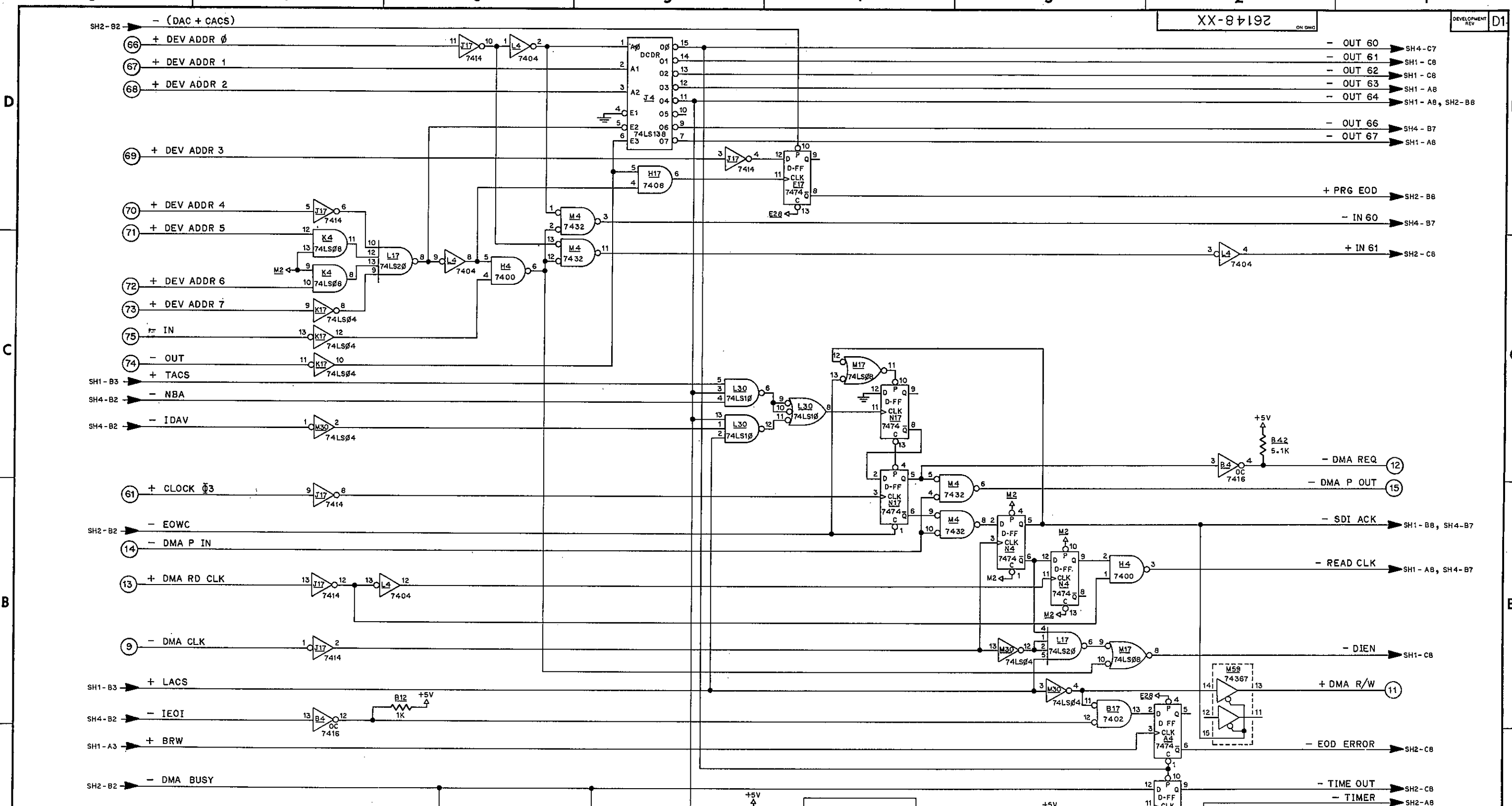
DRAWN	INT	DATE	Diablo Systems Incorporated Hayward California 94545
3-2-78	3-8-78	2-21-78	
CHECKED	DATE	DATE	THESE DRAWINGS AND/OR SPECIFICATIONS AND THE DATA CONTAINED THEREIN ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT BE REPRODUCED OR TRANSMITTED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS INC. OR SHOWN OR REPRODUCED IN ANY MANNER TO ANY OTHER PERSON OR ENTITY WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS INC. EVEN IF THE REPRODUCTION IS FOR ANY PURPOSE WHATSOEVER.
S.C. ZIMCO	3-8-78	3/1/78	
ENGR	DATE	DATE	FIRST USED INERT ASSY: 400312-XX
R. SWAN	3/1/78	3/1/78	
DATE	DATE	DATE	PW ASSY, EXDMA
S. FRATER	3/1/78	3/1/78	

26148-XX ON DWG



SCHEMATIC DIAGRAM

DRAWN	IC	DATE	2-23-78	Diablo Systems Incorporated Hayward, California 94545					
CHECKED	S. Camic	DATE	3-8-78						
ENGR	R. Swann	DATE	1/1/77	<small>THESE DRAWINGS AND/OR SPECIFICATIONS AND THE DATA CONTAINED THEREIN ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</small>					
APP'D	S. PRATER	DATE	3/7/78						
FIRST USED INERT ASSEMBLY			400312-XX	PW ASSY, EXDMA					
SHEET			2	TOT SHEET	11	SIZE	D	DWG NO.	26148-XX



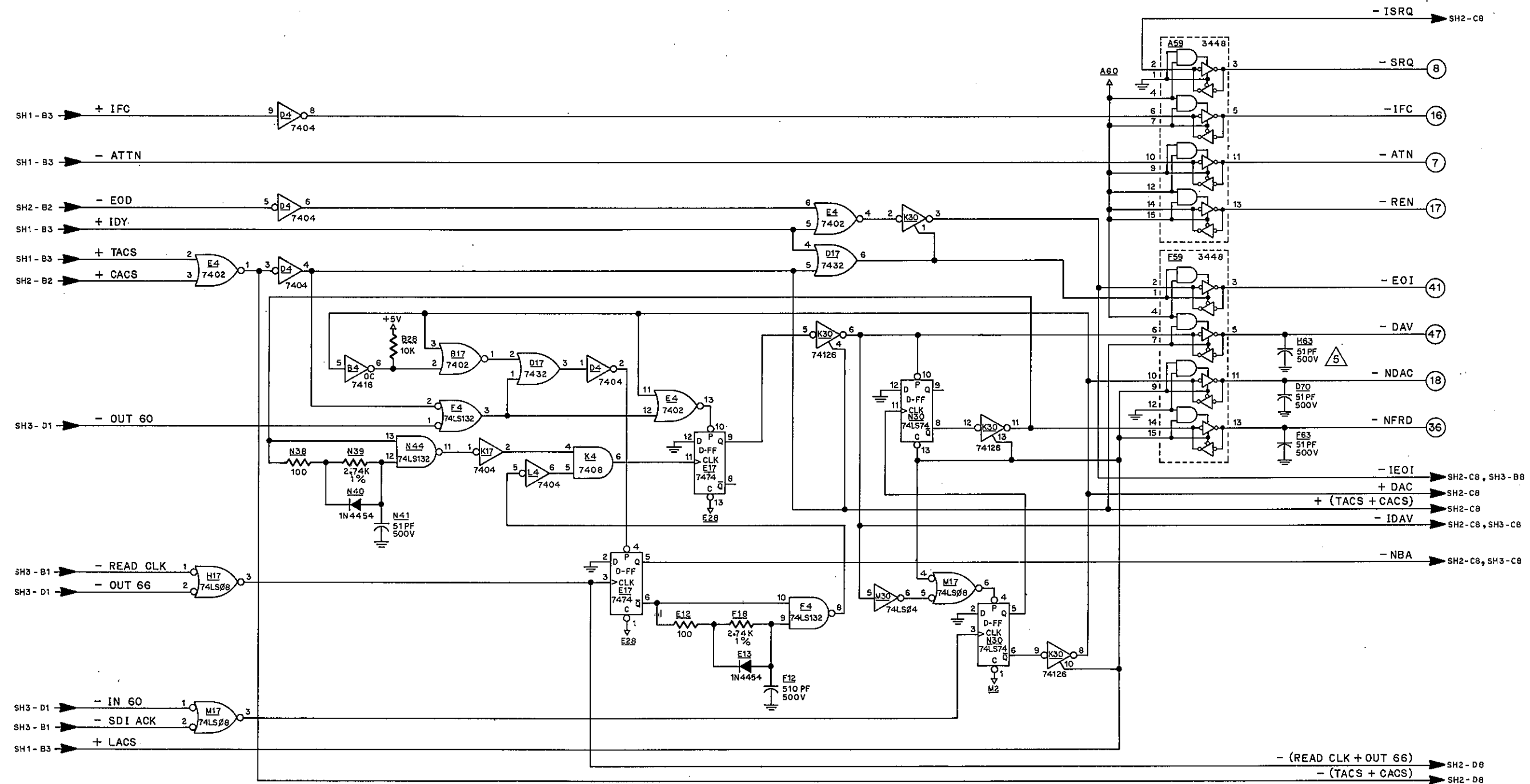
SCHEMATIC DIAGRAM

DRAWN	N C	DATE	2-28-78
CHECKED	S Camic	DATE	3-8-78
ENGR	A Jensen	DATE	3/1/78
MFG	S FRATEL	DATE	3/9/78

Diablo Systems Incorporated
Hayward, California 94545

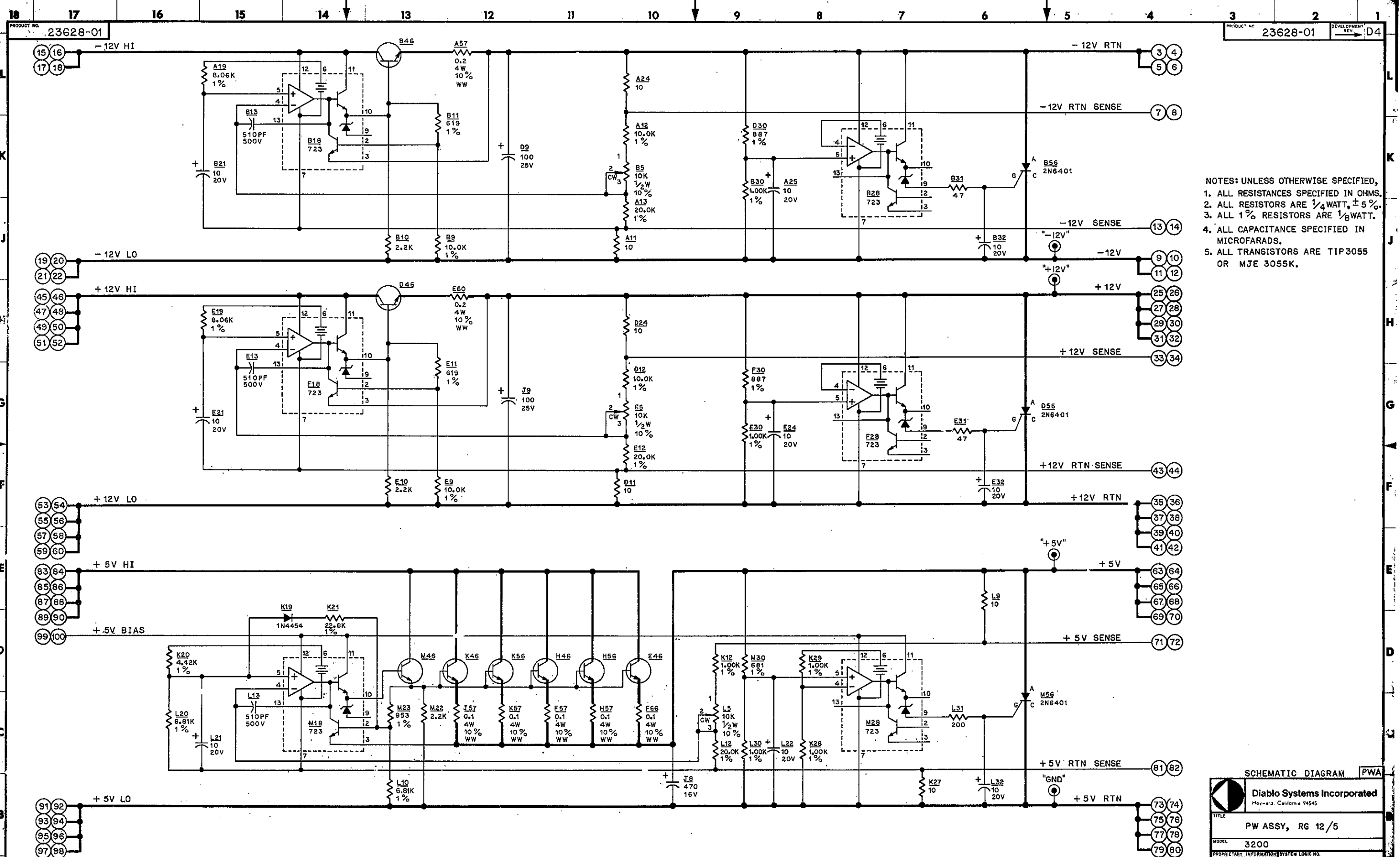
THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.

FIRST USED (NEXT ASSY):	400312-XX	PW ASSY, EXDMA	
SHEET	3	TOT SHEET	11
SIZE	D	DWG NO	26148-XX



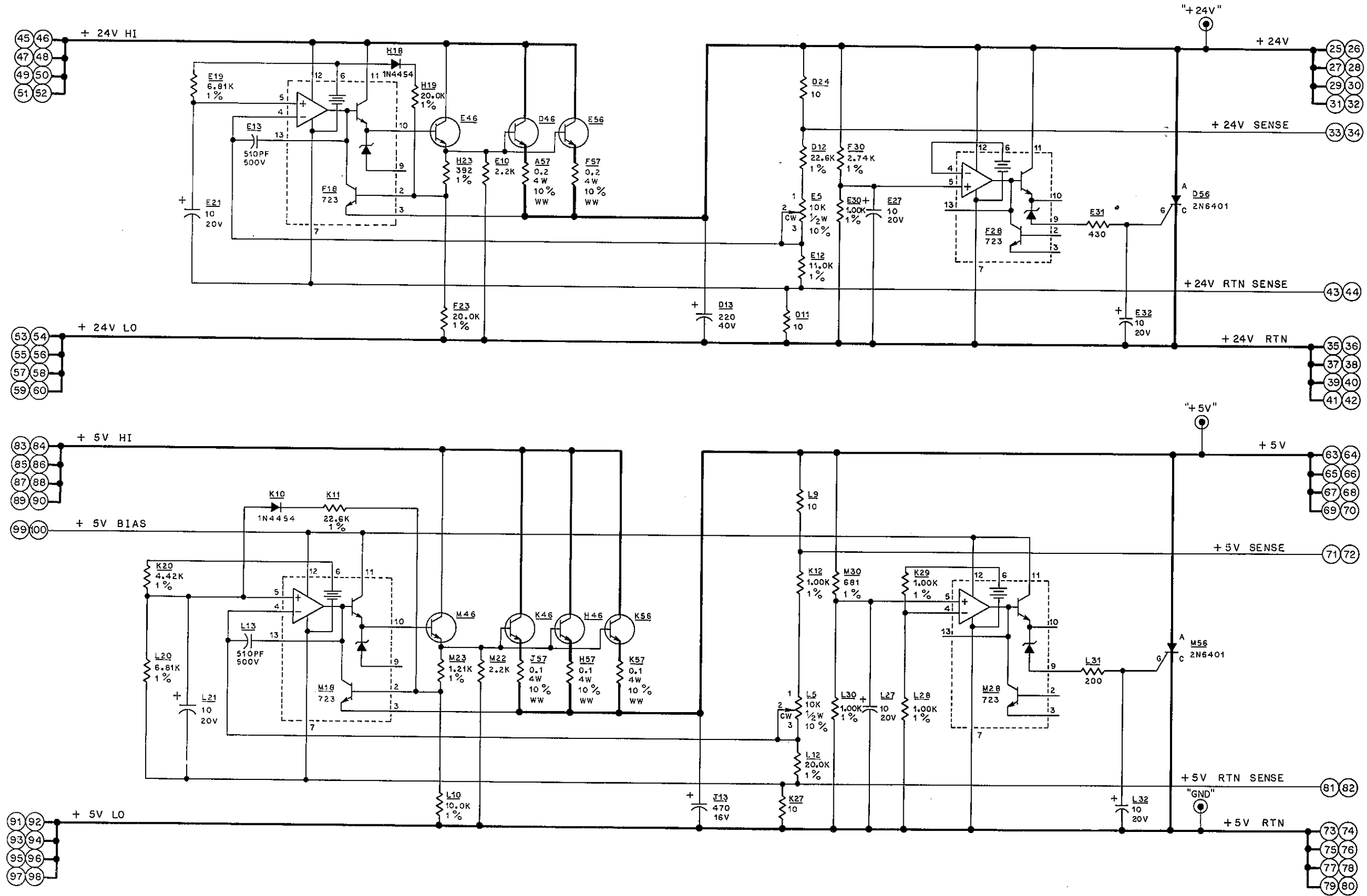
SCHEMATIC DIAGRAM

DRAWN		DATE		Diablo Systems Incorporated Hayward, California 94545	
CHECKED	3.0/2/mie	DATE	3-8-78	THESE DRAWINGS AND/OR SPECIFICATIONS AND THE DATA CONTAINED THEREIN ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT BE DISCLOSED OR REPRODUCED IN ANY MANNER BY ANY PERSON OR BE LOANED, REPRODUCED OR USED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. EXCEPT FOR THE MANUFACTURE OF ARTICLES FOR WHICH THEY WERE DESIGNED.	
ENGR	R. J. ...	DATE	3/8/78	FIRST USED IN NEXT ASSY	
WFO	S. FEATER	DATE	3/9/78	400312-XX	PW ASSY, EXDMA
SHEET 4 OF 4				4	11
				D	26148-XX



- NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ±5%.
 3. ALL 1% RESISTORS ARE 1/8 WATT.
 4. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 5. ALL TRANSISTORS ARE TIP3055 OR MJE 3055K.

SCHEMATIC DIAGRAM PWA
 Diablo Systems Incorporated
 Hayward, California 94545
 TITLE PW ASSY, RG 12/5
 MODEL 3200
 PROPRIETARY INFORMATION SYSTEM LOGIC NO. UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.
 SHEET TOTAL SHEETS SIZE PRODUCT NO. 23628-01

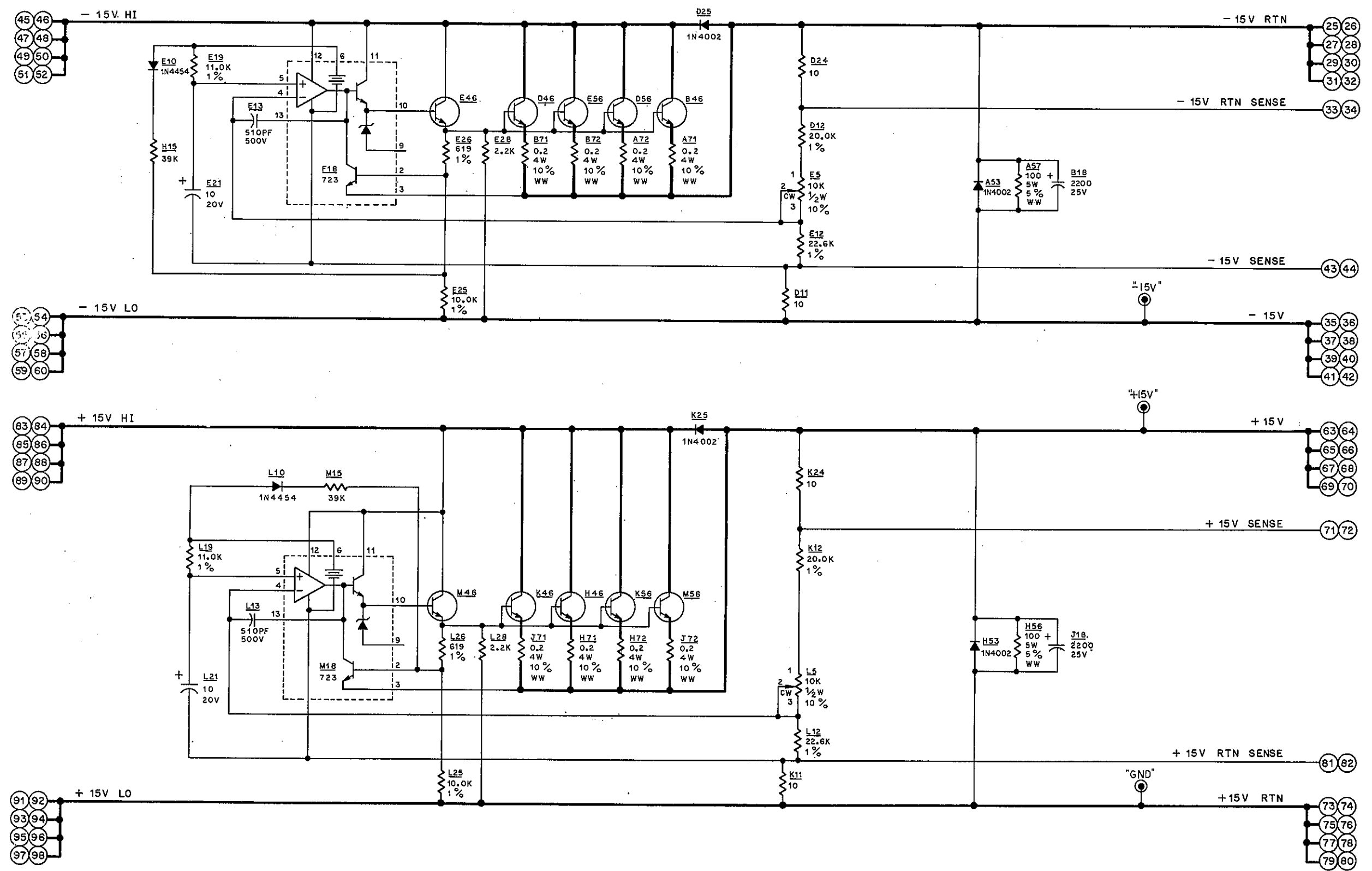


- NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL 1% RESISTORS ARE 1/8 WATT.
 4. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 5. ALL TRANSISTORS ARE TIP 3055 OR MJE 3055K.

SCHMATIC DIAGRAM PWA
 Diablo Systems Incorporated
 PW ASSY, RG 24/5
 3200
 9/27/76
 1 THRU 1
 1 7 D
 23626-01

PRODUCT NO. 23624-01

23624-01 DEVELOPMENT REV. D4



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL 1% RESISTORS ARE 1/8 WATT.
 4. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 5. ALL TRANSISTORS ARE TIP 3055 OR MJE 3055K.

SCHMATIC DIAGRAM PWA

Diablo Systems Incorporated
Hayward, California 94545

TITLE: PW ASSY, RG 15V

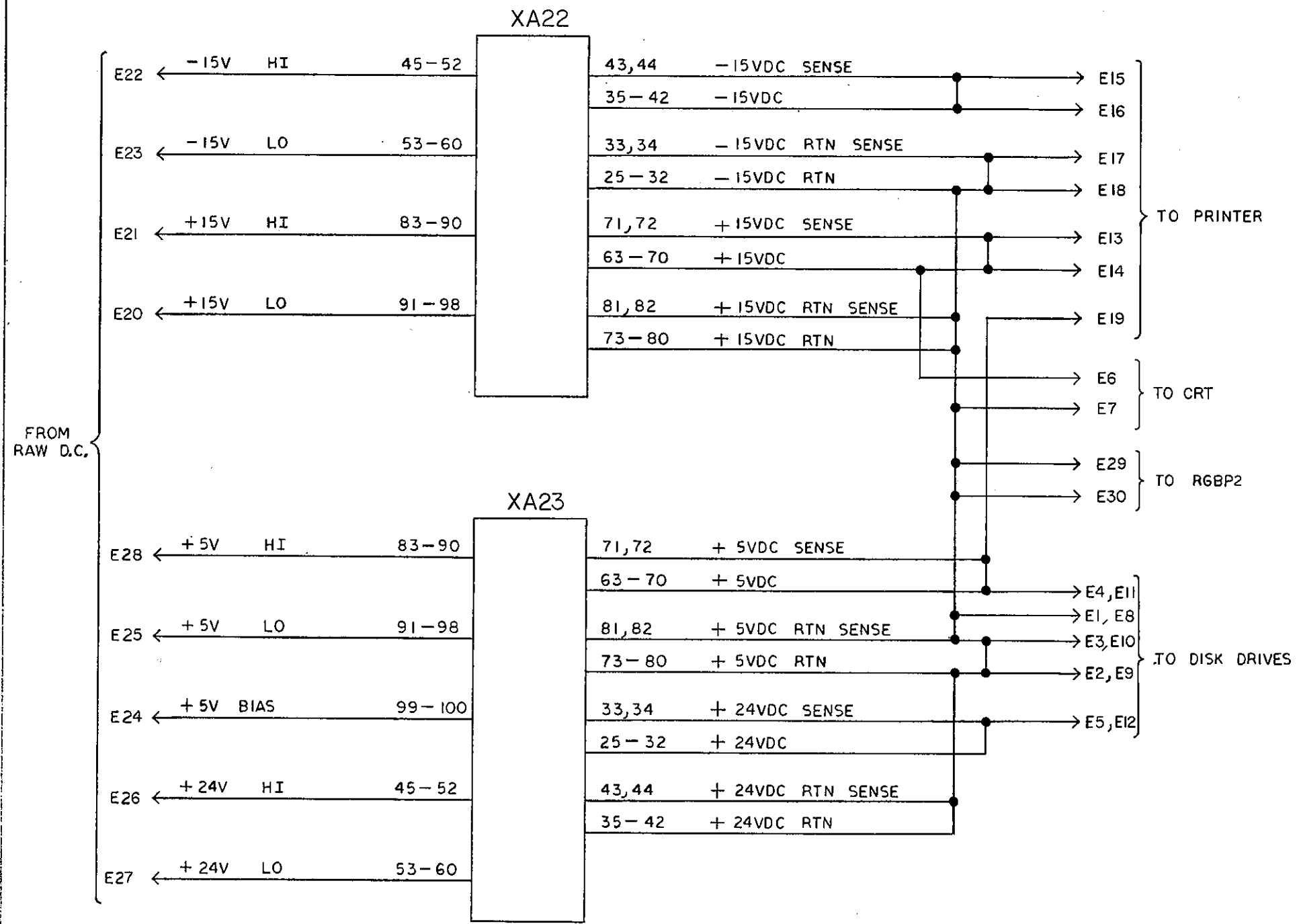
MODEL: 3200

UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.

SYSTEM LOGIC NO.

SHEET TOTAL SHEETS SIZE PRODUCT NO.

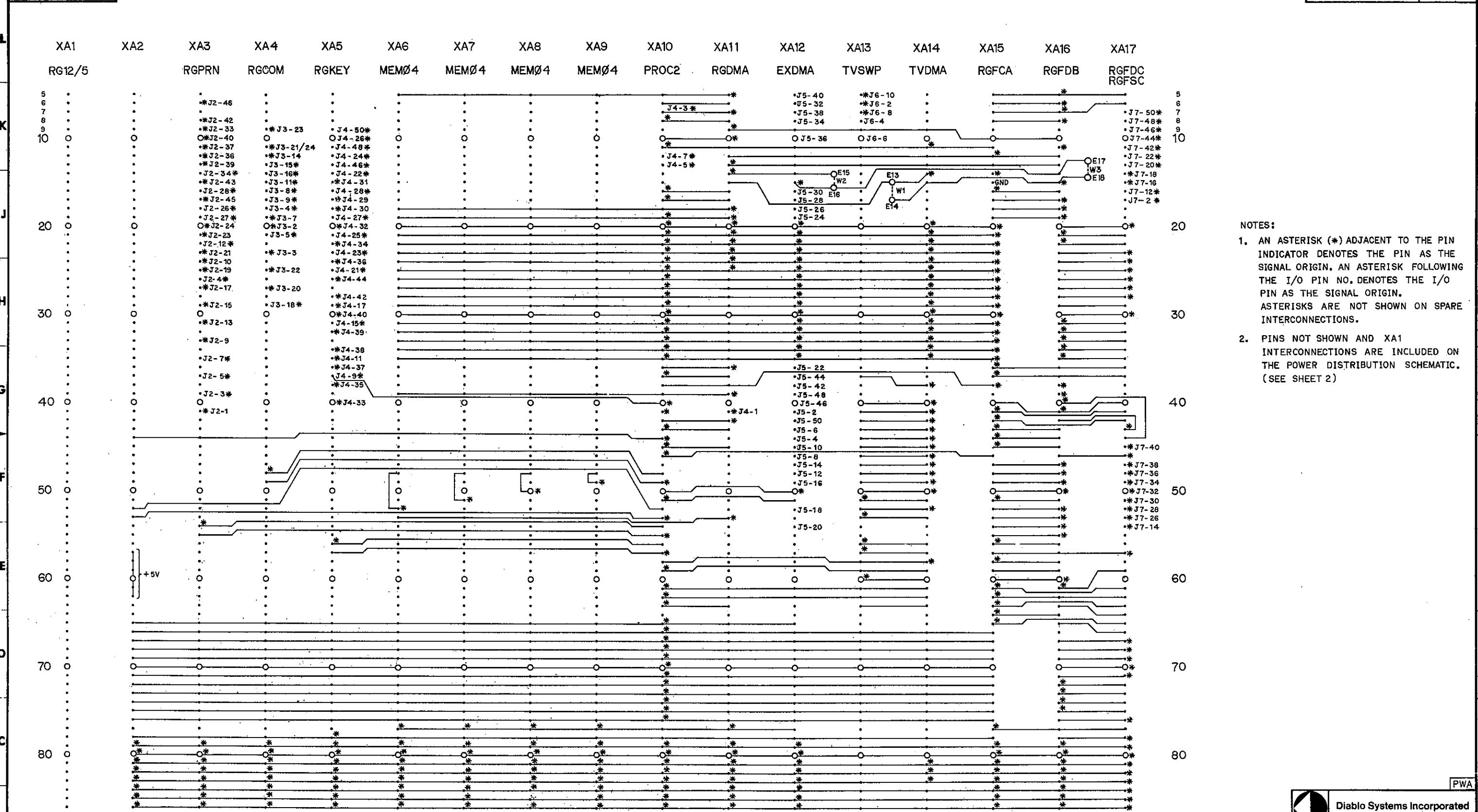
1 THRU 1 .1 6 D 23624-01



26146-01

J K G H E F C D A 7760 B 22 APR 77 REVISIONS		DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED TOLERANCES ARE LINEAR .005 ANGULAR .015 MATERIAL HEAT TREAT	DRAWN G. W. DIE CHECKED D. A. ... ENGR MFG S. FRATER DATE 24 MAR 77 4/17/77 22 APR 77 4/22/77	PWA Diablo Systems Incorporated PWA, RGN2 POWER DISTRIBUTION SCALE SHEET 1 OF 3 SIZE C 26146-01
--	--	---	--	--

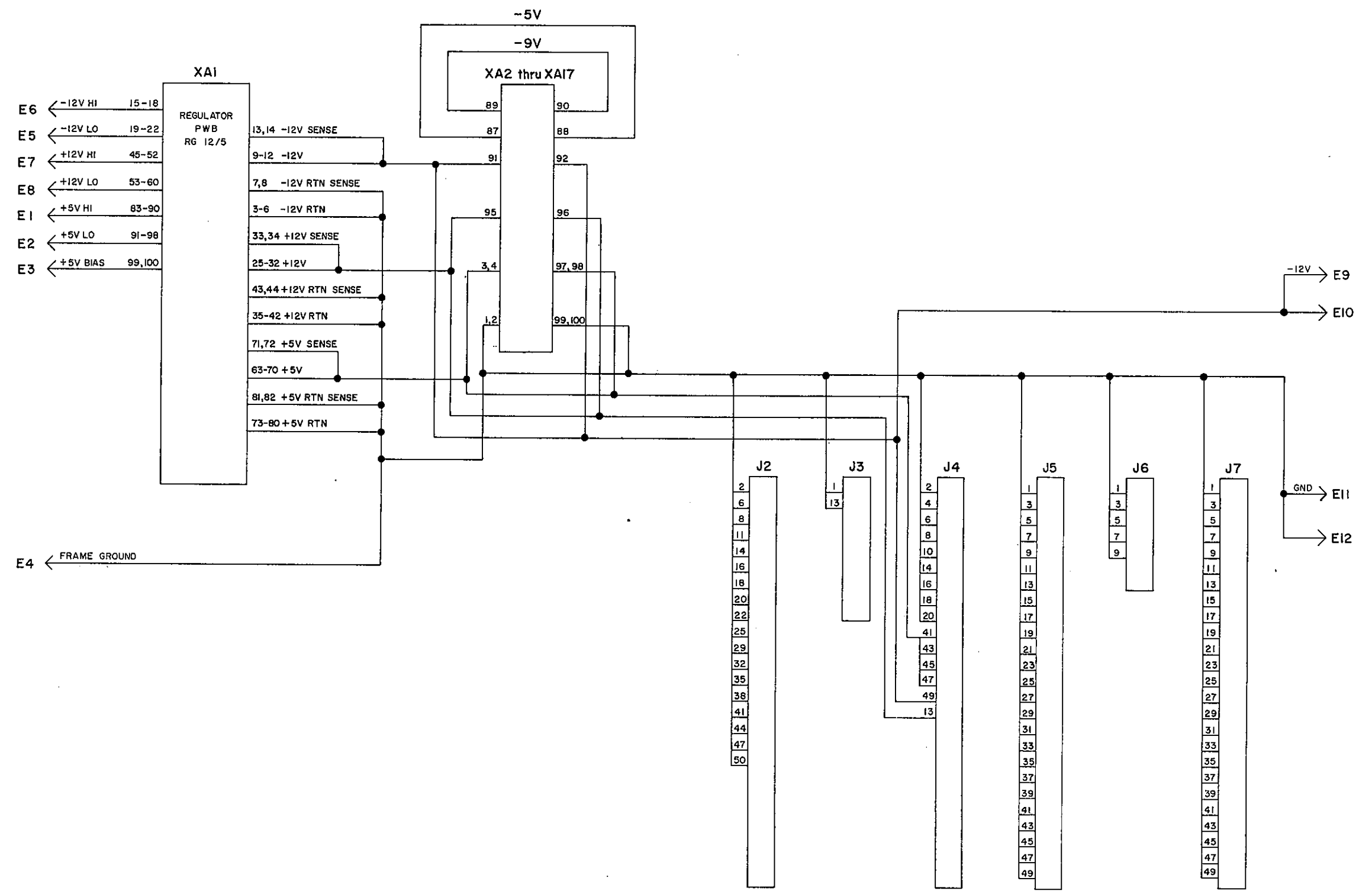
NEXT ASSY. 26250



- NOTES:
1. AN ASTERISK (*) ADJACENT TO THE PIN INDICATOR DENOTES THE PIN AS THE SIGNAL ORIGIN. AN ASTERISK FOLLOWING THE I/O PIN NO. DENOTES THE I/O PIN AS THE SIGNAL ORIGIN. ASTERISKS ARE NOT SHOWN ON SPARE INTERCONNECTIONS.
 2. PINS NOT SHOWN AND XA1 INTERCONNECTIONS ARE INCLUDED ON THE POWER DISTRIBUTION SCHEMATIC. (SEE SHEET 2)

Diablo Systems Incorporated
 Hayward, California 94545

TITLE: PWA, RGPB2, WIRING DIAGRAM
 MODEL: 3200
 PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED.
 SYSTEM LOGIC NO. 17 D
 SHEET TOTAL SHEETS 17 SIZE D PRODUCT NO. 26142-XX



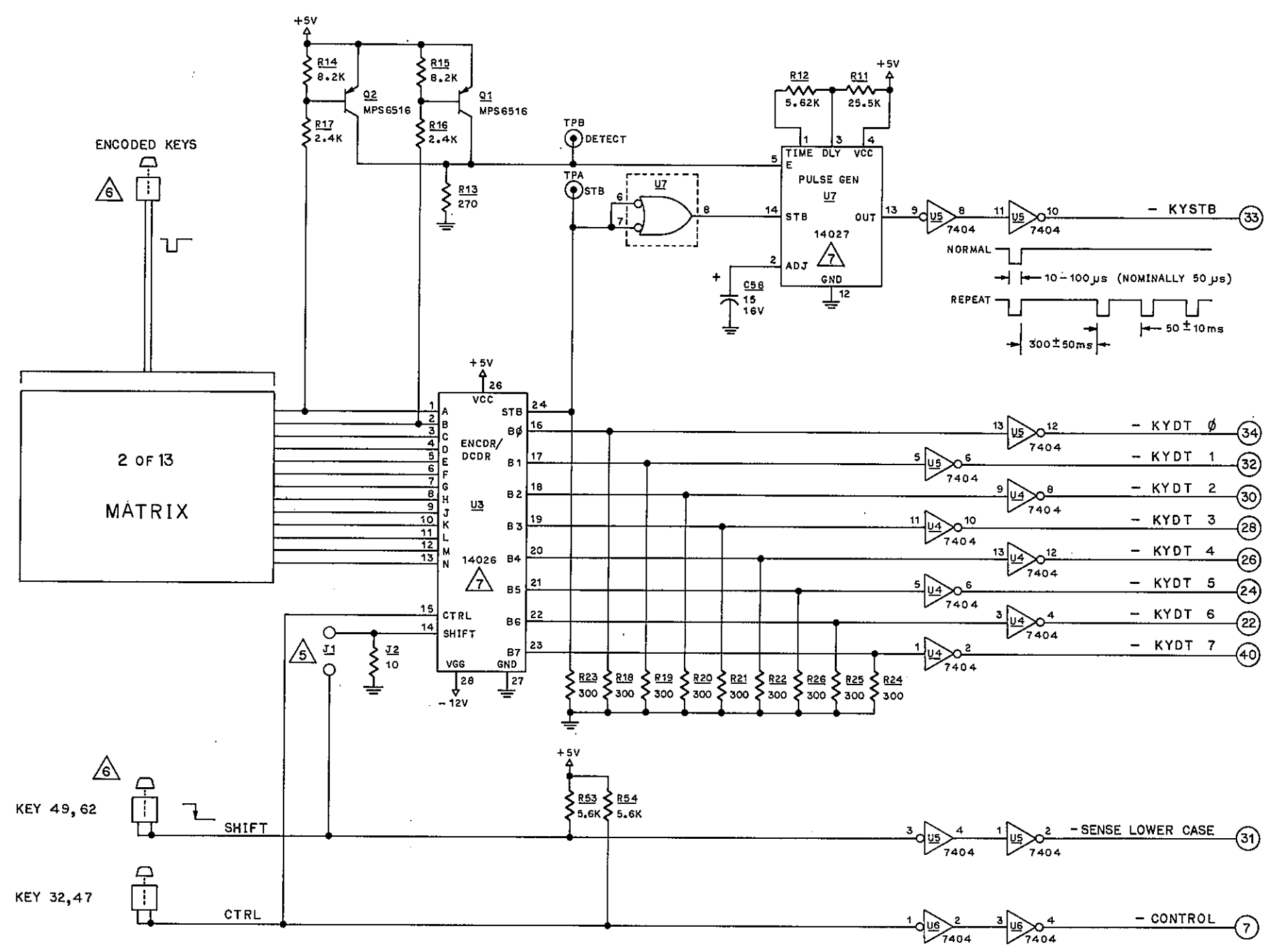
SCHEMATIC DIAGRAM

DRAWN ED A.	DATE 4-5-76		Diablo Systems Incorporated Hayward, California 94545
CHECKED D. K. Long	DATE 4/7/76		
ENGR S. F. White	DATE 2/2/76	<small>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</small>	
MFG	DATE		
FIRST USED (NEXT ASSY) 26250	SIZE D	PWA, RGBP2 POWER DISTRIBUTION	
SHEET 2	TOT SHT 7	SIZE D	DWG NO 26142-XX

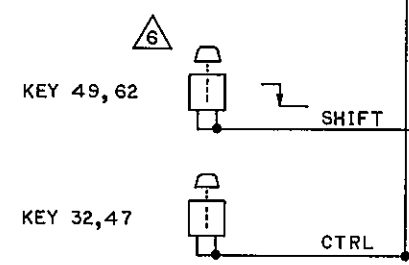
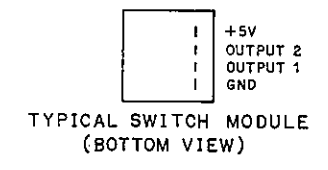
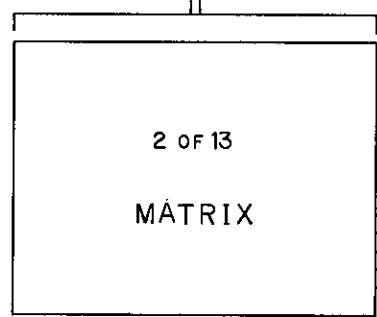
F	A4160 6-26-78	E	A4105 5-18-78	D	7976 1-3-77	C1	7940 1/10/77	C	7885 10-14-77	B	7778 6-8-77	REV	L.C.O. NO. A 7760
---	------------------	---	------------------	---	----------------	----	-----------------	---	------------------	---	----------------	-----	----------------------

PRODUCT NO. 26039

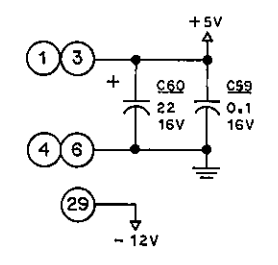
PRODUCT NO. 26039 DEVELOPMENT REV. D1



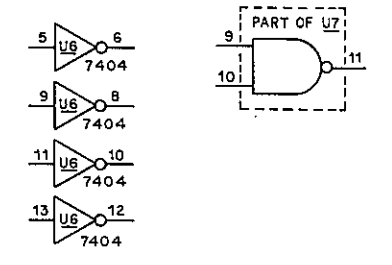
ENCODED KEYS



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7.
 5. J1 IS NOT INSTALLED.
 6. SOLID-STATE SWITCH MODULES.
 7. DIABLO PART NUMBER.



SPARES:



Diablo Systems Incorporated
Hayward, California 94545

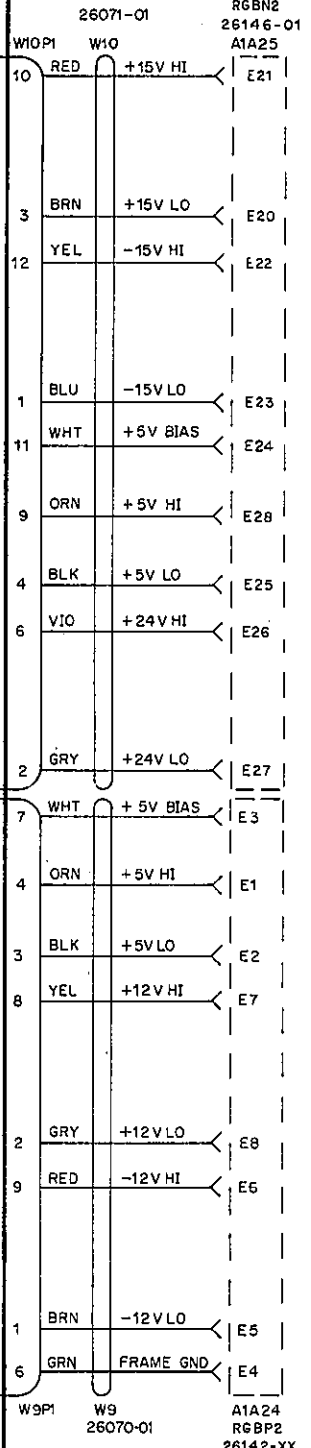
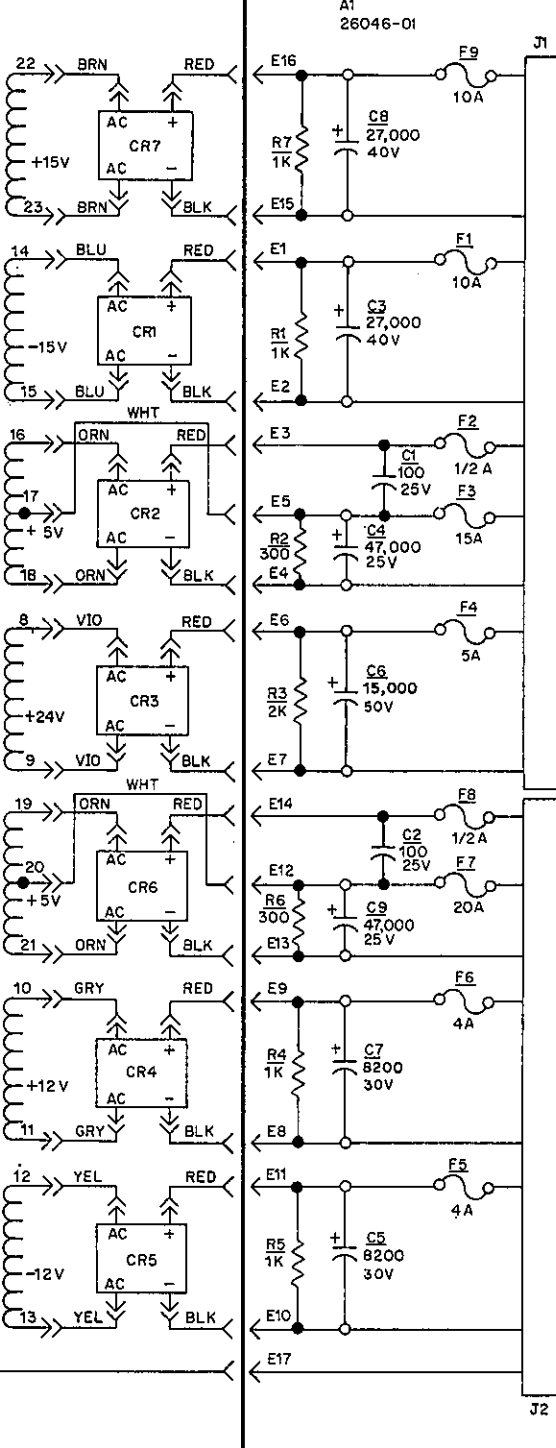
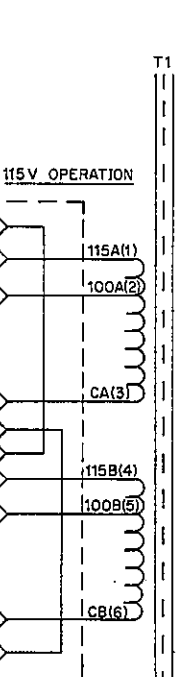
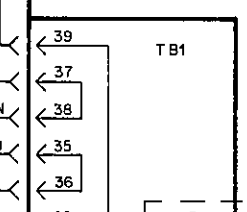
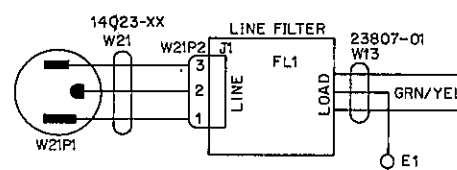
TITLE: **KEYBOARD, TYPEWRITER PAIRED, SCHEMATIC**

MODEL: **SYSTEM 3200**

PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED

SHEET TOTAL SHEETS SIZE PRODUCT NO. 26039

POWER SUPPLY ASSY
A6
23770-XX



- NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. ALL FUSES ARE SLOW-BLOW TYPE.
- 5 OPERATION FOR 115V @ 60 Hz SHOWN. FOR 200-230V @ 50 Hz OPERATION, FUSE A6F1 IS CHANGED TO 6.25A, FUSE A10F1 IS CHANGED TO 2A, A11P1 IS CONNECTED TO A10J2 AND CONNECTIONS TO TERMINAL BLOCK A6TB1 ARE CHANGED AS SHOWN. NO CHANGES ARE REQUIRED IN THE PRIMARY LEADS OF THE TRANSFORMER A6T1, OR IN THE AC CONNECTIONS OF THE DISK DRIVES, CRT OR FANS.
- 6 OPTIONAL DISK DRIVE.
- 7 ASSEMBLIES 26204-01 AND 26219-01 ARE PART OF THE MATRIX PRINTER OPTION (26210-XX).
- 8 SEE SHEET 2 FOR A DETAILED DRAWING OF THE 48V POWER SUPPLY.
- 9 THE JUMPER BETWEEN A6TB1. 33 AND 34. IS A SPARE JUMPER AND IS USED WHEN CONVERTING A 200V-230V MACHINE TO 115V.
- 10 ANY ONE UNIT MUST USE ALL THE SAME TYPE OF DISKETTE DRIVES.

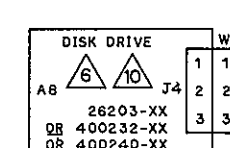
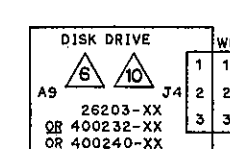
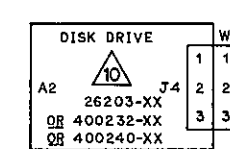
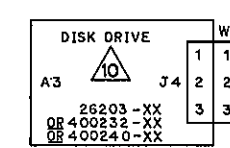
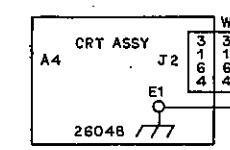
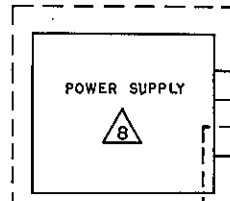


TABLE 1

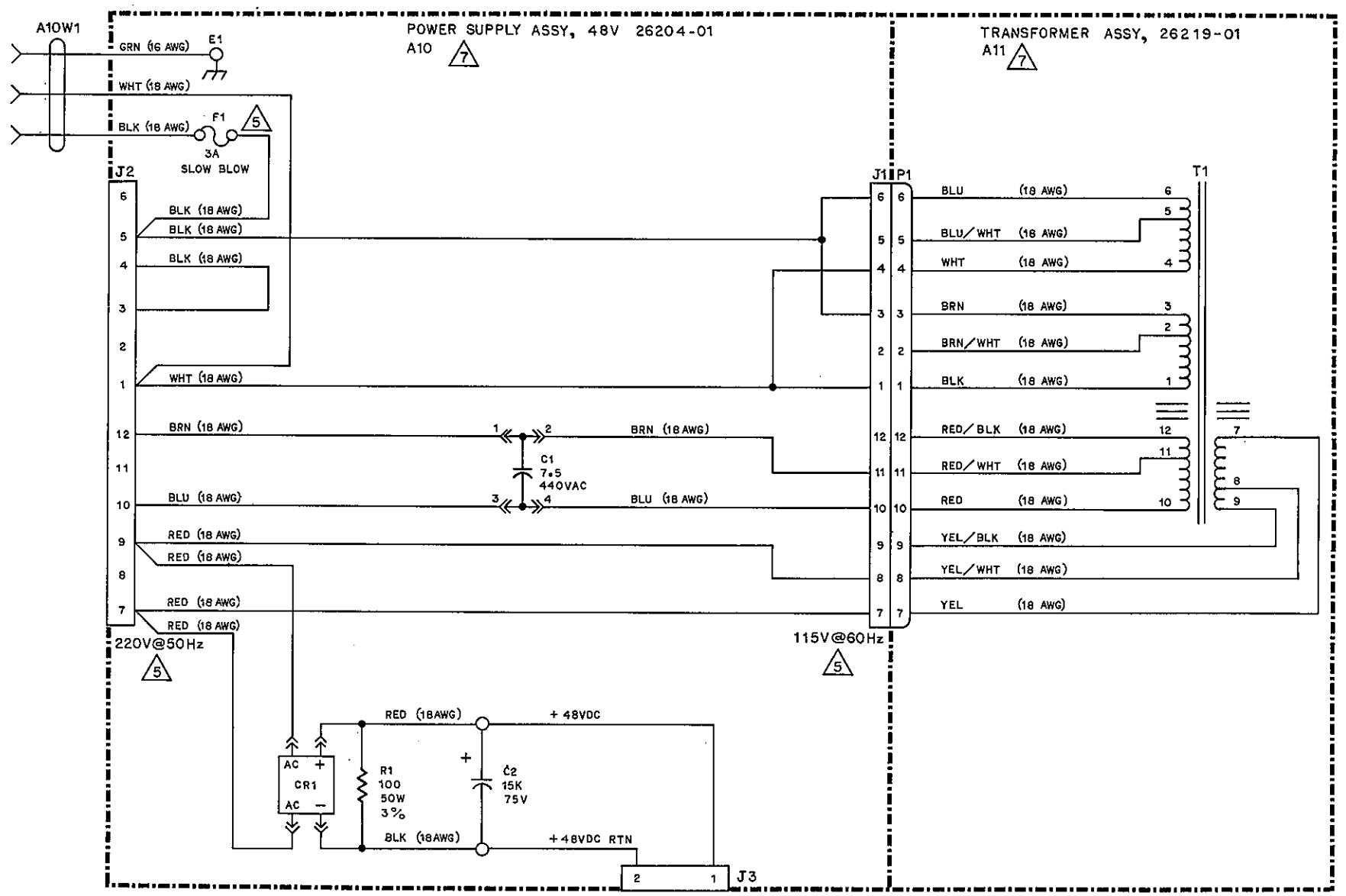
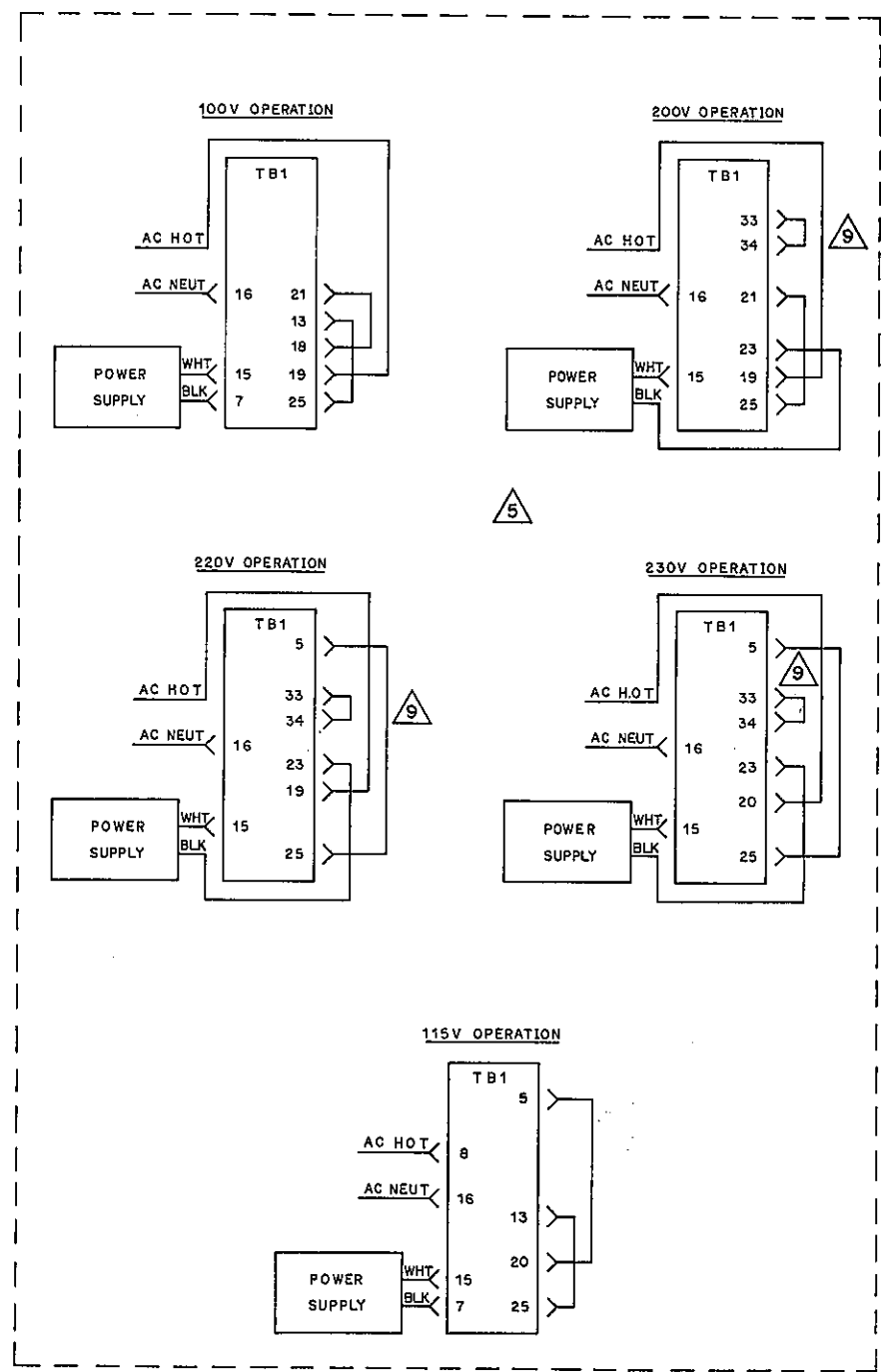
FLOPPY DISK DRIVES A2, A3, A8, A9	AC CABLE W5, W8
26203-XX	26212-01
400232-XX	26212-01
400240-XX	400179-01

SEE TABLE 1

SEE TABLE 1

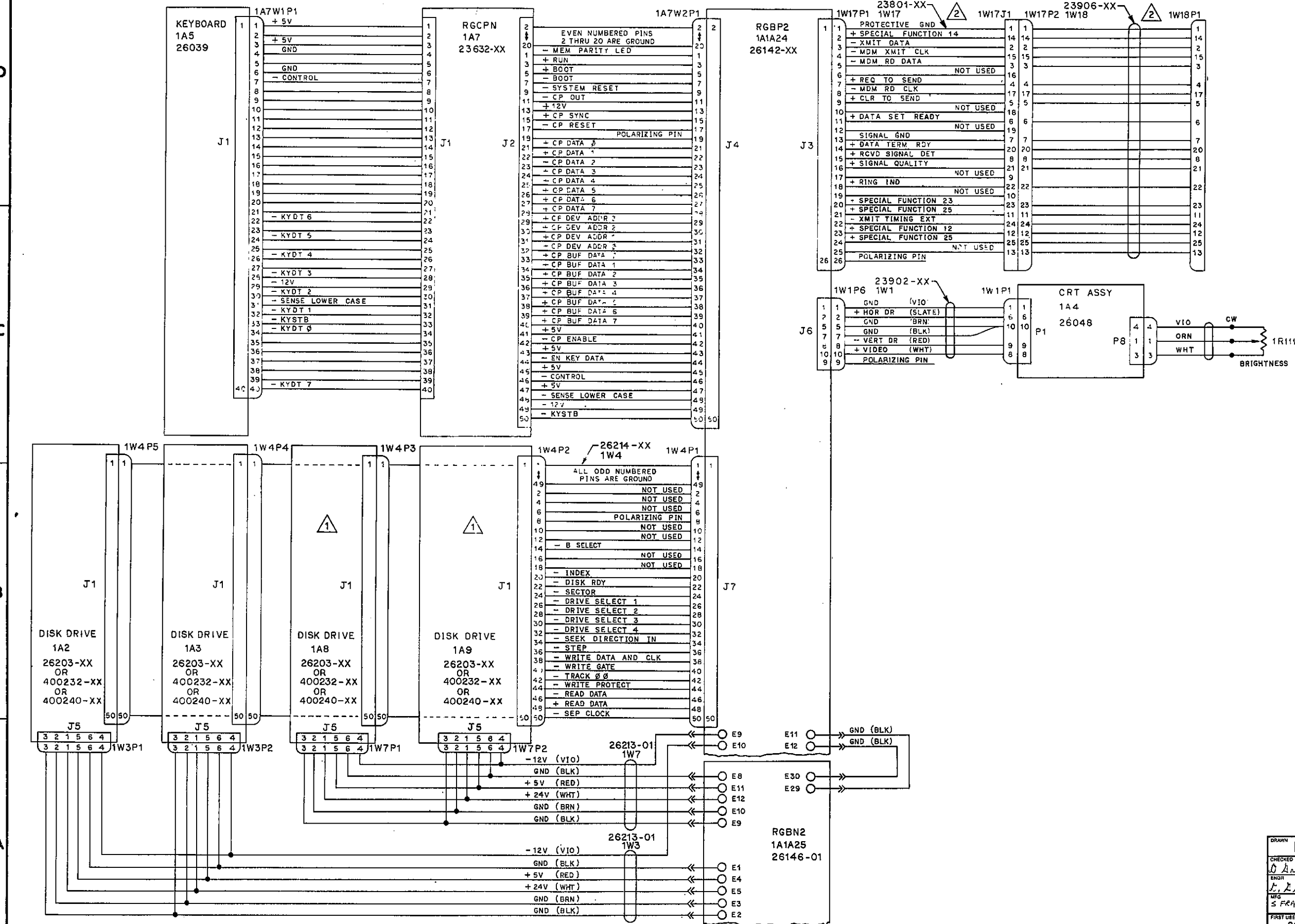
SCHEMATIC DIAGRAM

DRAWN N. HOLLANDER	DATE 4-27-77		Diablo Systems Incorporated Hayward, California 94545
CHECKED	DATE		
ENG	DATE	THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.	
MFG	DATE		
FIRST USED (NEXT ASSY) 26250-XX		SCHEMATIC, POWER DISTRIBUTION	
SHEET 1	TOT SHT 2	SIZE D	DWG NO 400112-01



SCHEMATIC DIAGRAM

DRAWN N C	DATE 4-29-77	<p>Diablo Systems Incorporated Hayward, California 94545</p>
CHECKED D. D. Amberg	DATE 5/12/77	
ENGR D. D. Amberg	DATE 5/12/77	<p>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON OR BY COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</p>
MFG S. PRATER	DATE 5/12/77	
FIRST USED IN EXT ASSY 26250-XX		SCHEMATIC, POWER DISTRIBUTION
SHEET 2	TOT SHEET 2	SIZE D
DWG NO 400112-01		

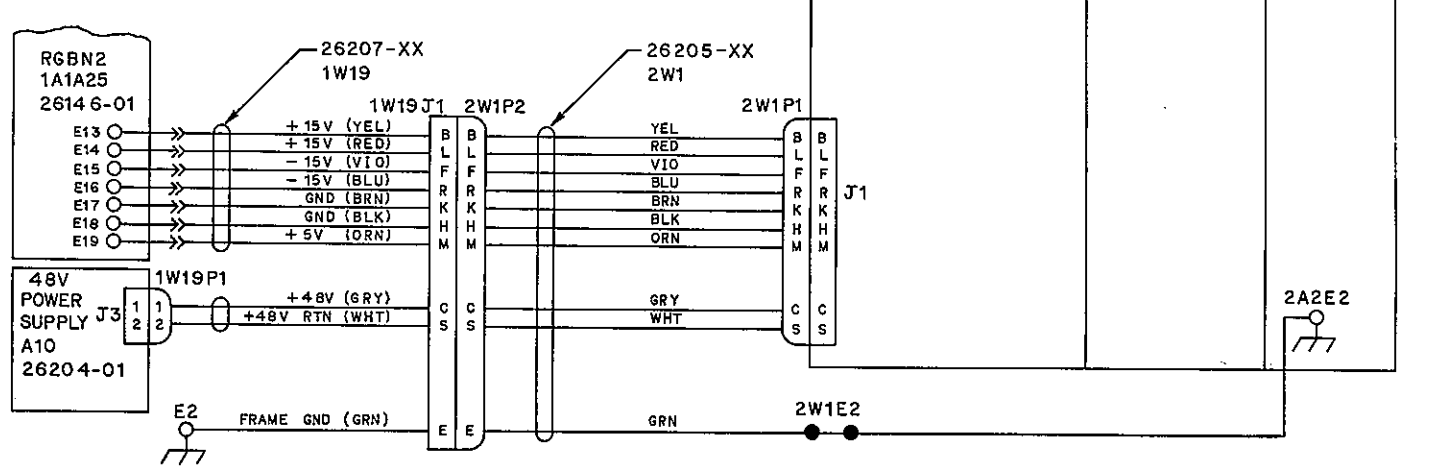
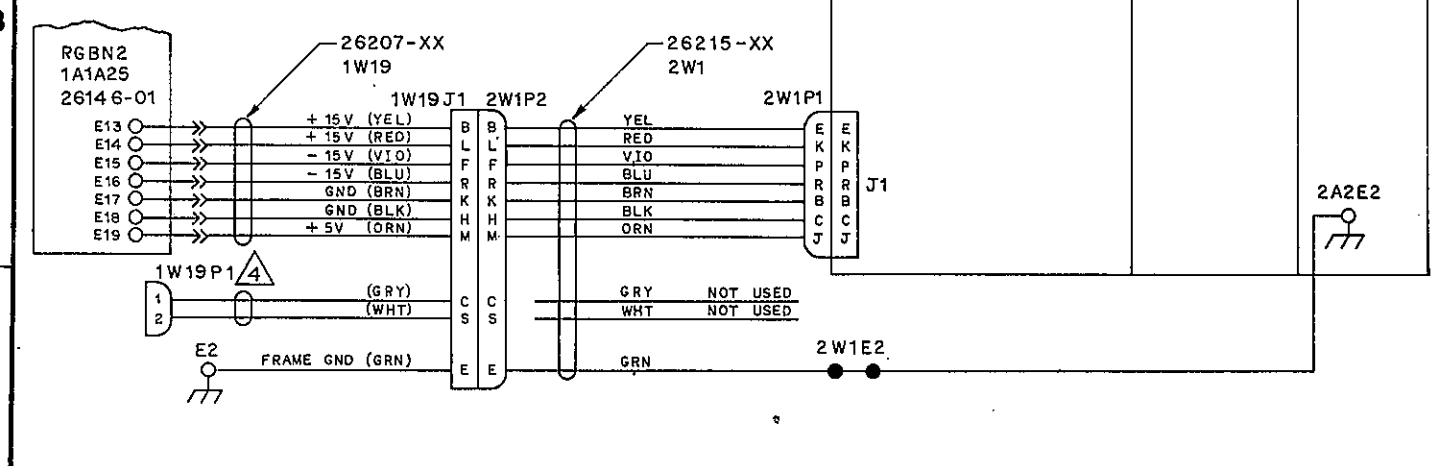
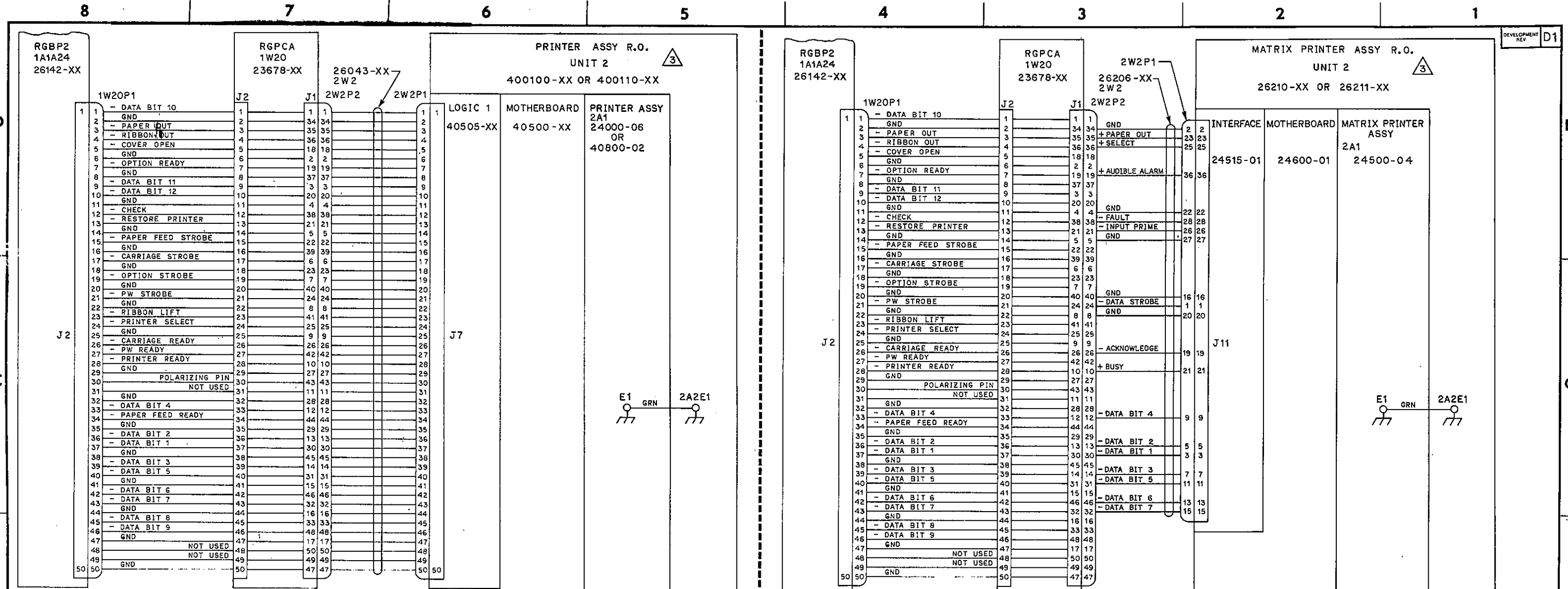


NOTES:

- 1 OPTIONAL DISK DRIVE.
- 2 CABLES 23901-XX AND 23906-XX ARE PART OF THE EIA OPTION (26094-XX).
- 3 THIS R.O. PRINTER ASSY (UNIT 2) AND ASSOCIATED CABLES ARE AN OPTION.
- 4 IN THIS CONFIGURATION THIS CONNECTOR (1W19P1) IS NOT USED.
- 5 THIS DISK DRIVE ASSY (UNIT 3) AND ASSOCIATED CABLES ARE AN OPTION.
- 6 PWA'S 400136-XX AND 400134-XX ARE A PART OF THE MULTICOM OPTION (400227-XX).
- 7 CABLE ASSY 400174-XX IS USED WITH PWA 26148-01, WHICH ARE PARTS OF THE IEEE 488 OPTION (400132-XX).

DRAWN LH	DATE 5-3-77		Diablo Systems Incorporated Hayward, California 94545
CHECKED L. L. ...	DATE 5/10/77		
ENGR L. L. ...	DATE 5/10/77	THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.	
REV S. F. ...	DATE 2/1/77	FIRST USED IN EXT ASSY 26250-XX	
SHEET 1		TOTAL SHEETS 3	
SIZE D		DWG NO 400113-XX	

REV 2/22/78
A
E.C.O. NO. 7769
5-11-77

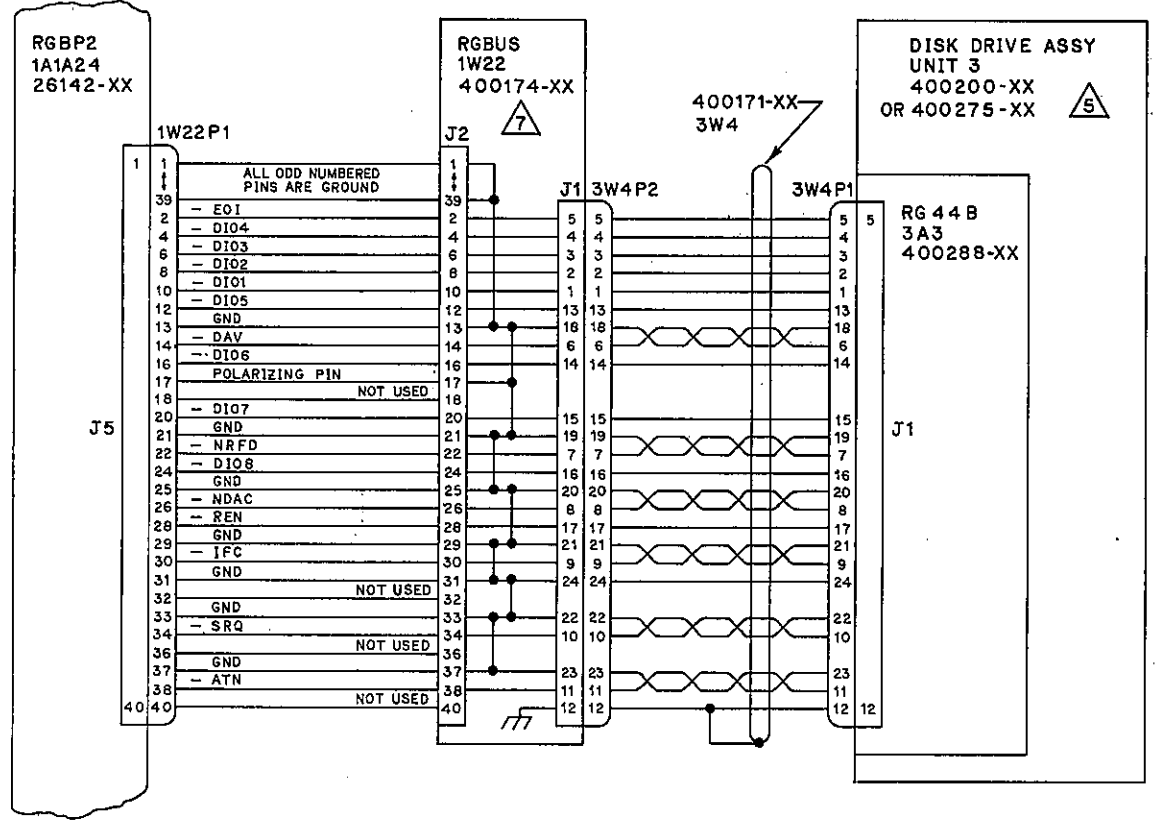


400113-01
HyTYPE II PRINTER

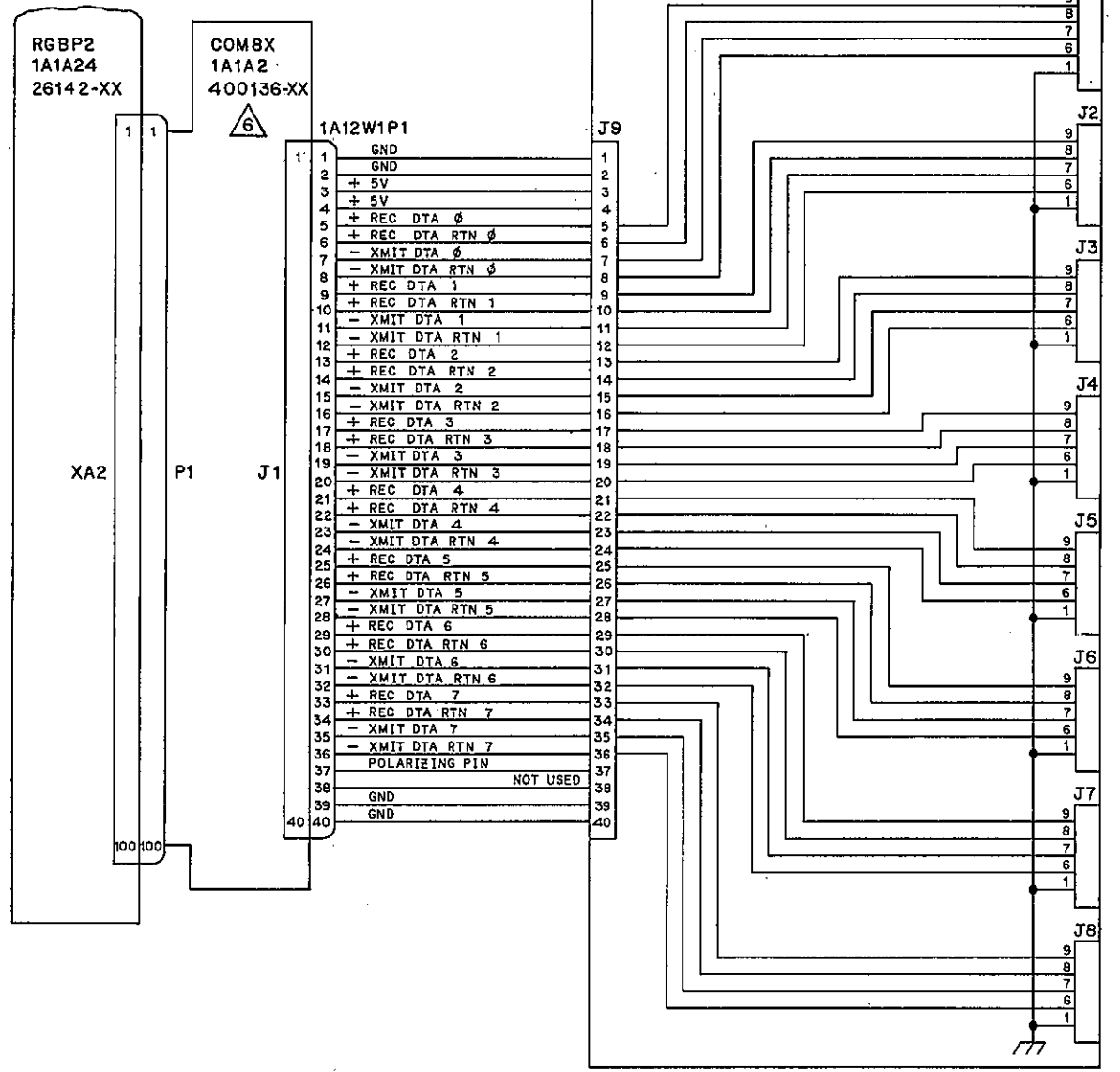
400113-02
MATRIX PRINTER

DRAWN N1	DATE 5-6-77		Diablo Systems Incorporated Hayward, California 94545
CHECKED D. K. ...	DATE 5/10/77		
ENGR D. K. ...	DATE 5/10/77	THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.	
MFG S. FEATER	DATE 5/10/77	FIRST USED NEXT ASSY: 26250-XX INTERCONNECTION DIAGRAM SIGNAL AND DC POWER CABLES	
SHEET 2	TOT SHEET 3	SIZE D	DWG NO 400113-XX

AA02B REV 2/21/78 E.C.O. NO. 7764 5-11-77

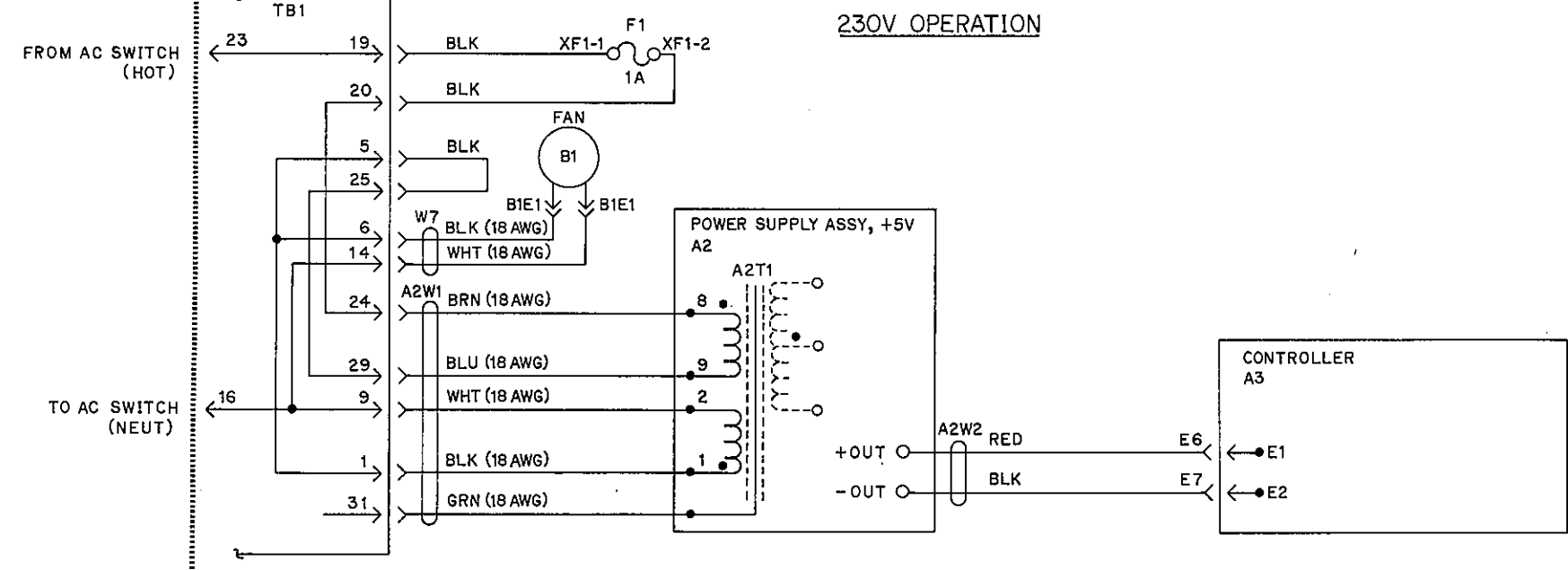
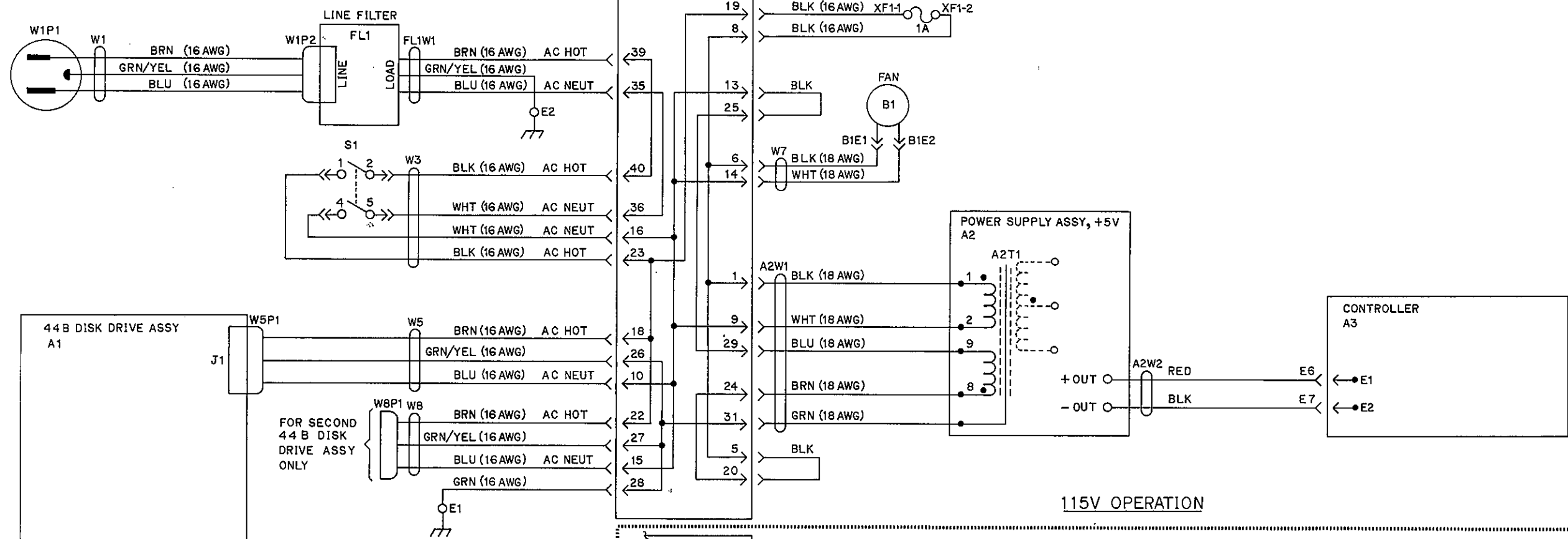


400113-03
44B DISK DRIVE



400113-04
MULTICOM WITH CURRENT LOOP

J	K	DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED TOLERANCES ARE LINEAR . ANGLER MATERIAL HEAT TREAT	DRAWN N/C CHECKED ENGR MFG	DATE 12-14-77 2-22-78 2/21/78 2/23/78	Diablo Systems Incorporated INTERCONNECTION DIAGRAM SIGNAL AND DC POWER CABLES SCALE SHEET 3 TOTAL 3 SIZE D 400113-XX
G	H		CONFIDENTIAL PROPRIETARY DATA THIS DRAWING AND ALL INFORMATION CONTAINED HEREIN ARE THE PROPERTY OF DIABLO SYSTEMS INC. NO PART OF THIS DRAWING OR INFORMATION CONTAINED HEREIN IS TO BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS INC.	S. FRATER 2/23/78	
E	F		REVISIONS A B A/D/B 2/23/78	PROPRIETARY INFORMATION UNAUTHORIZED DISCLOSURE OR USE PROHIBITED	
C	D		FIRST USED 26250-XX	DISCLOSURE OR USE PROHIBITED	
A	B				



SCHEMATIC DIAGRAM

DRAWN [NT]	DATE 7-14-78	<p>Diablo Systems Incorporated Hayward, California 94545</p> <p>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</p>					
CHECKED [C]	DATE 7-17-78						
ENGR [Signature]	DATE 7-19-78						
MFG [Signature]	DATE 2-10-78						
FIRST USED (NEXT ASSY) 400202-01							
REV A	REV NO. 1	DATE 9-15-78	SHEET 1 OF 1	SHEET 1	TOT SMT 1	SIZE D	DWG NO 400493-01

OPERATION: WRITE SECTOR
 CONTROL REGISTER = 0010

ROM/BIT

STEP	DIR CONTENTS	DIR TO DOR	DMA READ	DMA WRITE	DOR CONTENTS	TO DISK	COUNTER	CONTENTS	READ ON	WRITE ON	CLR DIR/DOR	DIR TO DOR	DMA EN	DATA TO MEMORY	DEC WC EN	STEP IF WC Z	LD XCTR	DEC XCTR	STEP IF X	XCTR 32	XCTR 16	XCTR 8	XCTR 4	XCTR 2	BR0 (IF CLK ERR)	BR0 (COMP ERR)	BR6	WRITE AM CLK	H CYC ERR EN	DAM ERR EN	D CLK ERR EN	D CYC ERR EN	LD MA LO F	LD MA HI F	XFR MA	LOAD MA	CYC ON WRITE	CYC OUT ENABLE	ERASE ON	LOCK ON	FORCE ON	FFLG	WFLG	STEP CMD	END	COMMENTS		
0	00				00	X	4		X					X						X																									SET UP			
1	HAM	X			00			X	X	X												X																								SEARCH FOR MISSING CLOCK PATTERN		
2	TRACK	X	X		HAM	X	4	X	X	X				X	X																															HEADER SEARCH:		
	00	X	X		TRACK	X	3	X	X	X				X	X																														COMPARE DISK HEADER WITH IMAGE IN			
	SECTOR	X	X		00	X	2	X	X	X				X	X																													MEMORY; IF DIFFERENT, START OVER				
	00	X	X		SECTOR	X	1	X	X	X				X	X																													AT STEP 0				
	CRC1	X	X		00	X	0	X	X	X				X	X																																	
3	CRC2	X	X		CRC1			X		X																	X		X																CHECK HEADER CYC; GET DATA MA LO FROM MEM'RY			
4	00		X		00	X	19		X					X		X																													GET DATA MA HI FROM MEMORY; LOAD XCTR			
5						X	19							X	X																																	
						X	18							X	X																																	
						X	5							X	X																																	
						X	4							X	X																																	
6					00	00	X	3	X					X	X																																	
					FF	FF	X	2	X					X	X																																	
					FF	FF	X	1	X					X	X																																	
					FF	FF	X	0	X					X	X																																	
7					FF	FF			X	X																																						
8	DAM	X	X		FF	FF			X	X	X																																					
9	DATA 1	X	X		DAM	DAM	W	W-1	X	X	X			X	X																																	
10	DATA 2	X	X		DATA 1	DATA 1	W	W-2	X	X	X			X	X																																	
	DATA 3	X	X		DATA 2	DATA 2	W	W-3	X	X	X			X	X																																	
	DATA N	X	X		DATA N-1	DATA N-1	W	0	X	X	X			X	X																																	
	DATA N+1	X	X		DATA N	DATA N	W	-1	X	X	X			X	X																																	
11					DATA N+1	CRC1	X	3	X					X	X																																	
					FF	CRC2	X	2	X					X	X																																	
12					FF	FF	X	1	X					X	X																																	
13					FF		X	0						X	X																																	
14																																																
15																																																

① DMA EN OCCURS AT END OF BYTE TIME AND TRIGGERS DMA READ/WR THAT OCCURS DURING NEXT BYTE TIME.
 ② CONTENTS SHOW REGISTERS BEFORE DIR/DOR TRANSFER OCCURS AT END OF BYTE TIME.

Diablo Systems Incorporated
 PROGRAM CHART
 DISKETTE CONTROLLER
 DOUBLE DENSITY
 MODEL 3200

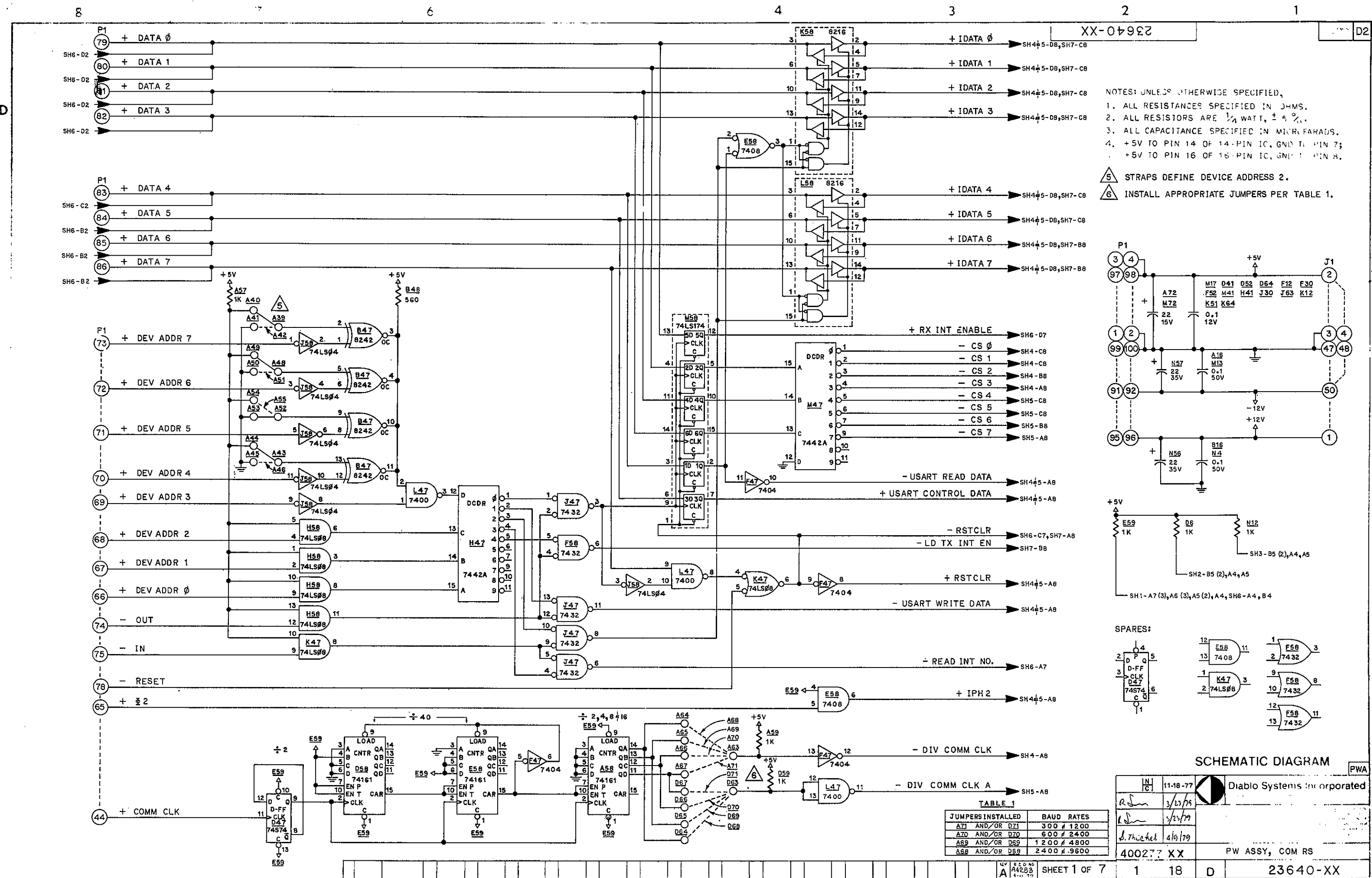
OPERATION: COMPARE SECTOR
 CONTROL REGISTER = 0101

ROM/BIT

STEP	DIR CONTENTS	DMA READ	DMA WRITE	DOR CONTENTS	TO DISK	COUNTER	READ ON	WRITE ON	CLR DIR/DOR	DIR TO DOR	DMA EN	DATA TO MEMORY	DEC WC EN	STEP IF WC Z	LD XCTR	DEC XCTR	STEP IF X	XCTR 32	XCTR 16	XCTR 8	XCTR 4	XCTR 2	BR0 (IF CLK ERR)	BR0 (COMP ERR)	BR6	WRITE AM CLK	DAM ERR EN	D CLK ERR EN	D CYC ERR EN	LD MA LO F	LD MA HI F	XFR MA	LOAD MA	CYC CYC	CYC ON WRITE	CYC OUT ENABLE	ERASE ON	LOCK ON	FORCE ON	FFLG	WFLG	STEP CMD	END	COMMENTS				
0	00			00	X	4		X																																					SET UP			
1	HAM	X		00			X	X	X													X																								SEARCH FOR MISSING CLOCK PATTERN		
2	TRACK	X	X	HAM		X	4	X	X	X																																				HEADER SEARCH:		
	00	X	X	TRACK		X	3	X	X	X																																			COMPARE DISK HEADER WITH IMAGE IN			
	SECTOR	X	X	00		X	2	X	X	X																																		MEMORY; IF DIFFERENT, START OVER				
	00	X	X	SECTOR		X	1	X	X	X																																		AT STEP 0				
3	CRC1	X		00		X	0	X	X	X																																				CHECK HEADER CRC; GET DATA MA LO FROM MEMORY		
4	CRC2	X		CRC1		X			X																																					GET DATA MA HI; LOAD X=20; SET UP CRC FOR DATA		
5	00			00		X	20		X			X	X					X	X																											WAIT 17 BYTE TIMES		
6	00			00		X	19																																									
7	DAM	X		00		W	W-1	X		X	X	X																																		WAIT FOR & TEST DAM CLOCK PATTERN		
8	DATA 1	X	X	DAM		W	W-2	X		X	X	X																																		COMPARE DAM WITH MEMORY; DATA READ BEGINS;		
9	DATA 2	X	X	DATA 1		W	W-3	X		X	X	X	X																																			
	DATA 3	X	X	DATA 2		W	W-4	X		X	X	X	X																																			
	DATA N-1	X	X	DATA N-2		W	0	X		X	X	X	X																																			
	DATA N	X	X	DATA N-1		W	-1	X		X	X	X	X																																			
10	CRC1			DATA N						X																																					COMPARE LAST DATA BYTE; READ FIRST CRC BYTE	
11	CRC2									X																																				CHECK CRC		
12																																															WAIT ONE BYTE TIME	
13																																															END; NOT BUSY; INTERRUPT	
14																																																
15																																																

1 DMA EN OCCURS AT END OF BYTE TIME AND TRIGGERS DMA READ/WR THAT OCCURS DURING NEXT BYTE TIME.
 2 CONTENTS SHOW REGISTERS BEFORE DIR/DOR TRANSFER OCCURS AT END OF BYTE TIME.

Diablo Systems Incorporated
 PROGRAM CHART
 DISKETTE CONTROLLER
 DOUBLE DENSITY
 MODEL 3200



- NOTES: UNLESS OTHERWISE SPECIFIED,
 1. ALL RESISTANCES SPECIFIED IN OHMS.
 2. ALL RESISTORS ARE 1/4 WATT, ± 5%.
 3. ALL CAPACITANCE SPECIFIED IN MICROFARADS.
 4. +5V TO PIN 14 OF 14-PIN IC, GND TO PIN 7;
 +5V TO PIN 16 OF 16-PIN IC, GND TO PIN 8.

- 5 STRAPS DEFINE DEVICE ADDRESS 2.
 6 INSTALL APPROPRIATE JUMPERS PER TABLE 1.

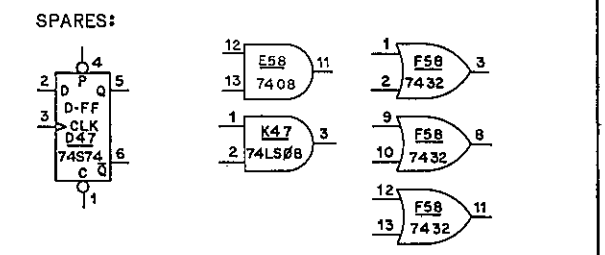
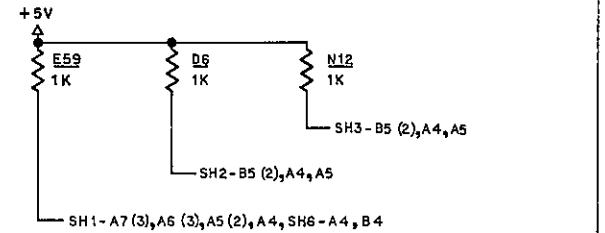
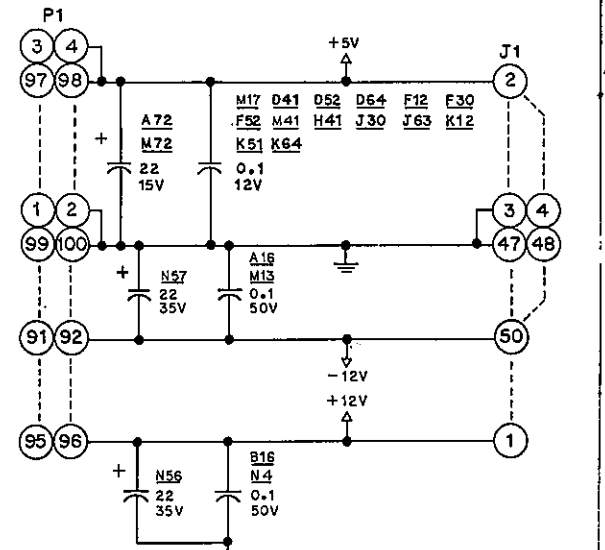
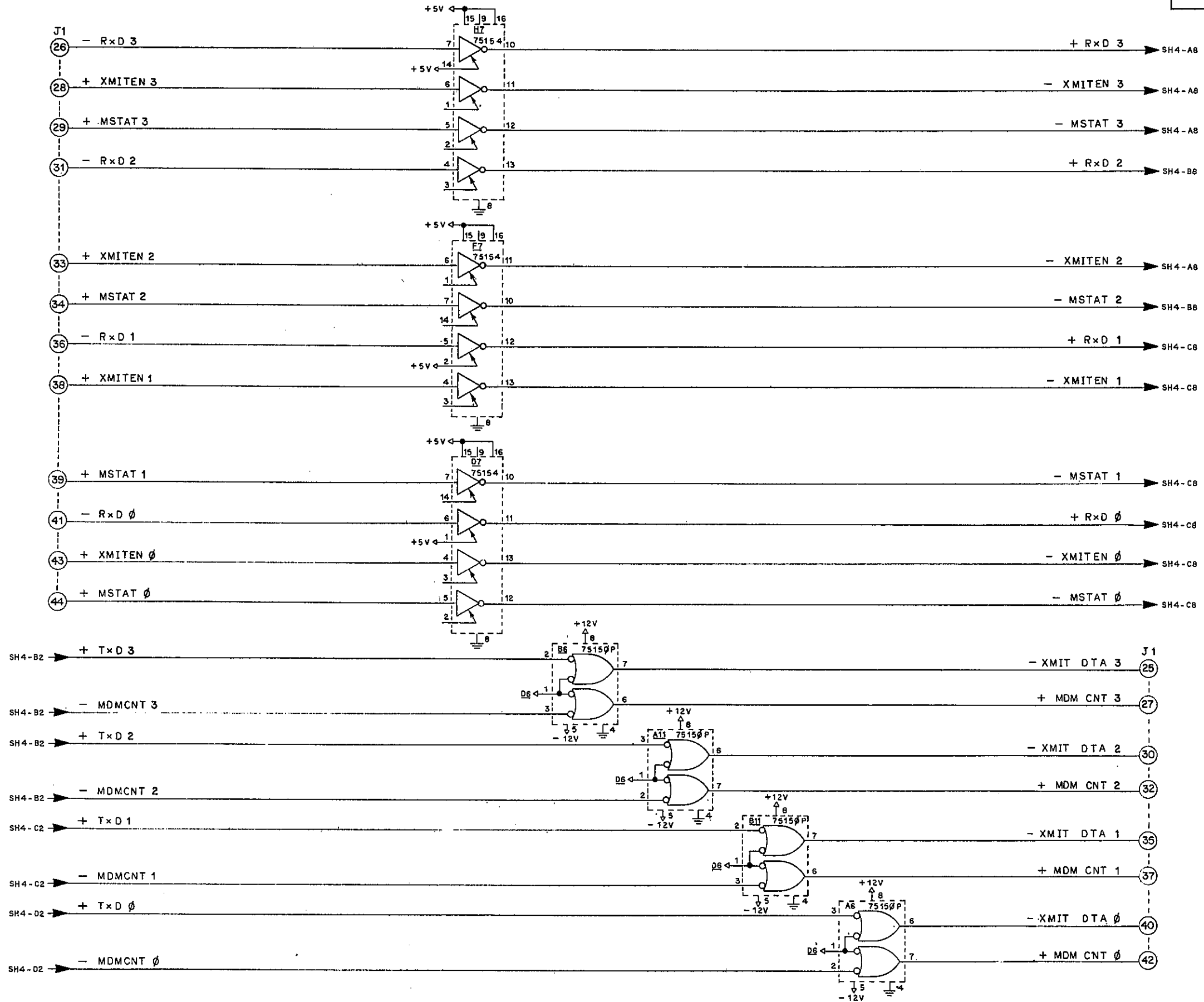


TABLE 1
 JUMPERS INSTALLED BAUD RATES

A71 AND/OR D71	300 # 1200
A70 AND/OR D70	600 # 2400
A69 AND/OR D69	1200 # 4800
A68 AND/OR D68	2400 # 9600

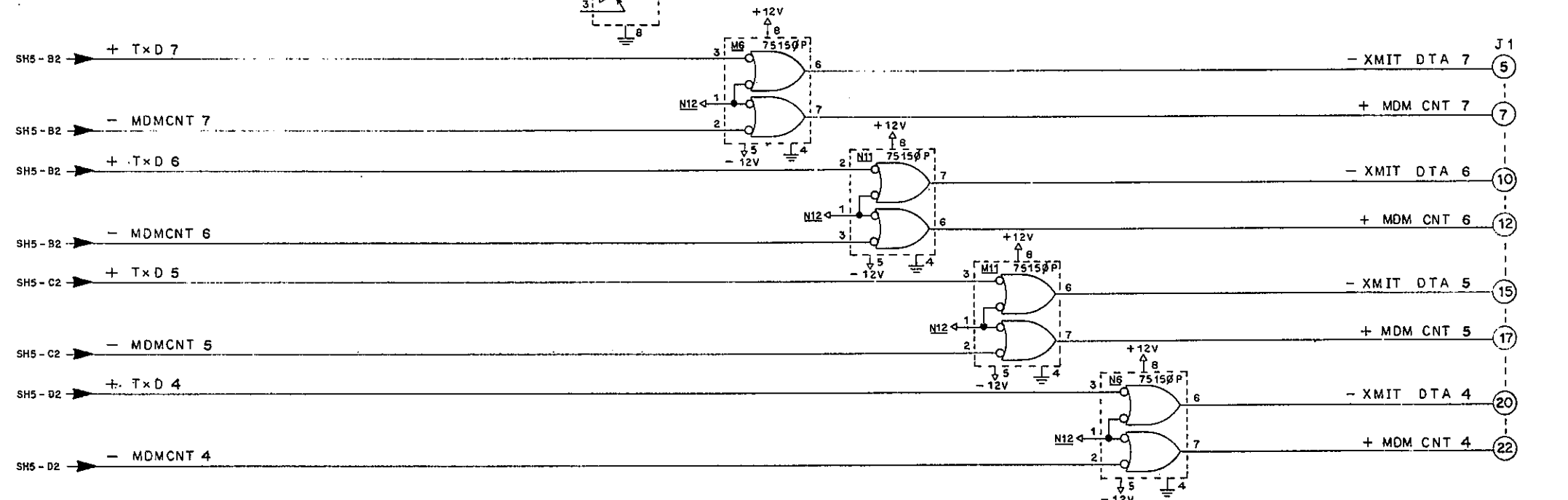
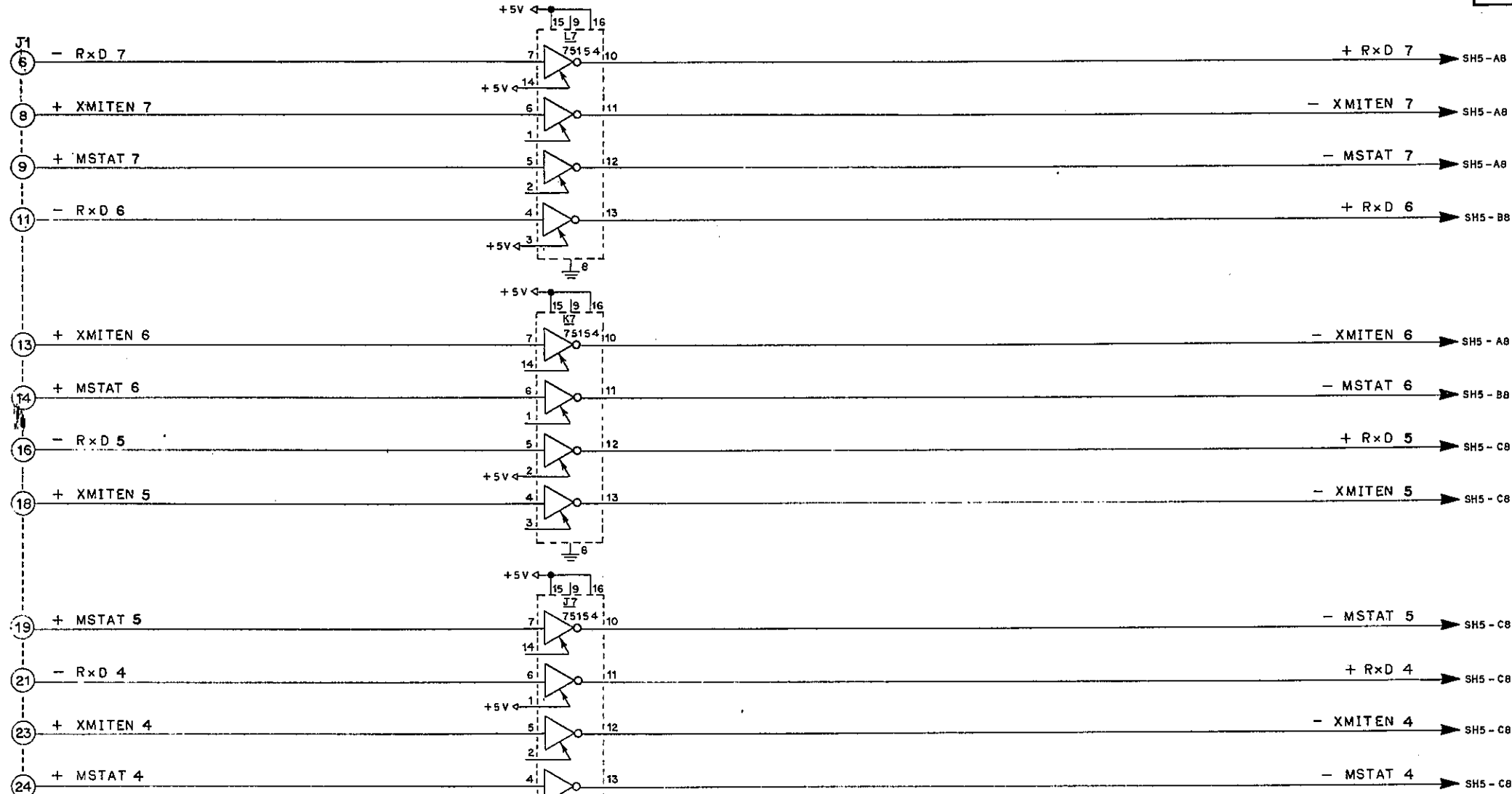
SCHMATIC DIAGRAM

Diablo Systems Incorporated
 400277 XX PW ASSY, COM RS
 SHEET 1 OF 7 1 18 D 23640-XX



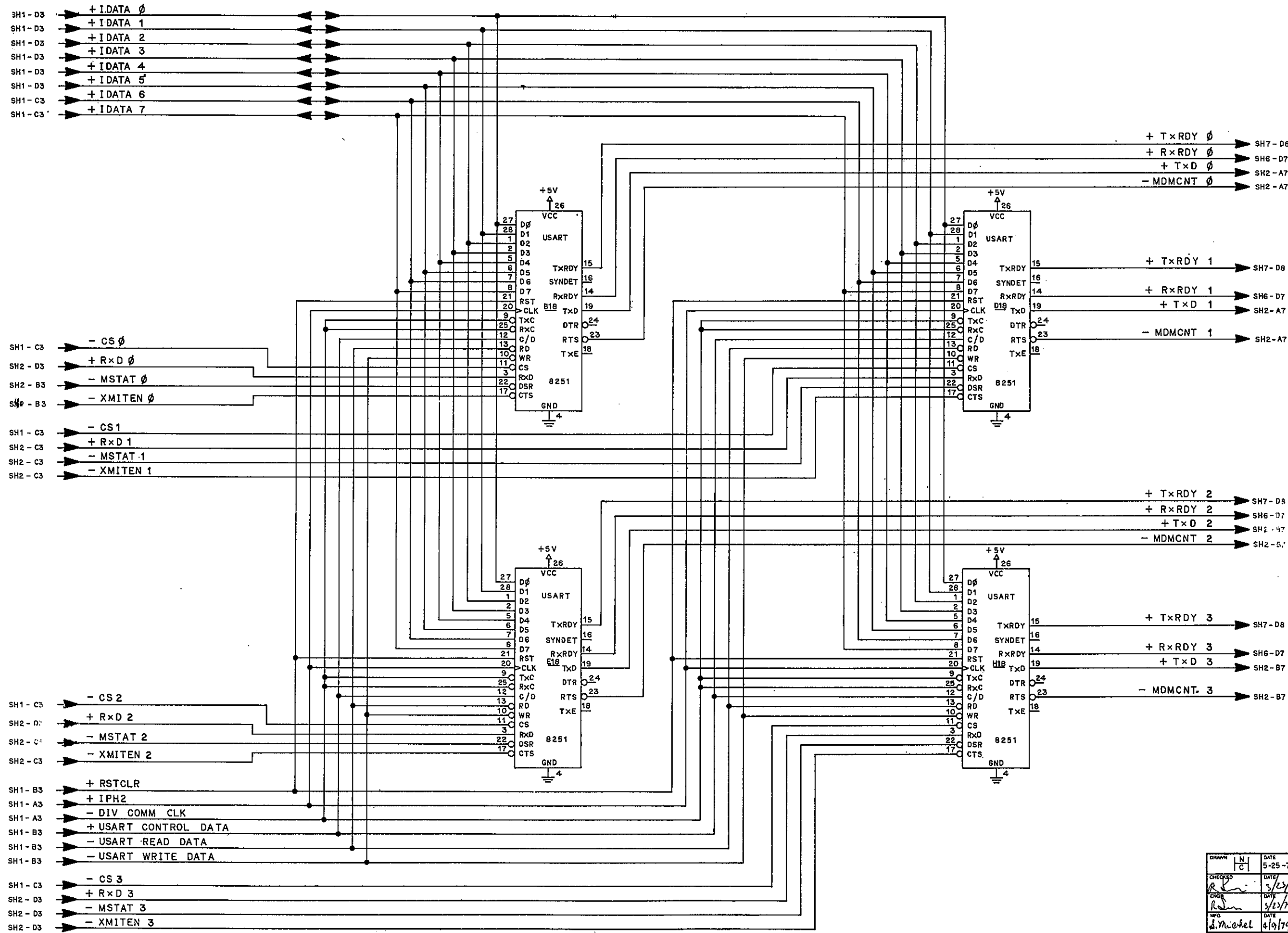
SCHEMATIC DIAGRAM

DRAWN HCT	DATE 11-18-77	Diablo Systems Incorporated Hayward, California 94545
CHECKED RST	DATE 3/23/79	
ENG J. Michel	DATE 4/9/79	THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.
FIRST USER NEXT ASSY: 4610	REV -XX	PW ASSY, COM RS
SHEET 2	TOT SHT 18	SIZE D
REV C.C.O. NO. A4283		DWG NO 23640-XX



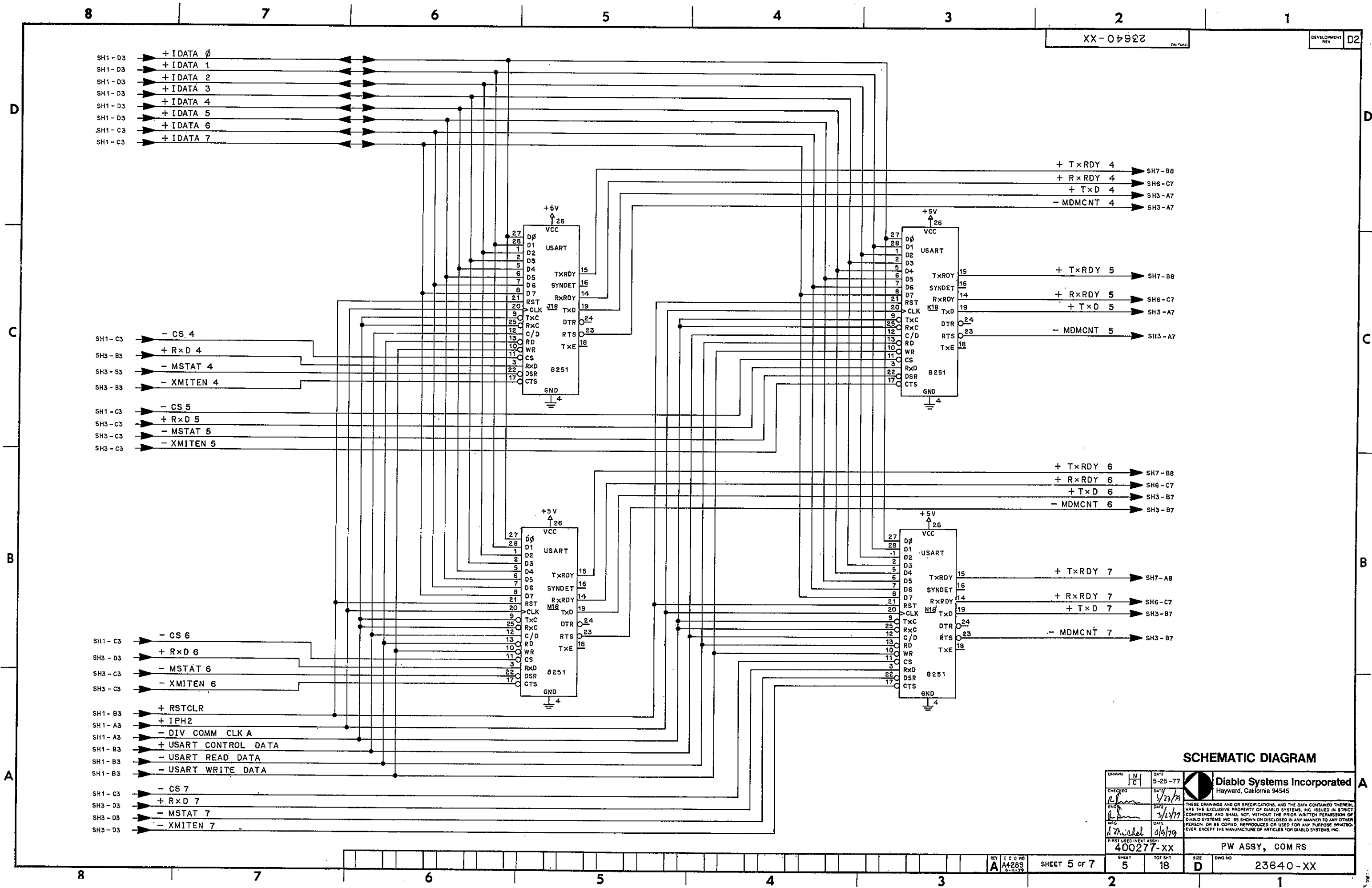
SCHEMATIC DIAGRAM

DRAWN	IN	DATE	11-18-77	Diablo Systems Incorporated Hayward, California 94545
CHECKED	IC	DATE	3/23/79	
ENGR		DATE	5/23/79	<small>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</small>
WFO		DATE	4/9/79	
FIRST USED IN EXT ASSY				PW ASSY, COM RS
450277-XX				23640-XX
REV	E.C.O. NO.	SHEET	TOT BHT	SIZE
A	A4283	3	18	D



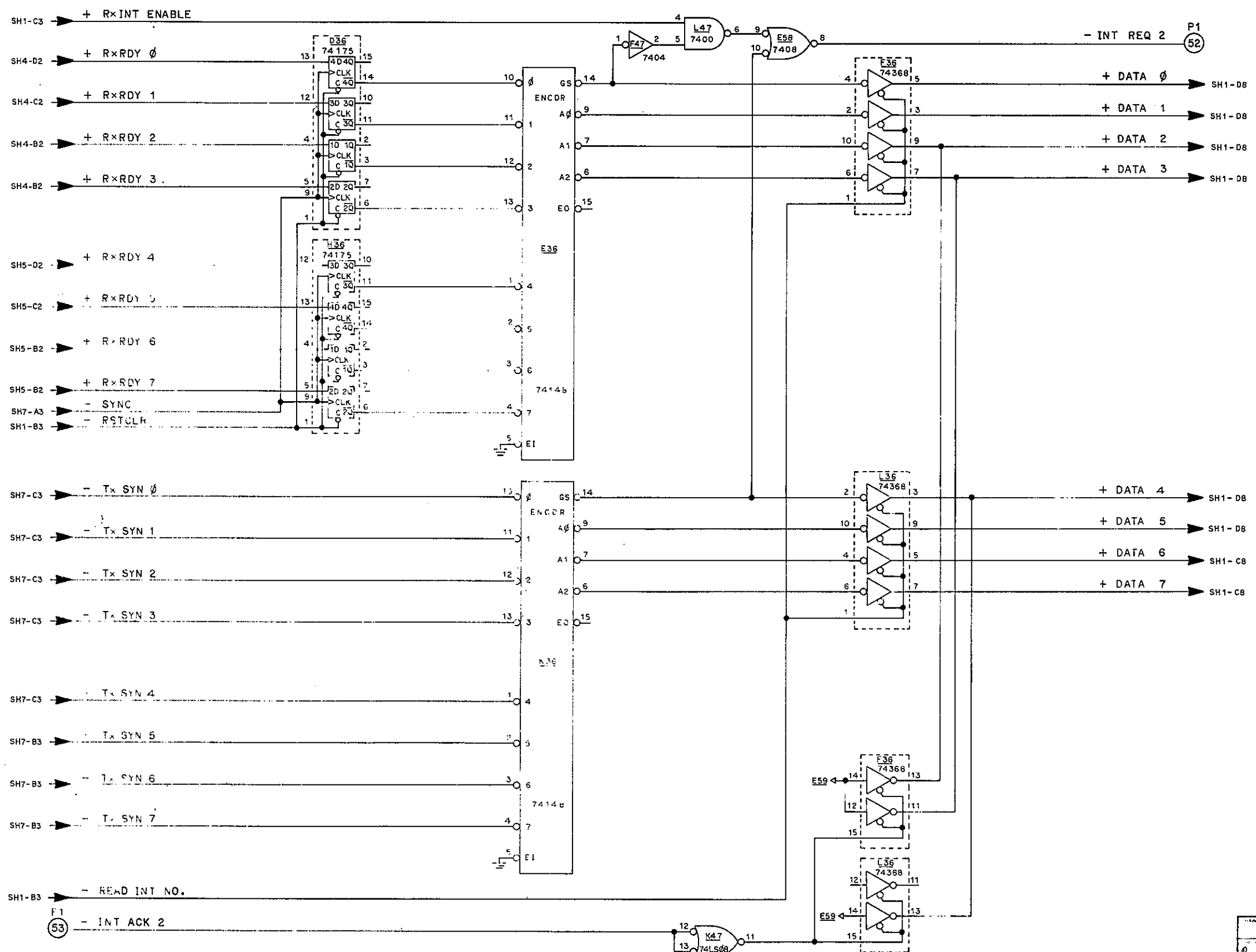
SCHEMATIC DIAGRAM

DRAWN N C	DATE 5-25-77	<p>Diablo Systems Incorporated Hayward, California 94545</p> <p>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. SHOULD BE KEPT CONFIDENTIAL AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC., BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON, OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSES WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</p>
CHECKED R	DATE 3/23/79	
ENG R	DATE 5/23/79	
DATE 4/9/79		
FIRST USED (SHEET ASSY) 400271-XX		PW ASSY, COM RS
REV A	E.C.D. NO. A4288 A-11-74	SHEET 4 OF 7
SHEET 4	TOT SHT 18	SIZE D
		DWG NO 23640-XX



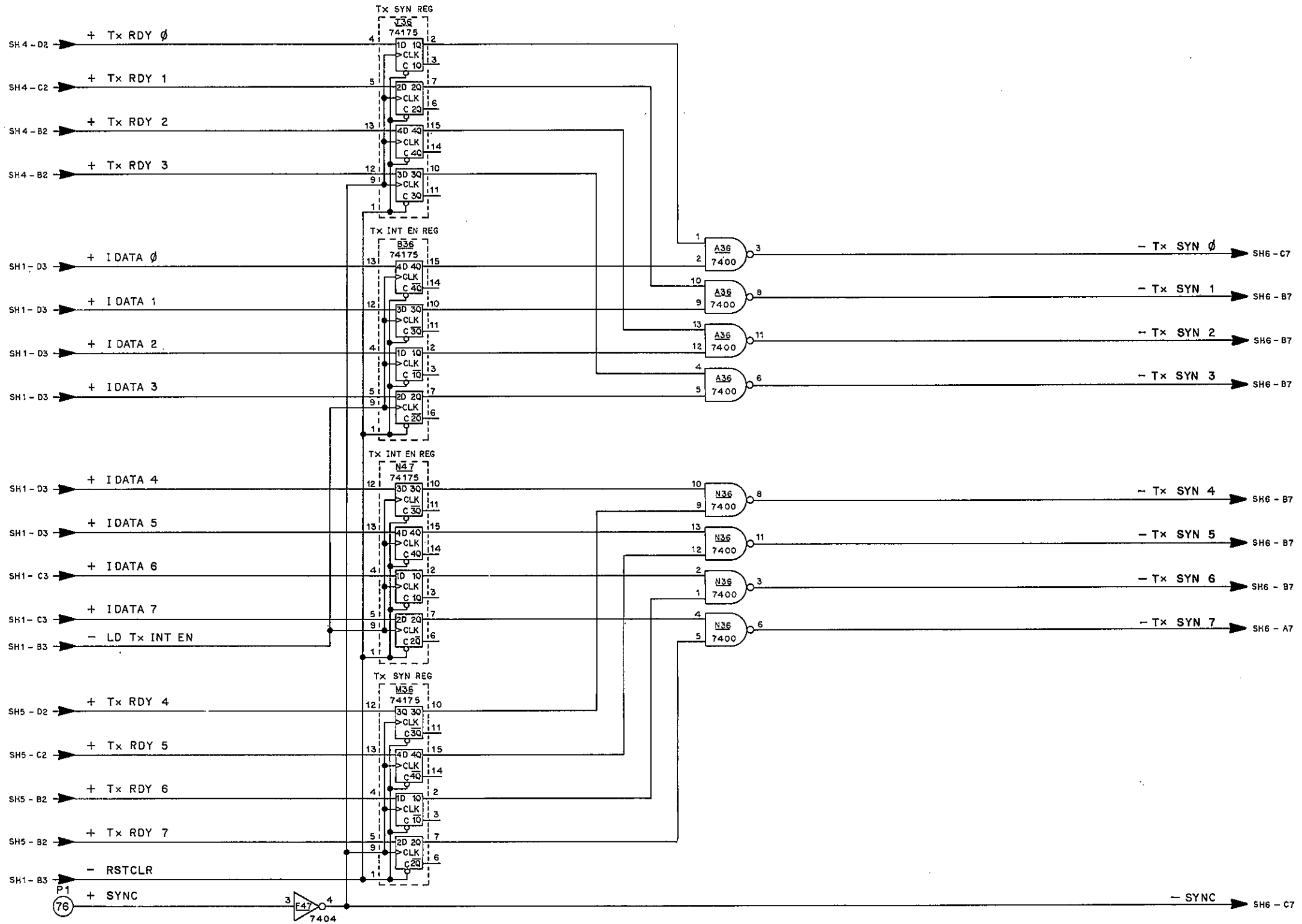
SCHEMATIC DIAGRAM

DRAWN N/C	DATE 5-25-77	<p>Diablo Systems Incorporated Hayward, California 94545</p> <p>THESE DRAWINGS AND/OR SPECIFICATIONS, AND THE DATA CONTAINED THEREIN, ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT, WITHOUT THE PRIOR WRITTEN PERMISSION OF DIABLO SYSTEMS, INC. BE SHOWN OR DISCLOSED IN ANY MANNER TO ANY OTHER PERSON OR BE COPIED, REPRODUCED OR USED FOR ANY PURPOSE WHATSOEVER, EXCEPT THE MANUFACTURE OF ARTICLES FOR DIABLO SYSTEMS, INC.</p>
CHECKED <i>[Signature]</i>	DATE 3/23/78	
ENG. <i>[Signature]</i>	DATE 3/23/77	
INFO <i>[Signature]</i>	DATE 4/6/79	
FIRST USED IN EXT ASSY 400277-XX		
SHEET 5 OF 7		SIZE D
REV I C D NO A 4283 4-11-79		DWG NO 23640-XX



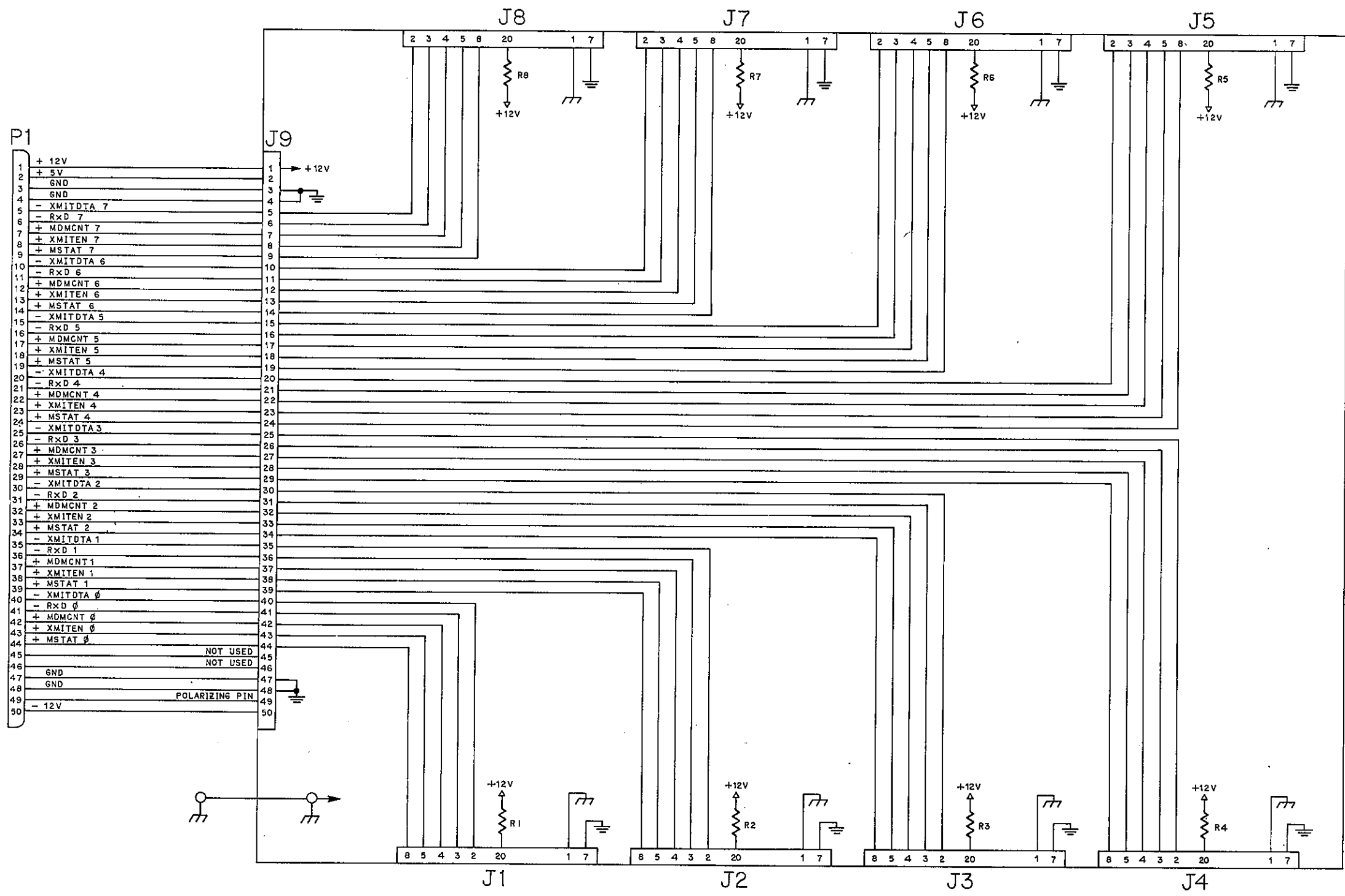
SCHEMATIC DIAGRAM

REV	IN	DATE	DIABLO SYSTEMS INCORPORATED Hayward, California 94545
	CH	5-27-77	
		3/23/77	
		3/23/77	
		4/9/79	
400277-XX			PW ASSY, COM RS
6	18	D	23640-XX

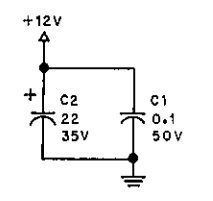


SCHEMATIC DIAGRAM

REV		DATE		BY		CHECKED		APPROVED	
A		8-23-77		[Signature]		[Signature]		[Signature]	
400277-XX		PW ASSY COM RS		7		18		D	
SHEET 7 OF 7		23640-XX		7		18		D	

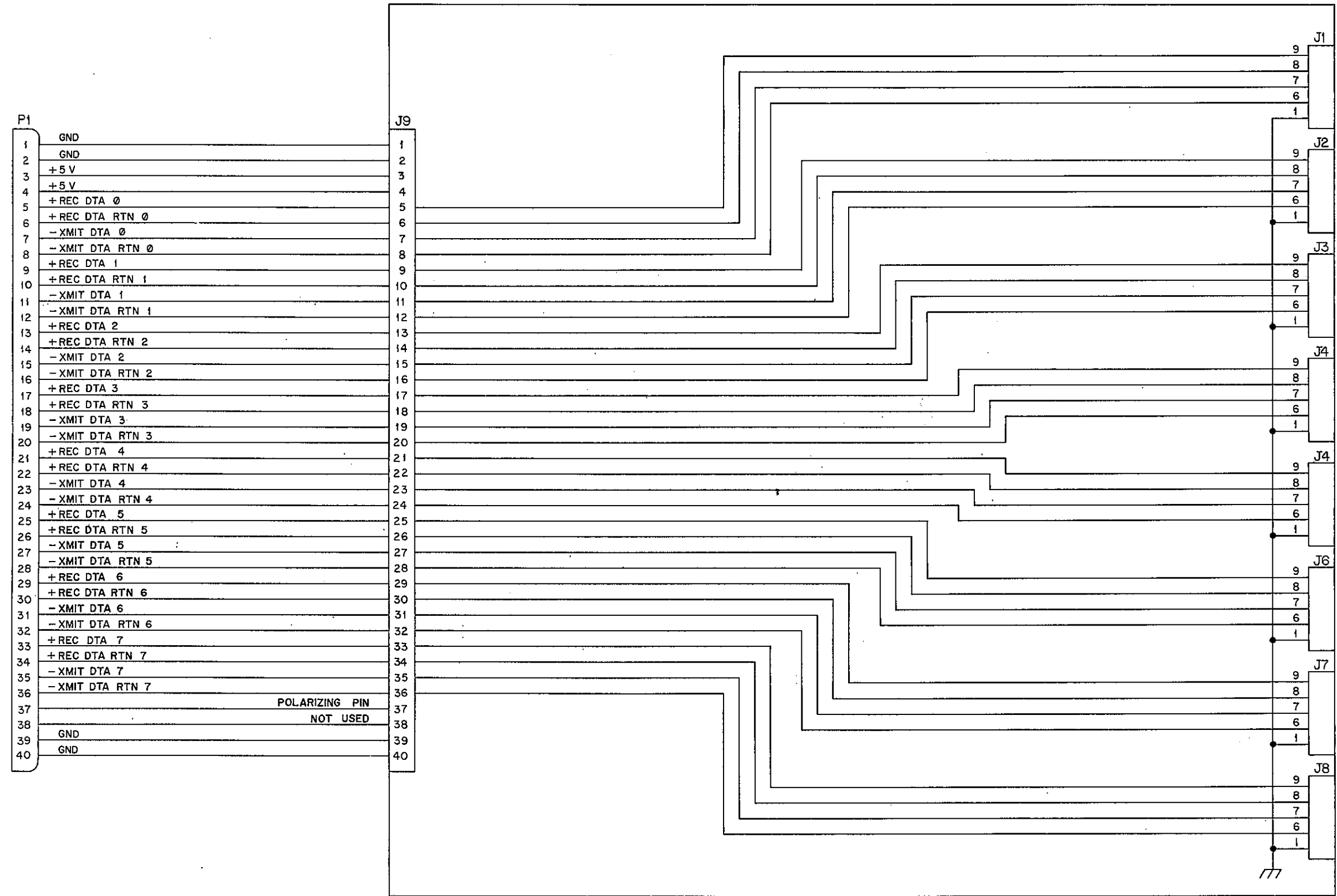


- NOTES:
1. ALL RESISTORS ARE 3.3K OHMS 1/4 WATT, ±5%.
 2. ALL CAPACITANCE SPECIFIED IN MICROFARADS.



SCHMATIC DIAGRAM PWA

DATE	1-10-78	 Diablo Systems Incorporated Hayward, California 94545								
REV	3/23/79									
DATE	3/23/79	<small>THIS DRAWING AND ITS SPECIFICATIONS AND THE DATA CONTAINED THEREIN ARE THE EXCLUSIVE PROPERTY OF DIABLO SYSTEMS, INC. ISSUED IN STRICT CONFIDENCE AND SHALL NOT BE REPRODUCED OR TRANSMITTED IN ANY MANNER TO ANY OTHER PERSON OR BE COPIED, REPRODUCED OR USED IN ANY MANNER WHATSOEVER WITHOUT THE WRITTEN PERMISSION OF DIABLO SYSTEMS, INC.</small>								
DATE	4/10/79									
REV	400277-XX	PW ASSY, COMMD								
REV	A1 A4298	REV	A4283	REV	4-11-79	SHEET 1 OF 1	1	3	D	400130-01



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J	K	DATE	8-8-77
G	H	DRAWN	R. LUBRIN
E	F	CHECKED	D. Manning 12/5/77
C	D	ENGR	J. Manning 12/5/77
A	B	DATE	1-15-78
REVISIONS		BY	S. FRATER 1/4/78
FIRST USED		26250-XX	

DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED TOLERANCES ARE

LINEAR .005 ANGULAR .005

MATERIAL

HEAT TREAT

FINISH

SCHEMATIC DIAGRAM PWA

Diablo Systems Incorporated
 PW ASSY, COMPL

NONE 1 3

400134-01